

Features

- High Reliability Capability
- Extremely low on-resistance $R_{DS(on)}$
- Excellent gate charge x $R_{DS(on)}$ product(FOM)

Product Summary			
V_{DS}	$R_{DS(on)}$ (m Ω) Typ	I_D (A)	Q_g (Typ)
40V	15.6 @ 10V 9A	20	20nc

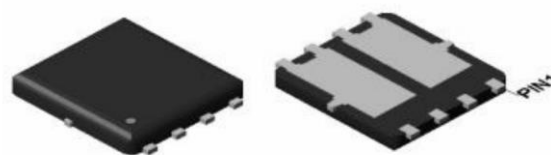
Mechanical Data

- Case:DFN3×3 Package

DFN3×3
D16N04G

Application

- Charger
- DC/DC converter
- Ideal for high-frequency switching and synchronous rectification



Top View

Bottom View

Ordering Information

Part No.	Package Type	Package	Quantity(box)
D16N04Q	DFN3×3	Tape & Reel	5000

Block Diagram

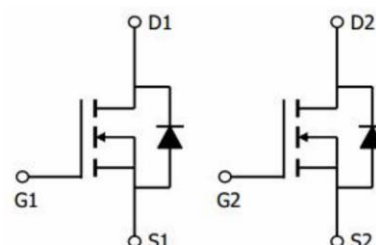


Table1 Absolute Maximum Ratings ($T_c=25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	20	A
		13	
Pulsed Drain Current (Note 1)	I_{DM}	80	A
Single Pulse Avalanche Energy(Note 2)	E_{AS}	46	mJ
Power Dissipation $T_c=25^\circ\text{C}$	P_D	12	W
Operating Junction and Storage Temperature	T_J/T_{STG}	$-55\sim +150$	$^\circ\text{C}$

Table 2. Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance Junction to Ambient	$R_{\theta JA}$	62	$^{\circ}\text{C}/\text{W}$
Thermal resistance Junction to Case	$R_{\theta JC}$	10	$^{\circ}\text{C}/\text{W}$

Table 3. Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise specified)

Parameter		Symbol	Test Conditions	Min	Typ	Max	Unit
Off Characteristics							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V,I _D =250μA	40	-	-	V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =40V,V _{GS} =0V	-	-	1	μA
Gate- Source Leakage Current	Forward	I _{GSS}	V _{GS} =20V,V _{DS} =0V	-	-	100	nA
	Reverse		V _{GS} =-20V,V _{DS} =0V	-	-	-100	nA
On Characteristics(Note 3)							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} ,I _D =250μA	1.0	1.5	2.5	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V,I _D =9A	-	15.6	21	mΩ
Dynamic Characteristics(Note 4)							
Input Capacitance		C _{ISS}	V _{DS} =20V,V _{GS} =0V,f=1MHz	-	716	-	pF
Output Capacitance		C _{OSS}		-	78	-	pF
Reverse Transfer Capacitance		C _{RSS}		-	63	-	pF
Switching Characteristics (Note 4)							
Turn-On Delay Time		t _{d(on)}	V _{DS} =20V,I _D =5A V _{GS} =10V,R _L =3Ω,	-	11	-	ns
Turn-On Rise Time		t _r		-	9	-	ns
Turn-Off Delay Time		t _{d(off)}		-	33	-	ns
Turn-Off Fall Time		t _f		-	17	-	ns
Total Gate Charge		Q _G	V _{DS} =20V,I _D =5A, V _{GS} =10V	-	20	-	nC
Gate-Source Charge		Q _{GS}		-	4	-	nC
Gate-Drain Charge		Q _{GD}		-	4	-	nC
Drain-Source Diode Characteristics and Maximum Ratings							
Drain-Source Diode Forward Voltage		V _{SD}	V _{GS} =0V, I _S =10A	-	-	1.2	V
Maximum Continuous Drain-Source Diode Forward Current		I _S		-	-	20	A
Reverse Recovery Time		t _{rr}	V _{GS} =0V, I _F =10A dI _F /dt=100A/μs	-	23	-	ns
Reverse Recovery Charge		Q _{RR}		-	11	-	nC

Notes : 1 Repetitive Rating:Pulse width limited by maximum junction temperature

2 $L=0.5\text{mH}$, $R_G=25\Omega$, Starting $T_J=25^{\circ}\text{C}$

3 Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$

4 Guaranteed by design, not subject to production

Typical Characteristics Diagrams

Figure 1. Output Characteristics

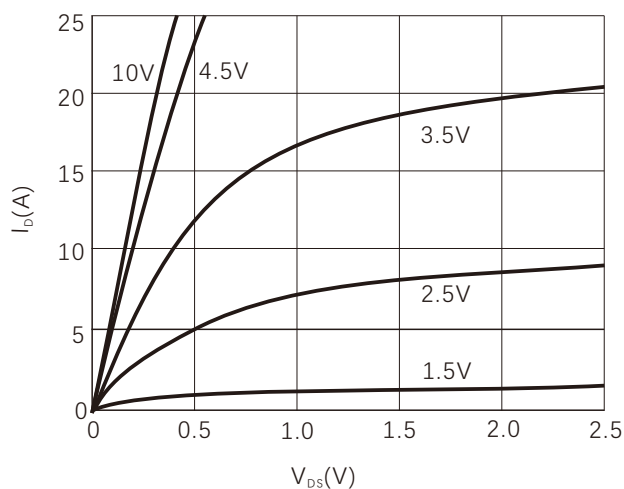


Figure 2. Transfer Characteristics

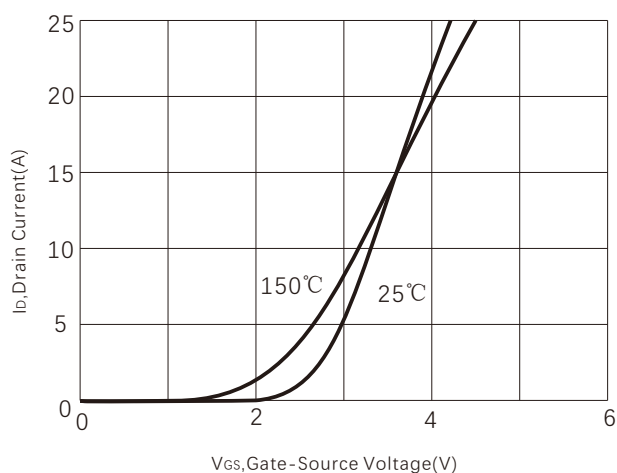


Figure 3. Normalized $R_{DS(on)}$ vs Temperature

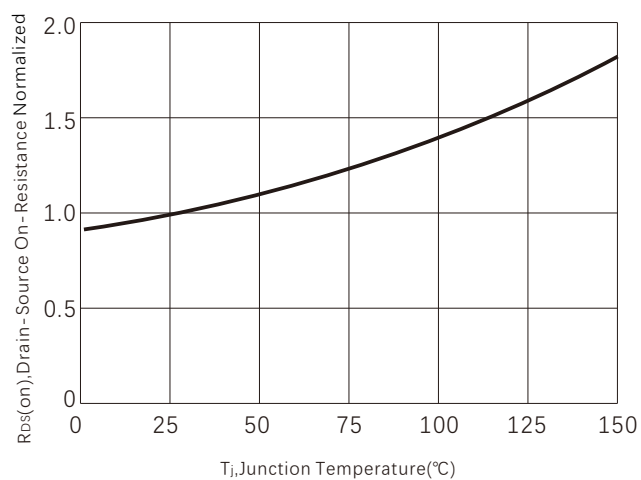


Figure 4. Capacitance

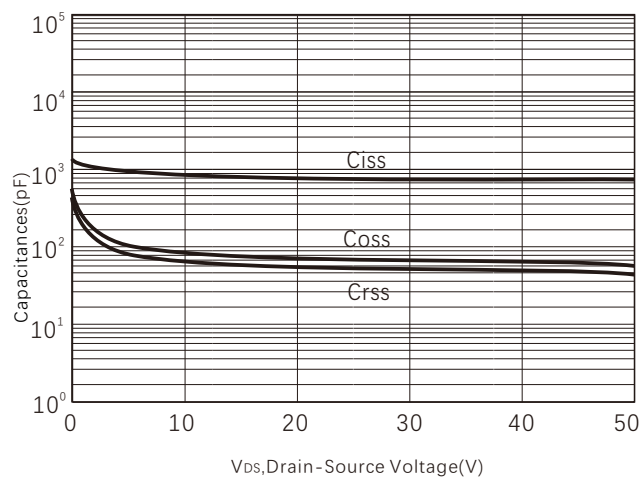


Figure 5. Gate charge

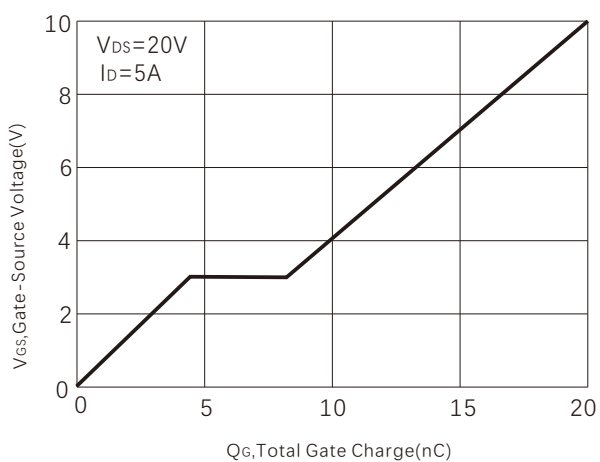


Figure 6. Source-Drain Diode Forward Voltage

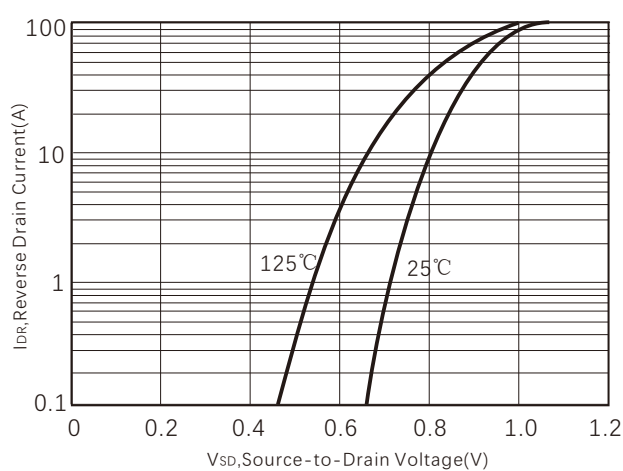


Figure7.Maximum Drain Current vs Temperature

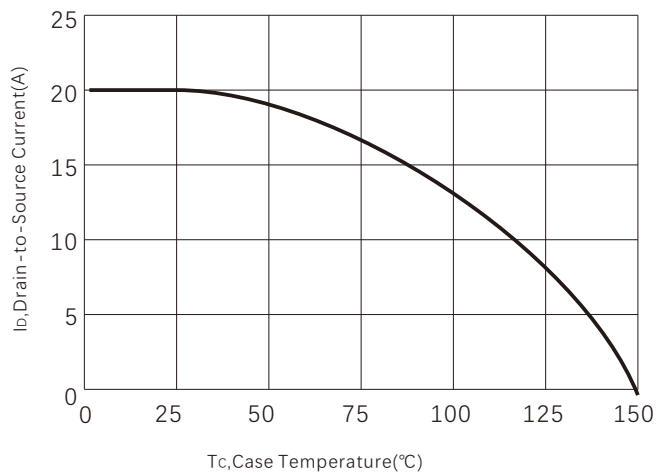
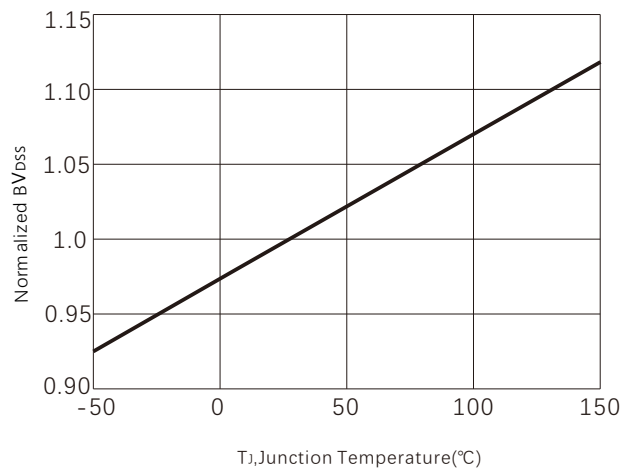
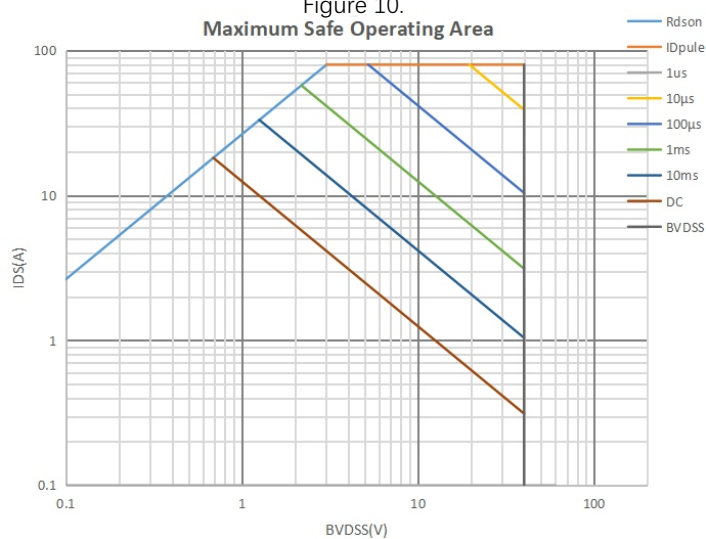
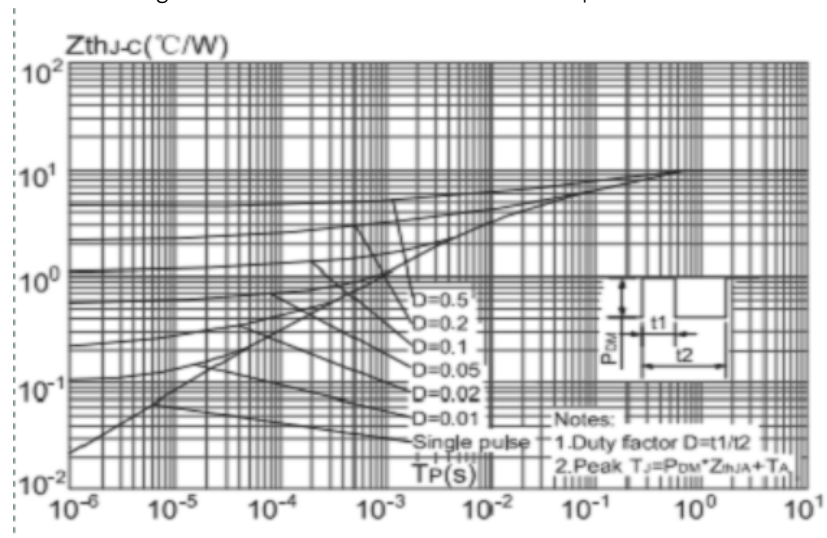
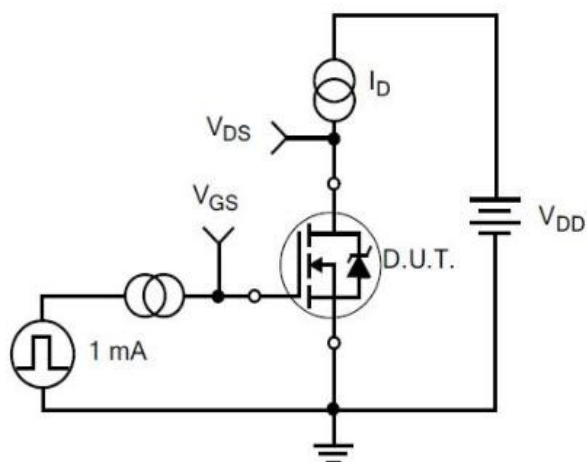

Figure 8. BV_{DSS} vs Junction Temperature

Figure 10.
Maximum Safe Operating Area


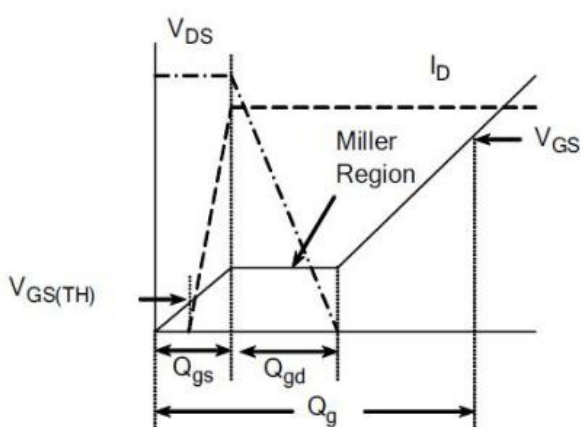
Figure 11. Maximum Transient Thermal Impedance



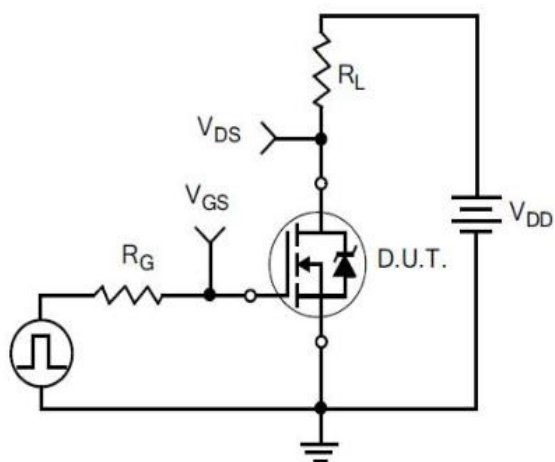
Typical Test Circuit



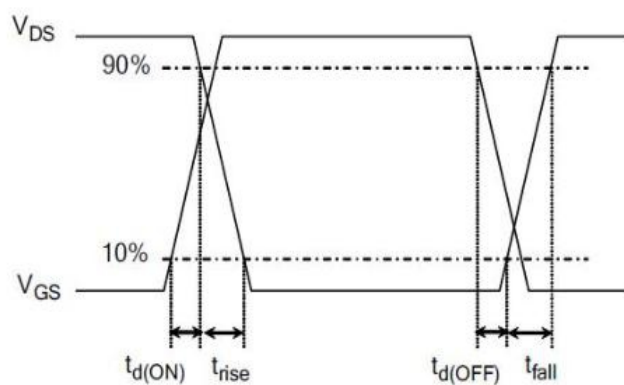
1) Gate Charge Test Circuit



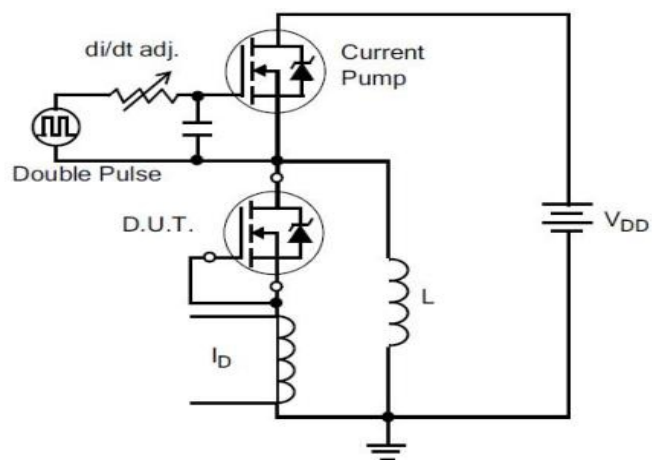
2) Gate Charge Waveform



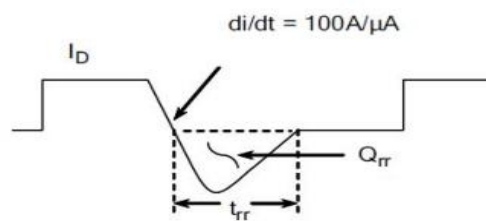
3) Resistive Switching Test Circuit



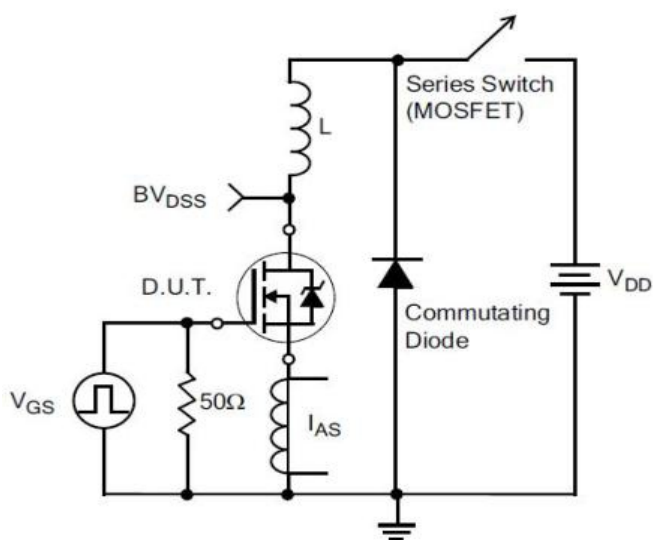
4) Resistive Switching Waveforms



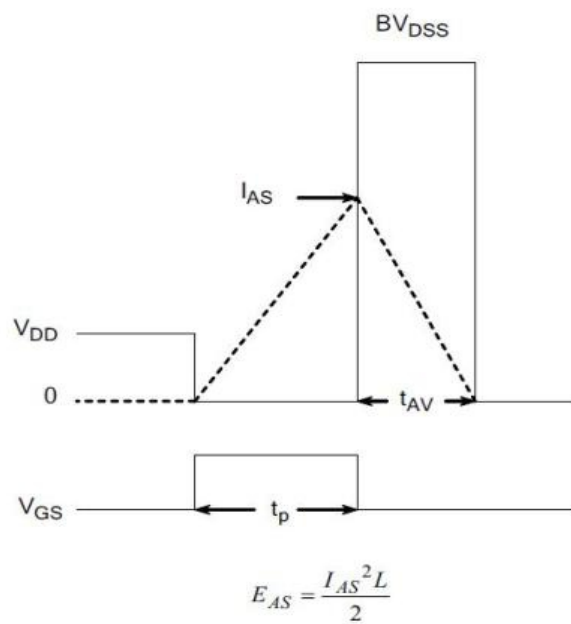
5) Diode Reverse Recovery Test Circuit



6) Diode Reverse Recovery Waveform



7) . Unclamped Inductive Switching Test Circuit



8) Unclamped Inductive Switching Waveforms

Product Names Rules

X X X X N E X X X

Process Type:
VDMOS:default
Super junction:SJ
Low Voltage trench:D

Rated Current Code
With 3 Digital,
For Example:
6.7mΩ:067,
10mΩ:10,

Channel Code
N channel:N
P channel:P

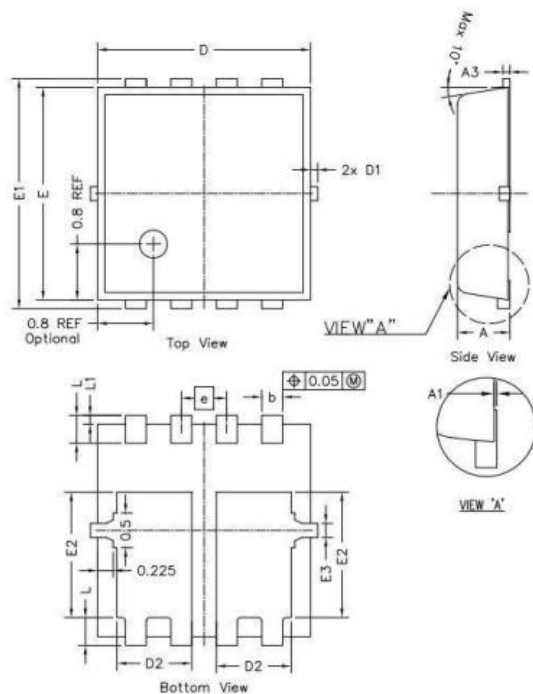
Package Code
TO-220:Default
ITO-220:F
TO-262:E
TO-263:D
TO-252:M
TO-251:N
TO-263-7L:D7
TOLL:T
DFN5×6:G

Rated Voltage Code
With 2 Digital,For Example:
600V:60
60V:06

Special Function Code
G-S ESD Protection:E
No Protection:Default

Dimensions

DFN3×3 PACKAGE OUTLINE DIMENSIONS



SYMBOLS	DIMENSION IN MM			DIMENSION IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.700	0.750	0.800	0.028	0.030	0.031
A1	---	---	0.050	----	----	0.002
A3	0.144	0.152	0.202	0.006	0.006	0.008
b	0.250	0.300	0.350	0.010	0.012	0.014
e	0.65 BSC			0.026 BSC		
D	2.950	3.050	3.150	0.116	0.120	0.124
E	2.950	3.050	3.150	0.116	0.120	0.124
D1	---	---	0.125	----	----	0.005
E1	3.200	3.300	3.400	0.126	0.130	0.134
D2	0.970	1.070	1.170	0.038	0.042	0.046
E2	1.700	1.800	1.900	0.067	0.071	0.075
E3	0.150	0.200	0.250	0.006	0.008	0.010
L	0.300	0.400	0.500	0.012	0.016	0.020
L1	0.075	0.125	0.175	0.003	0.005	0.007

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