Features

- Advanced Trench MOS Technology
- 100% EAS Guaranteed
- Green Device Available

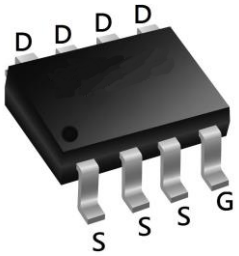
Product Summary

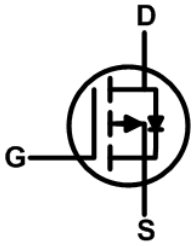
BVDSS	RDSON	ID
-150V	780mΩ	-1.1A

Applications

- Load Switch.
- Power Management.
- LED Backlighting.
- Networking application.

SOP-8 Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-150	V
V_{GS}	Gate-Source Voltage	±20	V
I_D@T_A=25˚C	Continuous Drain Current, -V_{GS} @ -10V[1]	-1.1	A
I_D@T_A=70˚C	Continuous Drain Current, -V_{GS} @ -10V[1]	-0.88	A
I_{DM}	Pulsed Drain Current[2]	-4.4	A
EAS	Single Pulse Avalanche Energy[3]	12.5	mJ
I_{AS}	Avalanche Current	5	A
P_D@T_A=25˚C	Total Power Dissipation[4]	2	W
T_{STG}	Storage Temperature Range	-55 to 150	˚C
T_J	Operating Junction Temperature Range	-55 to 150	˚C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1]	---	62	˚C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case[1]	---	40	˚C/W

Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=-250uA	-150	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=-10V , I_D=-1A	---	650	780	mΩ
		V_{GS}=-6V , I_D=-0.5A	---	700	980	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =-250uA	-2.0	-3.0	-4.0	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=-120V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=-120V , V_{GS}=0V , T_J=85℃	---	---	30	
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
R_g	Gate Resistance	V_{DS}=0V , V_{GS}=0V , f=1MHz	---	12	---	Ω
Q_g	Total Gate Charge	V_{DS}=-75V , V_{GS}=-10V , I_D=-1A	---	10.8	---	nC
Q_{gs}	Gate-Source Charge		---	3.1	---	
Q_{gd}	Gate-Drain Charge		---	2.2	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=-30V , V_{GS}=-10V , R_G=6Ω, I_D=-1A	---	21	---	ns
T_r	Rise Time		---	16	---	
$T_{d(off)}$	Turn-Off Delay Time		---	40	---	
T_f	Fall Time		---	18	---	
C_{iss}	Input Capacitance	V_{DS}=-75V , V_{GS}=0V , f=1MHz	---	706	---	pF
C_{oss}	Output Capacitance		---	23	---	
C_{rss}	Reverse Transfer Capacitance		---	13	---	
Diode Characteristics						
I_S	Continuous Source Current[1,5]	V_G=V_D=0V , Force Current	---	---	-1	A
V_{SD}	Diode Forward Voltage[2]	V_{GS}=0V , I_S=-1A , T_J=25℃	---	---	-1.2	V

Note :
1.The data tested by surface mounted on a 1 inch^2 FR-4 board with 2OZ copper.
2.The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%
3.The EAS data shows Max. rating . The test condition is V_{DD}=-50V,V_{GS}=-10V,L=1mH,I_{AS}=-5A
4.The power dissipation is limited by 150℃ junction temperature
5.The data is theoretically the same as I_D and I_S , in real applications , should be limited by total power dissipation.

Typical Characteristics

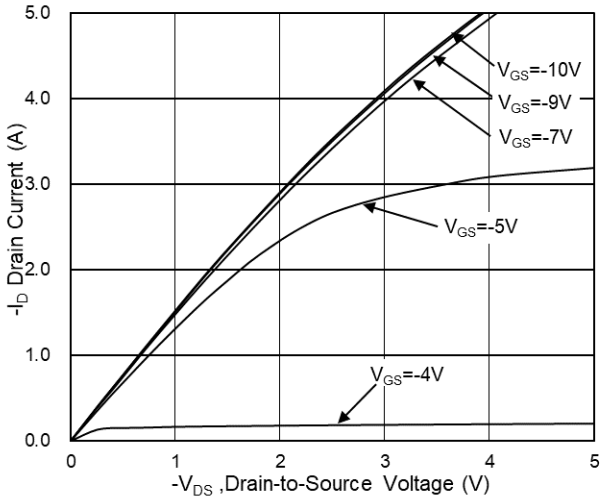


Fig.1 Typical Output Characteristics

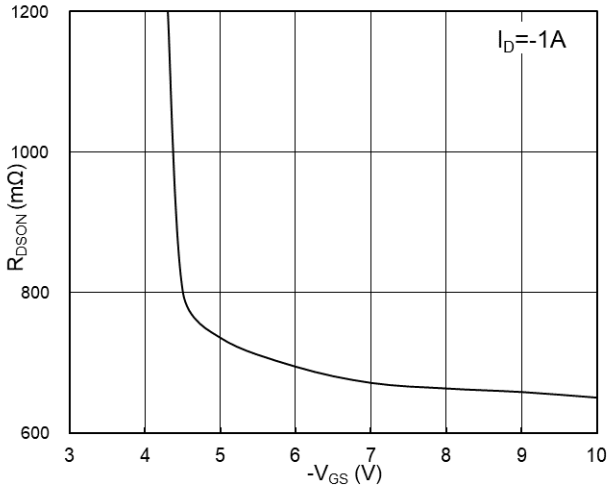


Fig.2 On-Resistance vs G-S Voltage

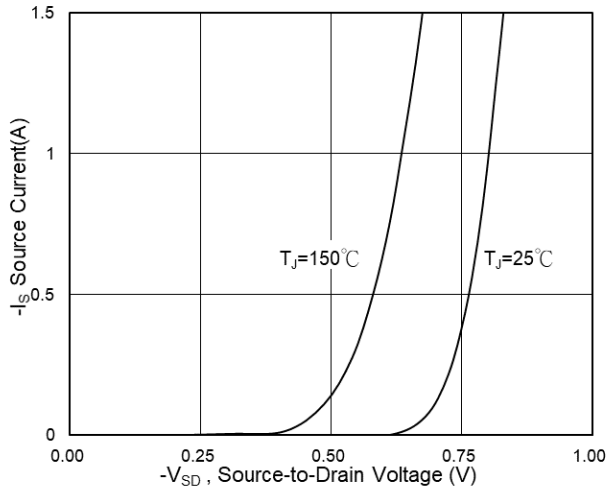


Fig.3 Source Drain Forward Characteristics

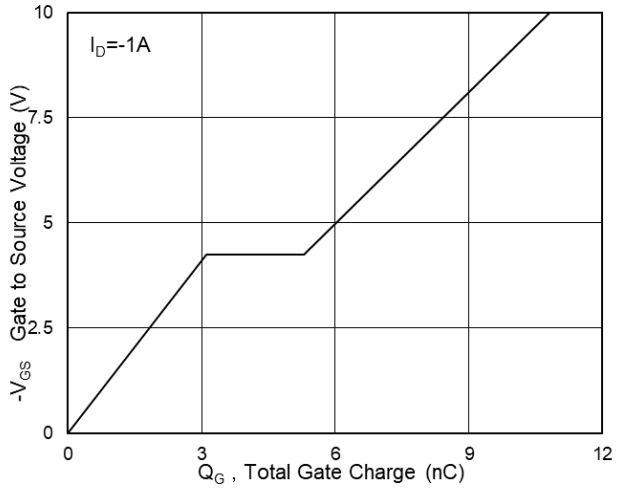


Fig.4 Gate-Charge Characteristics

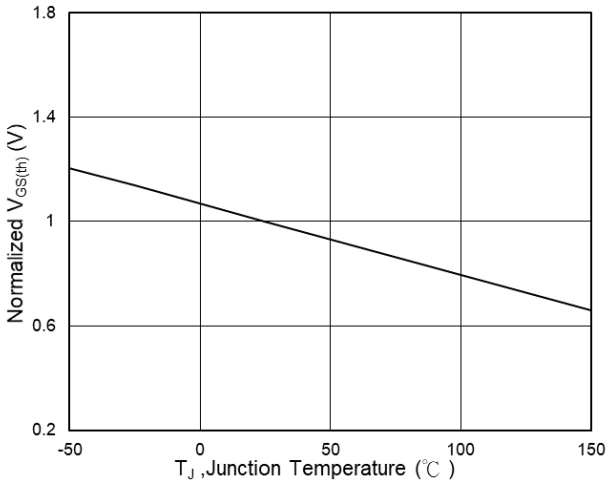


Fig.5 Normalized $V_{GS(th)}$ vs T_J

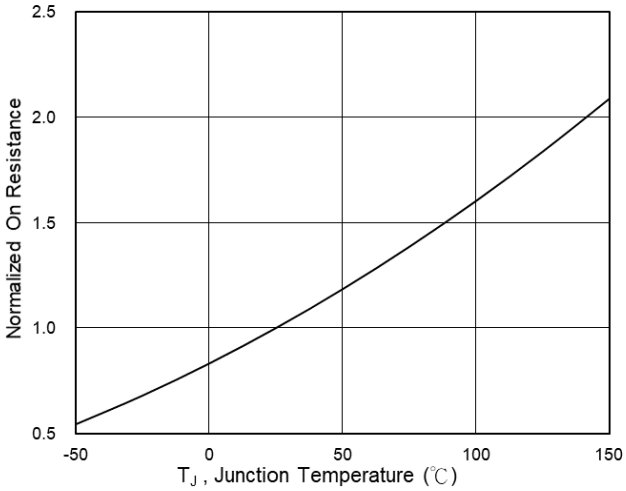


Fig.6 Normalized R_{DSON} vs T_J

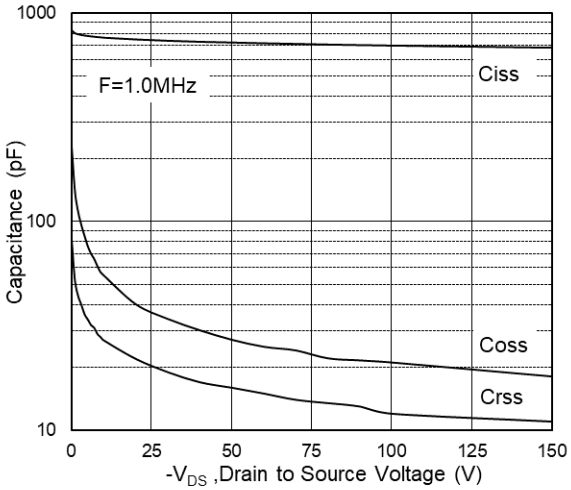


Fig.7 Capacitance

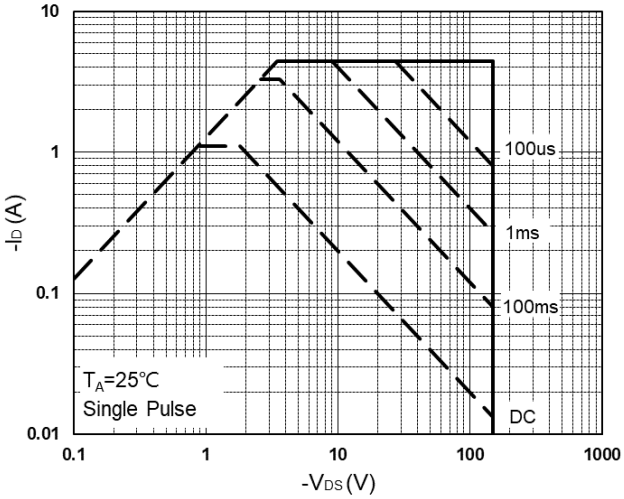


Fig.8 Safe Operating Area

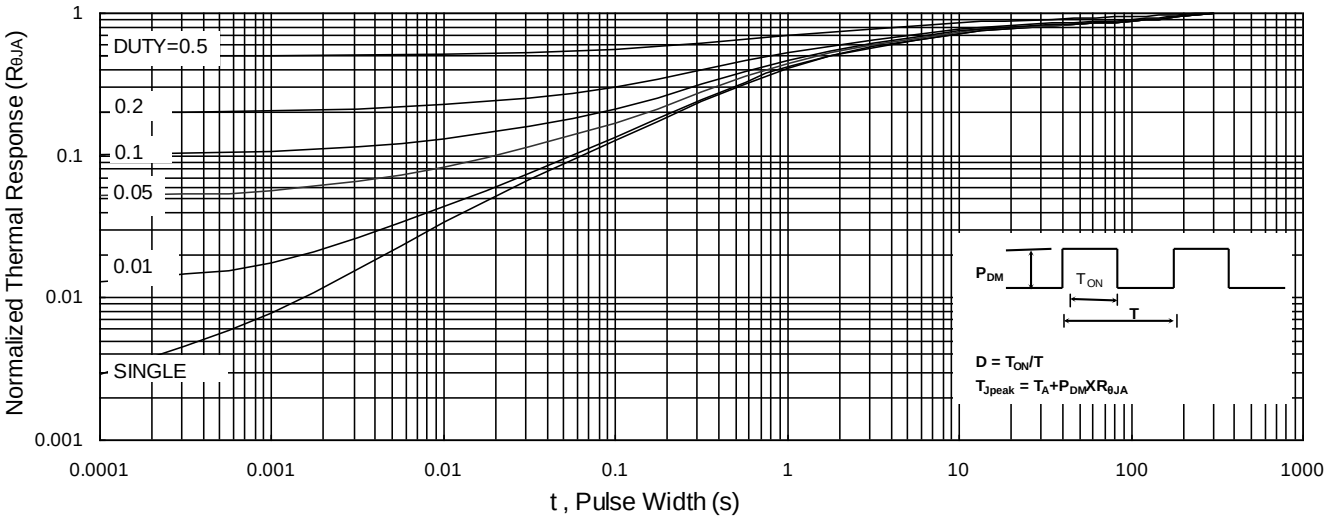


Fig.9 Normalized Maximum Transient Thermal Impedance

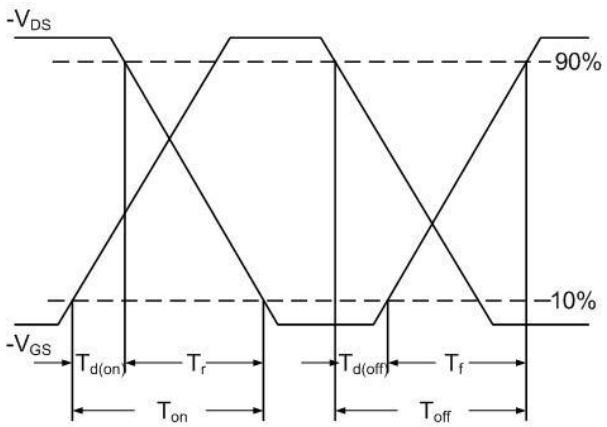


Fig.10 Switching Time Waveform

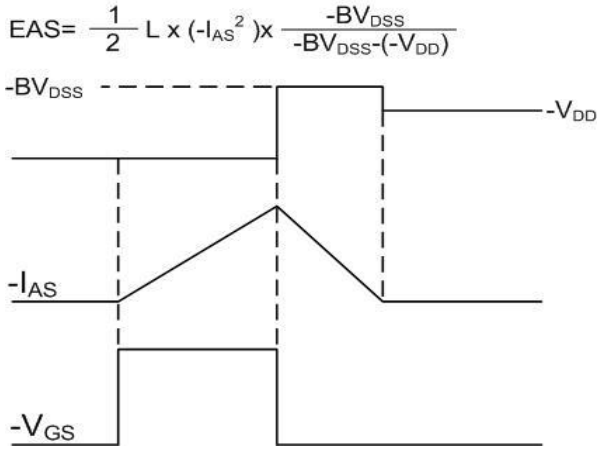
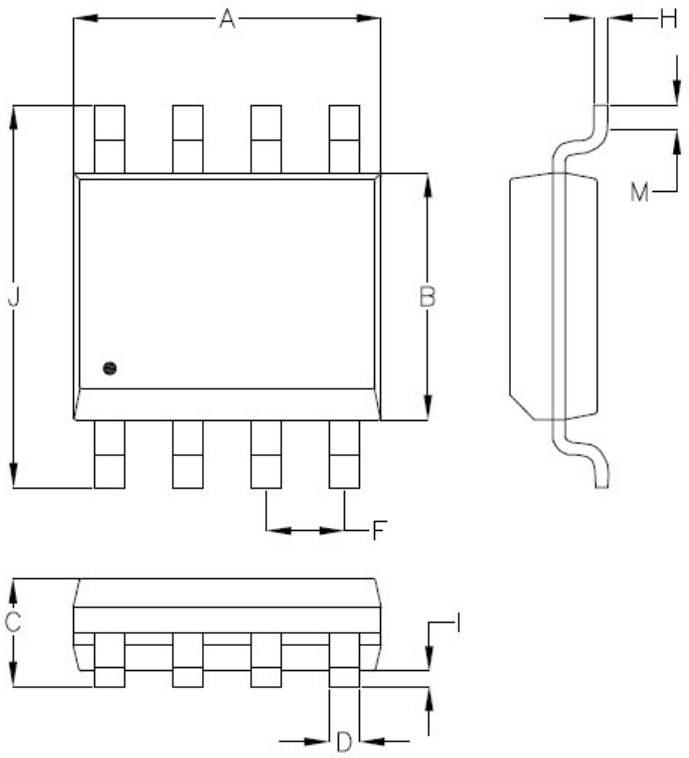


Fig.11 Unclamped Inductive Waveform

SOP-8L Package Outline



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.700	5.150	0.185	0.203
B	3.700	4.100	0.146	0.161
C	1.23	1.753	0.048	0.069
D	0.310	0.510	0.012	0.020
F	1.070	1.470	0.042	0.058
H	0.160	0.254	0.006	0.010
I	0.050	0.254	0.002	0.010
J	5.750	6.250	0.226	0.246
M	0.400	1.270	0.016	0.050

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