

## Features

- 100% EAS Guaranteed
- Green Device Available
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

## Product Summary

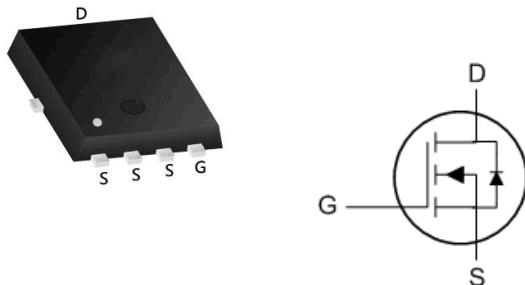
BVDSS	RDS(ON)	ID
30V	18mΩ	28A

## Description

The JHQ3002 is the high cell density trenched N-ch MOSFETs, which provide excellent RDS(ON) and gate charge for most of the synchronous buck converter applications.

The JHQ3002 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

## DFN3X3 Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	30	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	28	A
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	18	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	55	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	22.1	mJ
I <sub>AS</sub>	Avalanche Current	21	A
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	20	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient <sup>1</sup>	---	75	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>	---	6	°C/W

**Electrical Characteristics ( $T_J=25^{\circ}\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_{\text{D}}=250\mu\text{A}$	30	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BVDSS Temperature Coefficient	Reference to $25^{\circ}\text{C}$ , $I_{\text{D}}=1\text{mA}$	---	0.022	---	$\text{V}/^{\circ}\text{C}$
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=10\text{A}$	---	---	18	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$ , $I_{\text{D}}=5\text{A}$	---	---	30	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_{\text{D}}=250\mu\text{A}$	1.0	---	2.5	V
$\Delta V_{\text{GS(th)}}$	$V_{\text{GS(th)}}$ Temperature Coefficient		---	-5.1	---	$\text{mV}/^{\circ}\text{C}$
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=24\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^{\circ}\text{C}$	---	---	1	$\text{uA}$
		$V_{\text{DS}}=24\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=55^{\circ}\text{C}$	---	---	5	
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=5\text{V}$ , $I_{\text{D}}=1\text{A}$	---	4.5	---	S
$R_g$	Gate Resistance	$V_{\text{DS}}=0\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	2.5	---	$\Omega$
$Q_g$	Total Gate Charge (4.5V)	$V_{\text{DS}}=20\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $I_{\text{D}}=10\text{A}$	---	7.2	---	$\text{nC}$
$Q_{\text{gs}}$	Gate-Source Charge		---	1.4	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	2.2	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=12\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_{\text{G}}=3.3\Omega$	---	4.1	---	$\text{ns}$
$T_r$	Rise Time		---	9.8	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	15.5	---	
$T_f$	Fall Time		---	6.0	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=15\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	572	---	$\text{pF}$
$C_{\text{oss}}$	Output Capacitance		---	81	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	65	---	

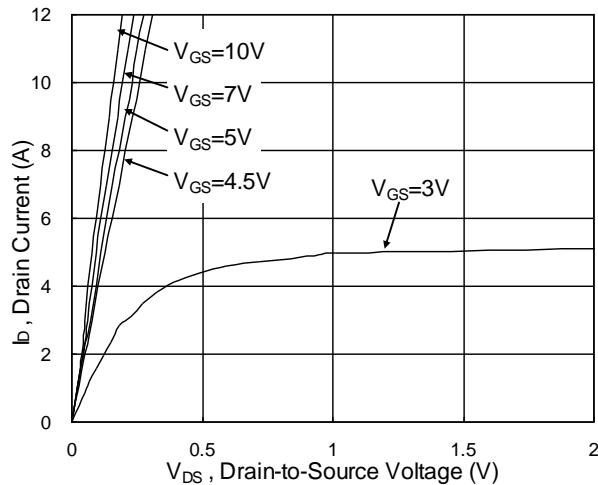
**Diode Characteristics**

$I_s$	Continuous Source Current <sup>1,5</sup>	$V_G=V_D=0\text{V}$ , Force Current	---	---	28	A
$I_{\text{SM}}$	Pulsed Source Current <sup>2,5</sup>		---	---	55	A
$V_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^{\circ}\text{C}$	---	---	1.2	V

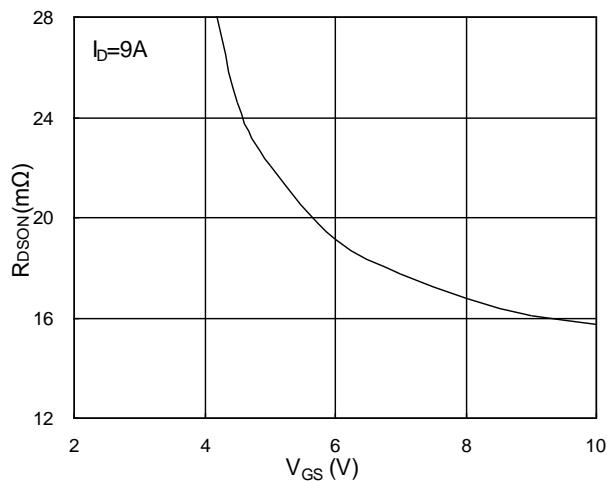
Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=25\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $L=0.1\text{mH}$ , $I_{\text{AS}}=21\text{A}$
- 4.The power dissipation is limited by  $150^{\circ}\text{C}$  junction temperature
- 5.The data is theoretically the same as  $I_{\text{D}}$  and  $I_s$  , in real applications , should be limited by total power dissipation.

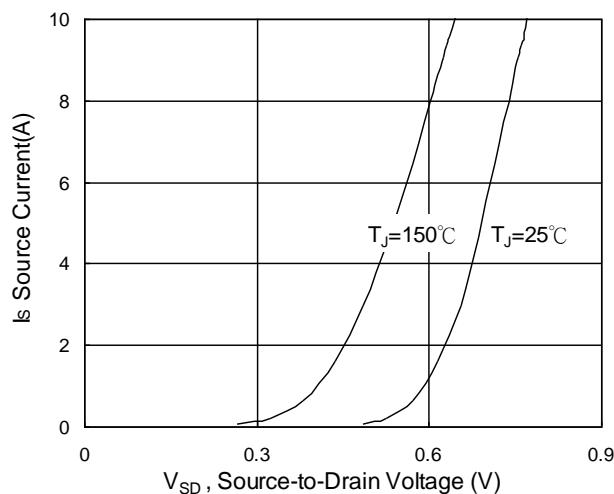
### Typical Characteristics



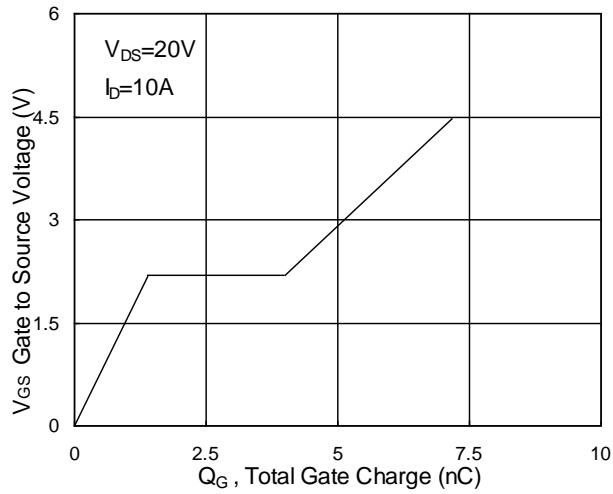
**Fig.1 Typical Output Characteristics**



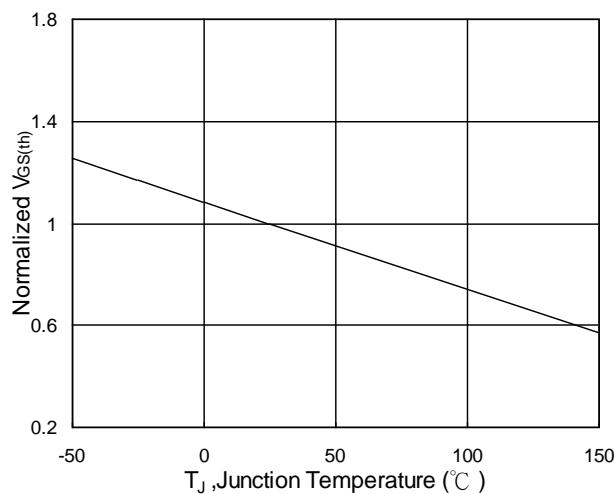
**Fig.2 On-Resistance vs. Gate-Source**



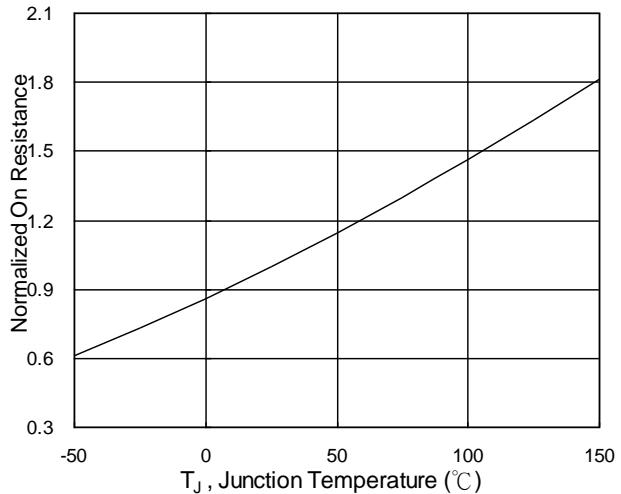
**Fig.3 Forward Characteristics Of Reverse**



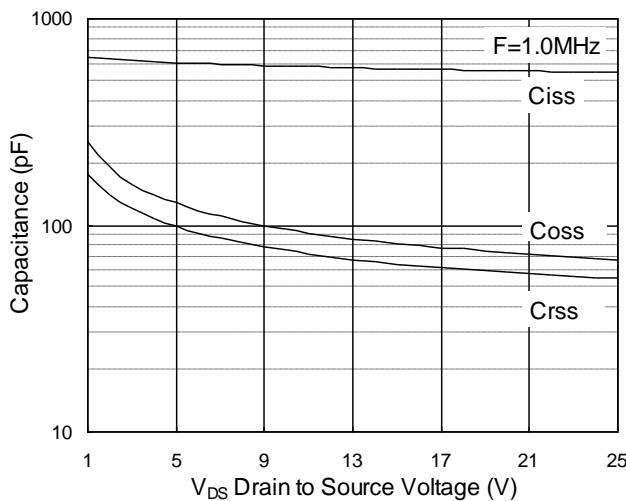
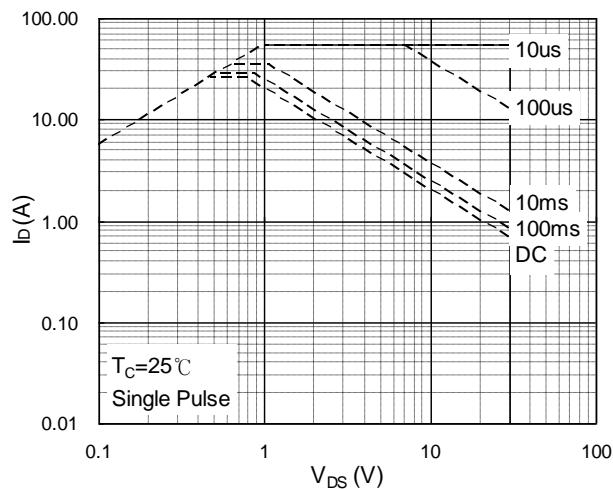
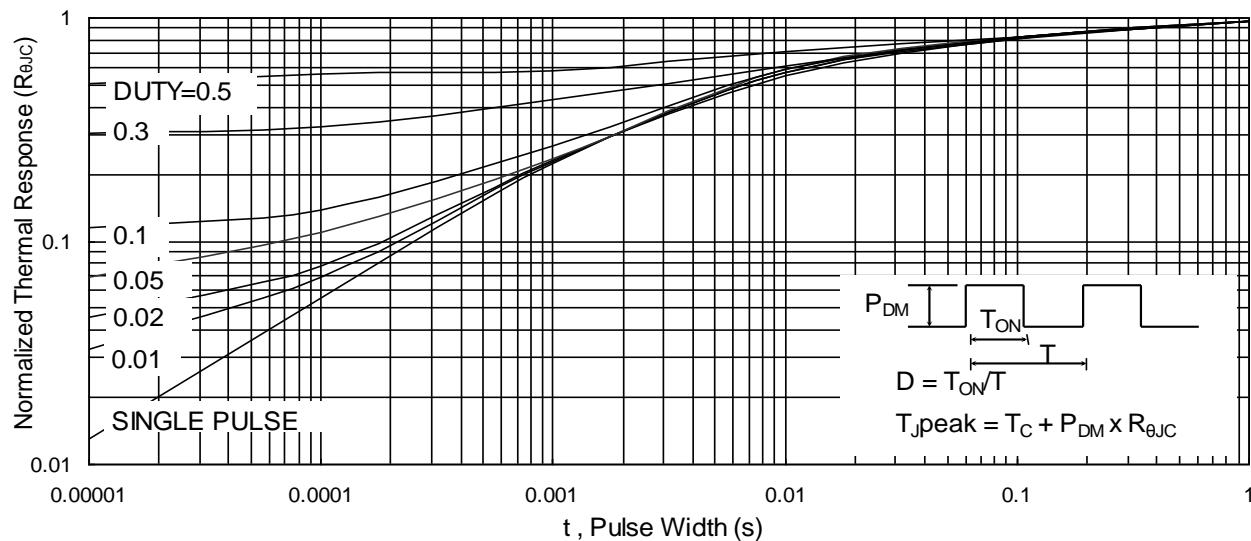
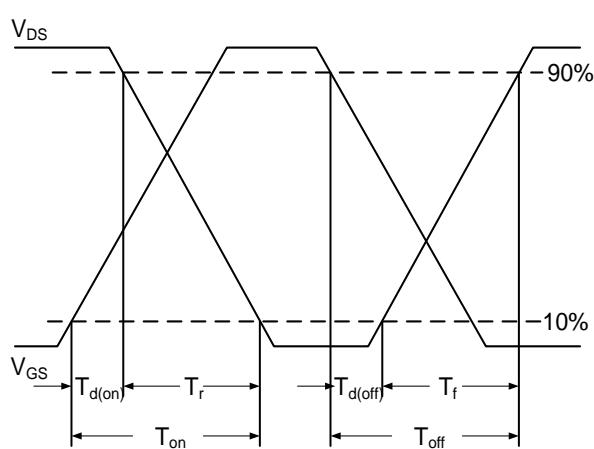
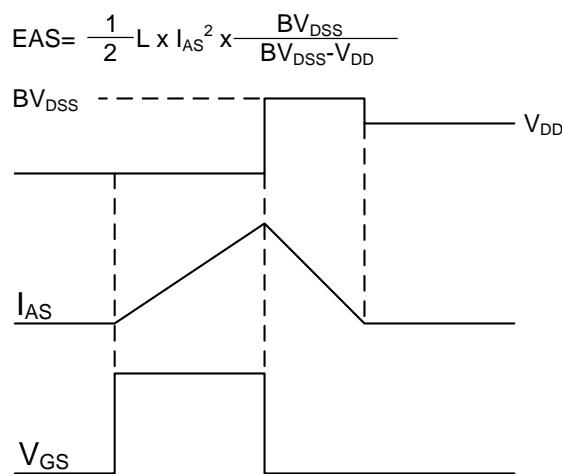
**Fig.4 Gate-Charge Characteristics**



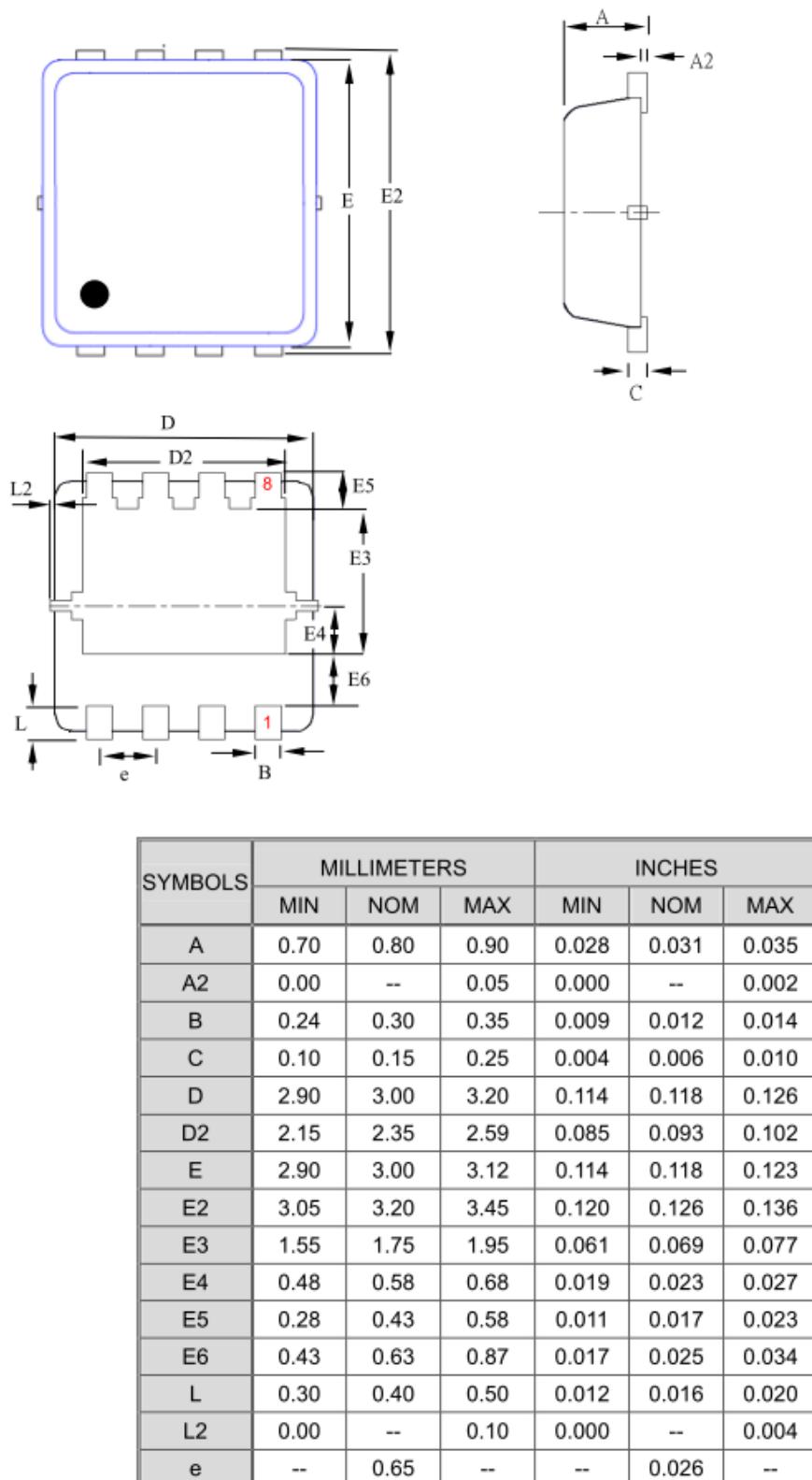
**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**



**Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$**


**Fig.7 Capacitance**

**Fig.8 Safe Operating Area**

**Fig.9 Normalized Maximum Transient Thermal Impedance**

**Fig.10 Switching Time Waveform**

**Fig.11 Unclamped Inductive Waveform**

## DFN3\*3 Package Outline Dimensions



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