

## Features

- Advanced Trench MOS Technology
- 100% EAS Guaranteed
- Fast Switching Speed
- Green Device Available

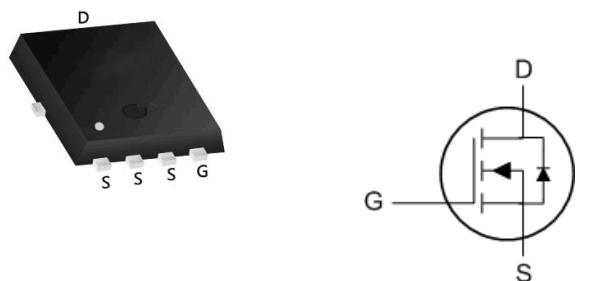
## Product Summary

BVDSS	RDS(ON)	ID
100V	8mΩ	78A

## Applications

- High Frequency Switching and Synchronous Rectification.
- DC/DC Converter.

## DFN5X6 Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	100	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current <sup>1,6</sup>	78	A
I <sub>D</sub> @T <sub>C</sub> =70°C	Continuous Drain Current <sup>1,6</sup>	62	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	280	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	39.2	mJ
I <sub>AS</sub>	Avalanche Current	28	A
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	108	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-Ambient <sup>1</sup> (t≤10s)	---	25	°C/W
	Thermal Resistance Junction-Ambient <sup>1</sup>	---	55	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>	---	1.3	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	100	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=10\text{V}$ , $I_D=13.5\text{A}$	---	6.6	8	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$	2	---	4	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=80\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{\text{DS}}=80\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=55^\circ\text{C}$	---	---	5	
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_D=20\text{A}$	---	80	---	S
$Q_g$	Total Gate Charge (10V)	$V_{\text{DS}}=50\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_D=13.5\text{A}$	---	45	---	nC
$Q_g$	Total Gate Charge (4.5V)		---	19.3	---	
$Q_{\text{gs}}$	Gate-Source Charge		---	9.5	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	4.8	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=50\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_G=3\Omega$ , $I_D=13.5\text{A}$	---	10	---	ns
$T_r$	Rise Time		---	6.5	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	45	---	
$T_f$	Fall Time		---	7.5	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=50\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	3148	---	pF
$C_{\text{oss}}$	Output Capacitance		---	693	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	26	---	

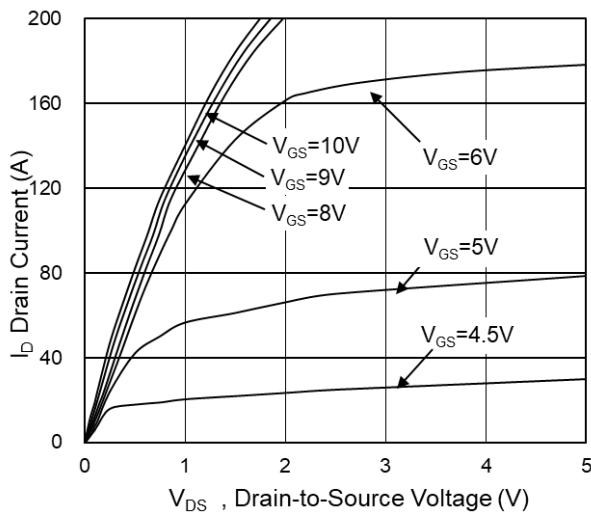
**Diode Characteristics**

$I_s$	Continuous Source Current <sup>1,5,6</sup>	$V_{\text{G}}=V_{\text{D}}=0\text{V}$ , Force Current	---	---	45	A
$V_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1.1	V
$t_{\text{rr}}$	Reverse Recovery Time	$I_F=13.5\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$ , $T_J=25^\circ\text{C}$	---	33	---	nS
$Q_{\text{rr}}$	Reverse Recovery Charge		---	150	---	nC

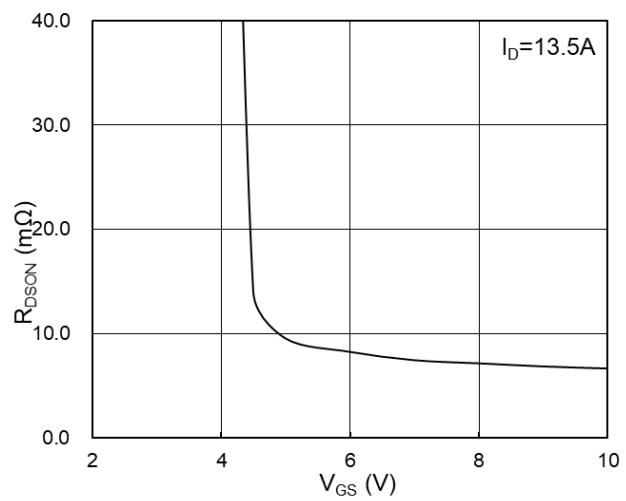
Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=50\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $L=0.1\text{mH}$ , $I_{\text{AS}}=28\text{A}$
- 4.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- 5.The data is theoretically the same as  $I_b$  and  $I_s$  , in real applications , should be limited by total power dissipation.
- 6.The maximum current rating is package limited.

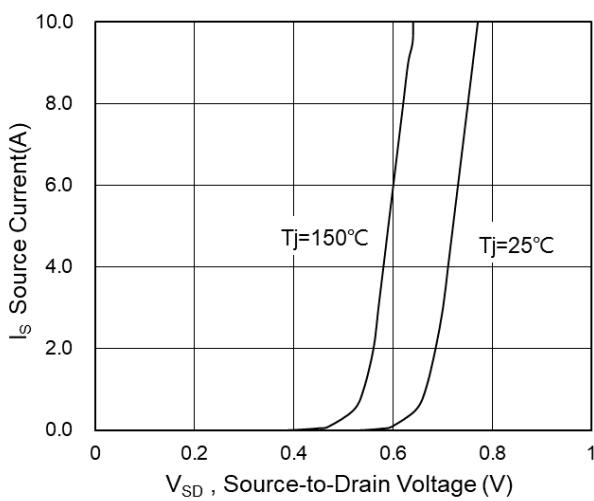
## Typical Characteristics



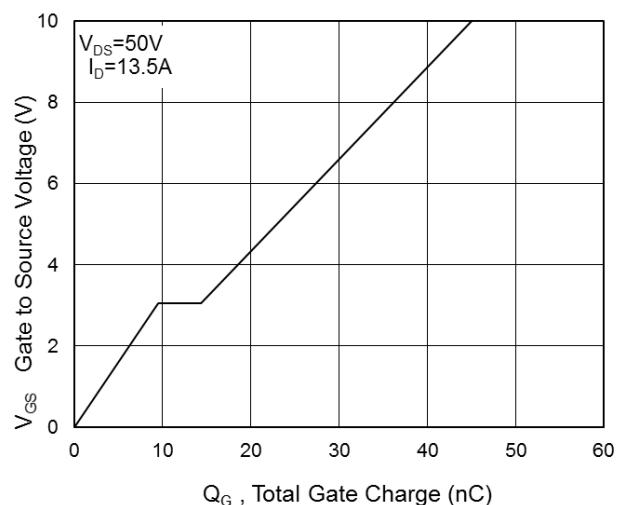
**Fig.1 Typical Output Characteristics**



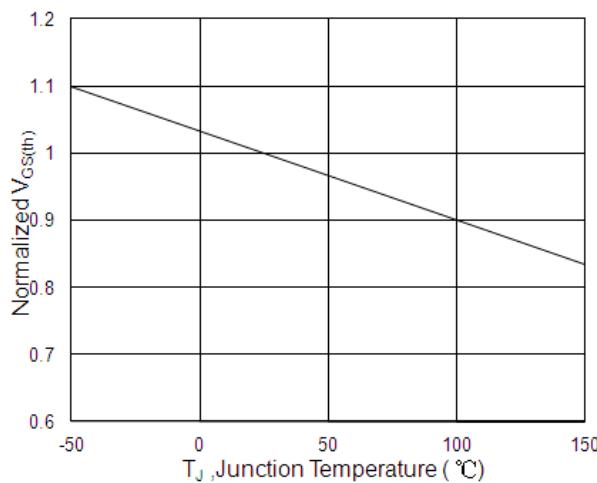
**Fig.2 On-Resistance vs G-S Voltage**



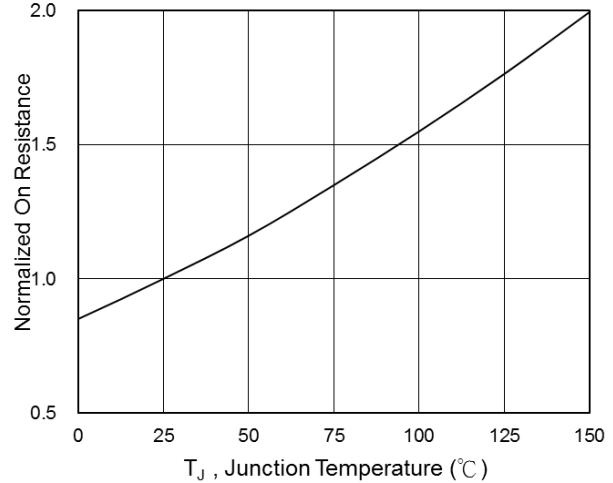
**Fig.3 Source-Drain Forward Characteristics**



**Fig.4 Gate-Charge Characteristics**



**Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$**



**Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$**

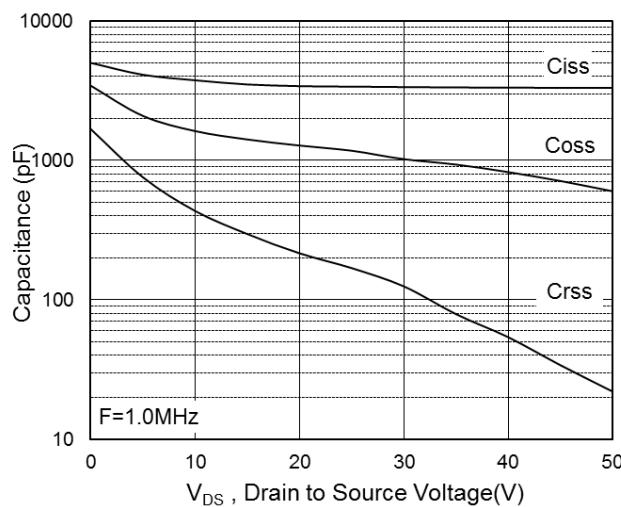


Fig.7 Capacitance

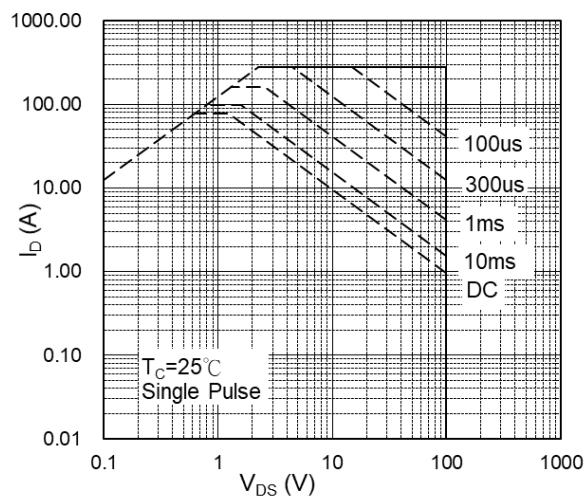


Fig.8 Safe Operating Area

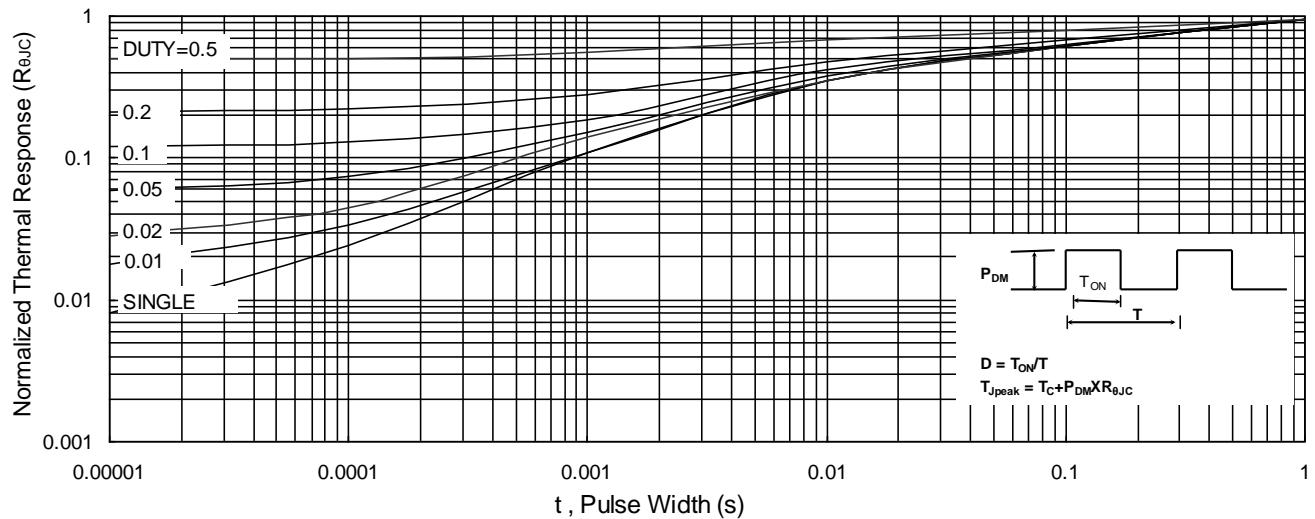


Fig.9 Normalized Maximum Transient Thermal Impedance

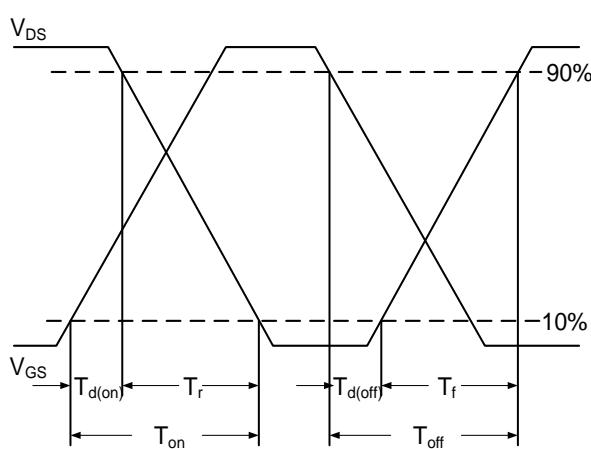


Fig.10 Switching Time Waveform

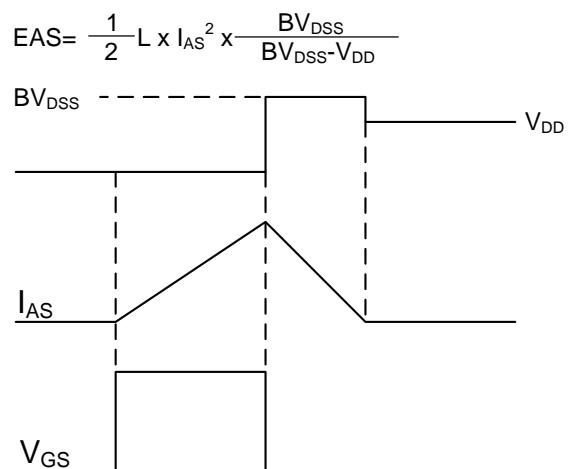
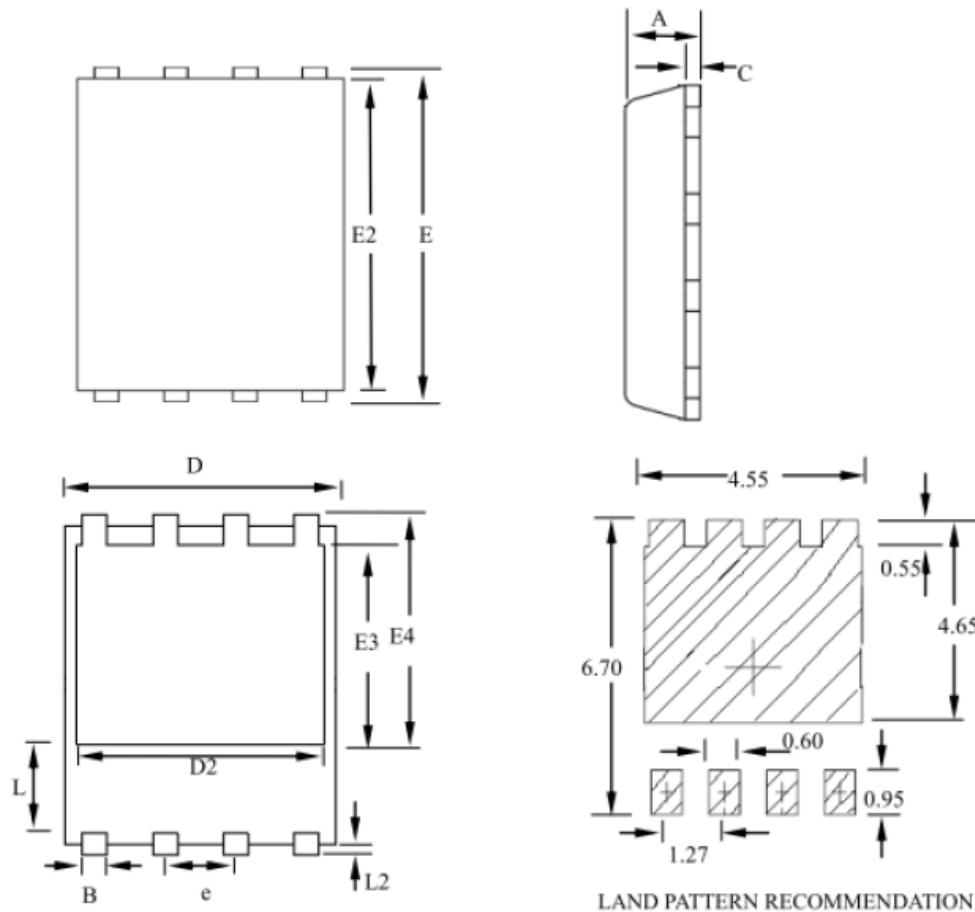


Fig.11 Unclamped Inductive Switching Waveform

## DFN5X6 Package Outline Dimensions



LAND PATTERN RECOMMENDATION



SYMBOLS	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	--	1.20	0.031	--	0.047
B	0.30	--	0.51	0.012	--	0.020
C	0.15	--	0.35	0.006	--	0.014
D	4.80	--	5.30	0.189	--	0.209
D2	3.61	--	4.35	0.142	--	0.171
E	5.90	--	6.35	0.232	--	0.250
E2	5.42	--	5.90	0.213	--	0.232
E3	3.23	--	3.90	0.127	--	0.154
E4	3.69	--	4.55	0.145	--	0.179
L	0.61	--	1.80	0.024	--	0.071
L2	0.05	--	0.36	0.002	--	0.014
e	--	1.27	--	--	0.050	--

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