

## Description

- Advanced Trench MOS Technology
- Low Gate Charge
- Low  $R_{DS(ON)}$
- 100% EAS Guaranteed
- Green Device Available

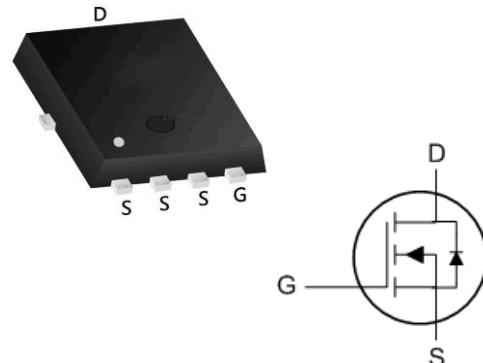
## Product Summary

| BVDSS | RDS(on) | ID  |
|-------|---------|-----|
| 40V   | 3.2mΩ   | 89A |

## Applications

- Power Management in Desktop Computer or DC/DC Converters.
- Isolated DC/DC Converters in Telecom and Industrial.

## DFN5X6 Pin Configuration



## Absolute Maximum Ratings

| Symbol                  | Parameter                                  | Rating     | Units |
|-------------------------|--------------------------------------------|------------|-------|
| $V_{DS}$                | Drain-Source Voltage                       | 40         | V     |
| $V_{GS}$                | Gate-Source Voltage                        | $\pm 20$   | V     |
| $I_D @ T_c=25^\circ C$  | Continuous Drain Current, $V_{GS} @ 10V^1$ | 89         | A     |
| $I_D @ T_c=100^\circ C$ | Continuous Drain Current, $V_{GS} @ 10V^1$ | 71         | A     |
| $I_{DM}$                | Pulsed Drain Current <sup>2</sup>          | 240        | A     |
| EAS                     | Single Pulse Avalanche Energy <sup>3</sup> | 145        | mJ    |
| $I_{AS}$                | Avalanche Current                          | 54         | A     |
| $P_D @ T_c=25^\circ C$  | Total Power Dissipation <sup>4</sup>       | 22         | W     |
| $T_{STG}$               | Storage Temperature Range                  | -55 to 150 | °C    |
| $T_J$                   | Operating Junction Temperature Range       | -55 to 150 | °C    |

## Thermal Data

| Symbol          | Parameter                                        | Typ. | Max. | Unit |
|-----------------|--------------------------------------------------|------|------|------|
| $R_{\theta JA}$ | Thermal Resistance Junction-Ambient <sup>1</sup> | ---  | 55   | °C/W |
| $R_{\theta JC}$ | Thermal Resistance Junction-Case <sup>1</sup>    | ---  | 1.7  | °C/W |

# JHG4094

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## N-Channel Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

| Symbol                       | Parameter                                      | Conditions                                                                                  | Min. | Typ. | Max.      | Unit             |
|------------------------------|------------------------------------------------|---------------------------------------------------------------------------------------------|------|------|-----------|------------------|
| $\text{BV}_{\text{DSS}}$     | Drain-Source Breakdown Voltage                 | $V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$                                            | 40   | ---  | ---       | V                |
| $R_{\text{DS}(\text{ON})}$   | Static Drain-Source On-Resistance <sup>2</sup> | $V_{\text{GS}}=10\text{V}$ , $I_D=20\text{A}$                                               | ---  | 2.5  | 3.2       | $\text{m}\Omega$ |
|                              |                                                | $V_{\text{GS}}=4.5\text{V}$ , $I_D=15\text{A}$                                              | ---  | 3.8  | 5.3       |                  |
| $V_{\text{GS}(\text{th})}$   | Gate Threshold Voltage                         | $V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$                                        | 1.2  | 1.7  | 2.2       | V                |
| $I_{\text{DSS}}$             | Drain-Source Leakage Current                   | $V_{\text{DS}}=40\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$             | ---  | ---  | 1         | $\text{uA}$      |
|                              |                                                | $V_{\text{DS}}=40\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=55^\circ\text{C}$             | ---  | ---  | 5         |                  |
| $I_{\text{GSS}}$             | Gate-Source Leakage Current                    | $V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$                                  | ---  | ---  | $\pm 100$ | nA               |
| $g_{\text{fs}}$              | Forward Transconductance                       | $V_{\text{DS}}=5\text{V}$ , $I_D=20\text{A}$                                                | ---  | 75   | ---       | S                |
| $R_g$                        | Gate Resistance                                | $V_{\text{DS}}=0\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$                     | ---  | 1.5  | ---       | $\Omega$         |
| $Q_g$                        | Total Gate Charge (4.5V)                       | $V_{\text{DS}}=20\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $I_D=20\text{A}$                 | ---  | 22.7 | ---       | $\text{nC}$      |
| $Q_{\text{gs}}$              | Gate-Source Charge                             |                                                                                             | ---  | 7.5  | ---       |                  |
| $Q_{\text{gd}}$              | Gate-Drain Charge                              |                                                                                             | ---  | 5.5  | ---       |                  |
| $T_{\text{d}(\text{on})}$    | Turn-On Delay Time                             | $V_{\text{DD}}=20\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_G=3\Omega$<br>$I_D=20\text{A}$ | ---  | 10   | ---       | $\text{ns}$      |
| $T_r$                        | Rise Time                                      |                                                                                             | ---  | 5    | ---       |                  |
| $T_{\text{d}(\text{off})}$   | Turn-Off Delay Time                            |                                                                                             | ---  | 33   | ---       |                  |
| $T_f$                        | Fall Time                                      |                                                                                             | ---  | 6.5  | ---       |                  |
| $C_{\text{iss}}$             | Input Capacitance                              | $V_{\text{DS}}=20\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$                    | ---  | 2648 | ---       | $\text{pF}$      |
| $C_{\text{oss}}$             | Output Capacitance                             |                                                                                             | ---  | 899  | ---       |                  |
| $C_{\text{rss}}$             | Reverse Transfer Capacitance                   |                                                                                             | ---  | 71   | ---       |                  |
| <b>Diode Characteristics</b> |                                                |                                                                                             |      |      |           |                  |
| $I_s$                        | Continuous Source Current <sup>1,5</sup>       | $V_G=V_D=0\text{V}$ , Force Current                                                         | ---  | ---  | 30        | A                |
| $V_{\text{SD}}$              | Diode Forward Voltage <sup>2</sup>             | $V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$                        | ---  | ---  | 1         | V                |

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=25\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $L=0.1\text{mH}$ , $I_{\text{AS}}=54\text{A}$
- 4.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- 5.The data is theoretically the same as  $I_D$  and  $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.

# JHG4094

## N-Channel Typical Characteristics

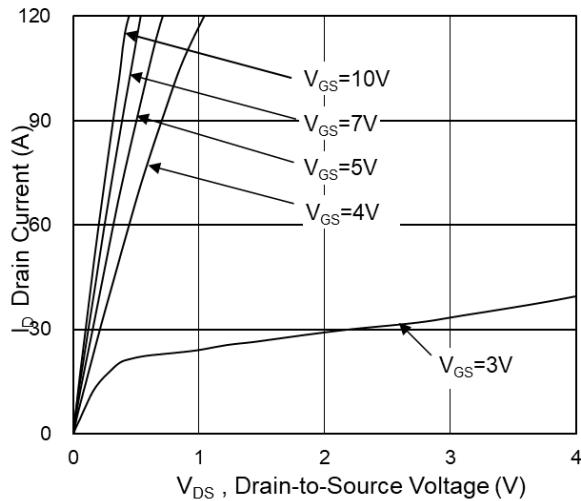


Fig.1 Typical Output Characteristics

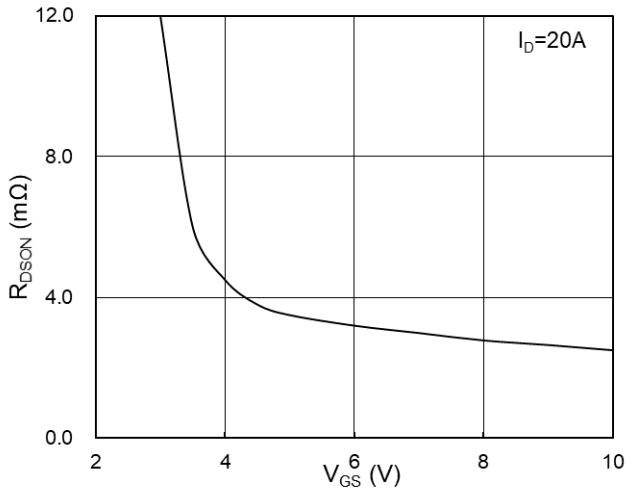


Fig.2 On-Resistance vs G-S Voltage

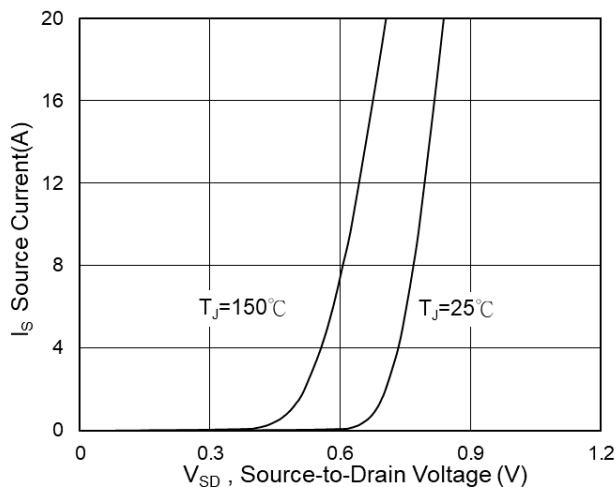


Fig.3 Source Drain Forward Characteristics

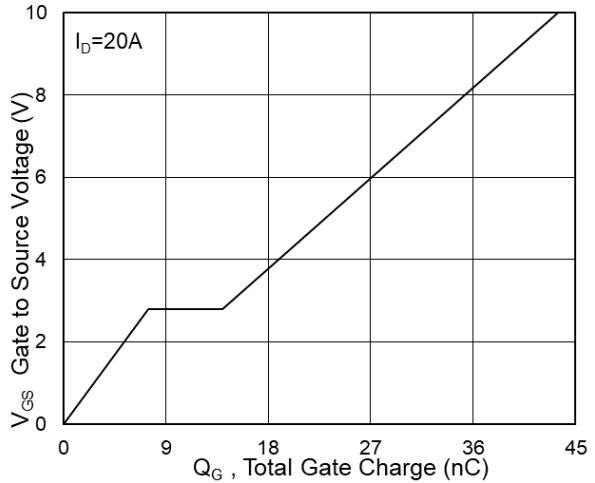


Fig.4 Gate-Charge Characteristics

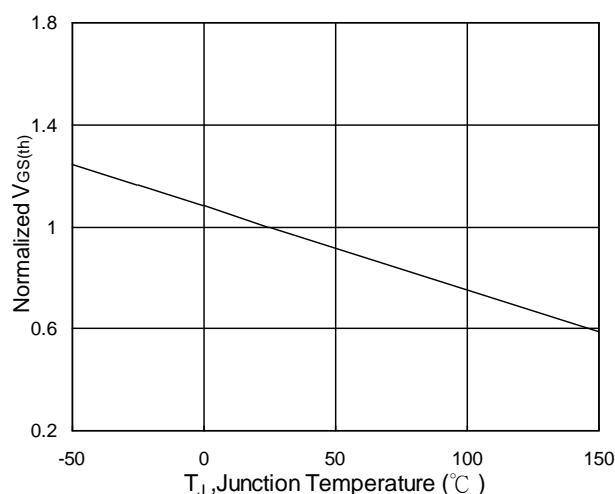


Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$

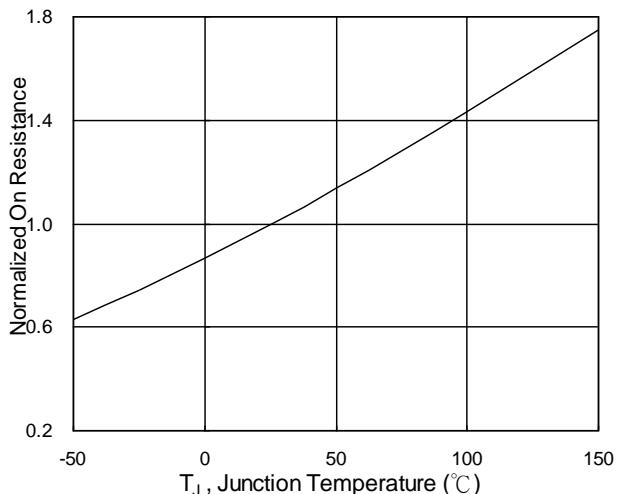
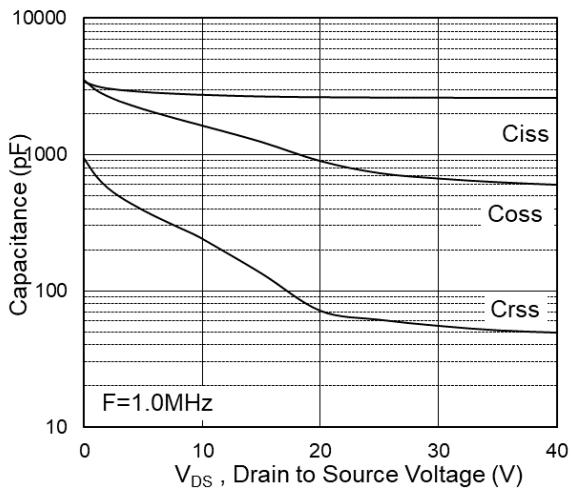
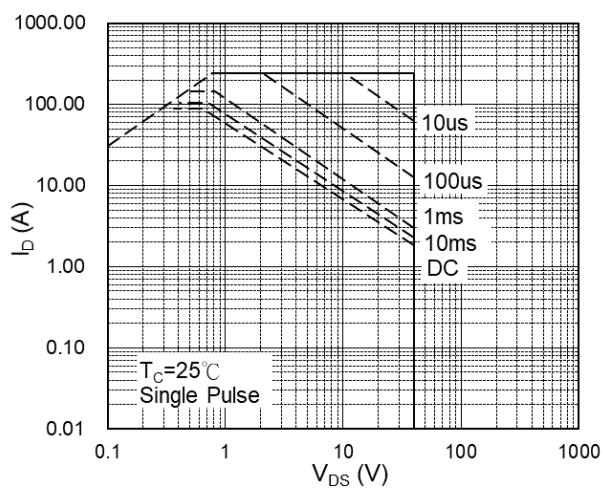


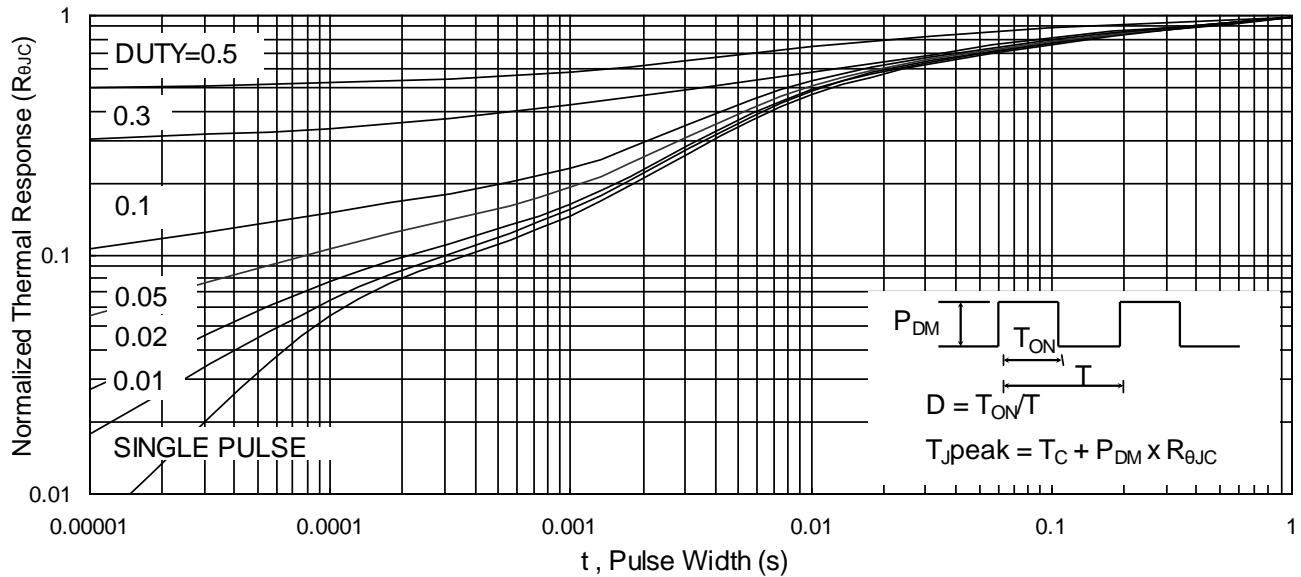
Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$



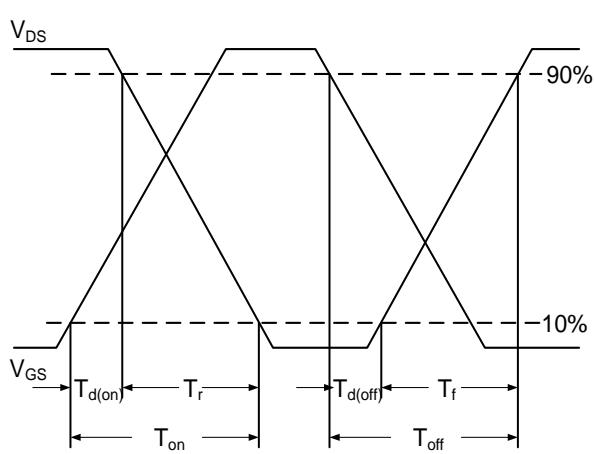
**Fig.7 Capacitance**



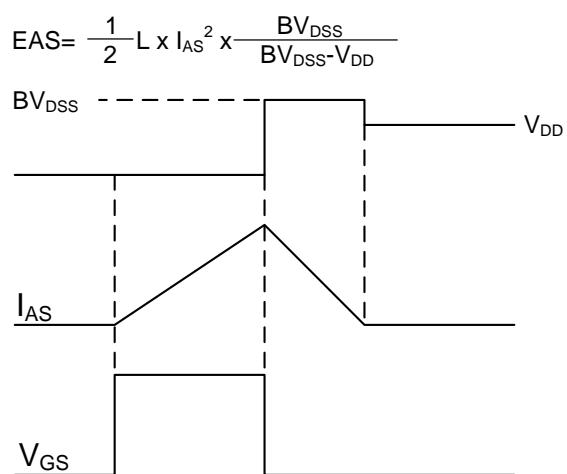
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**

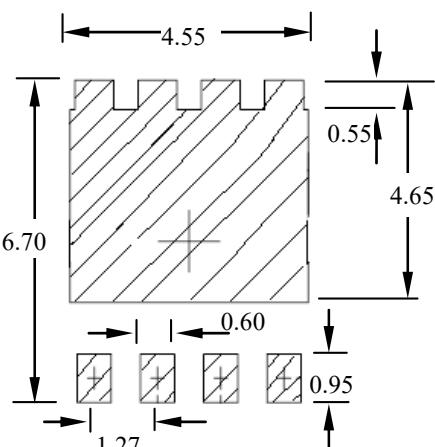
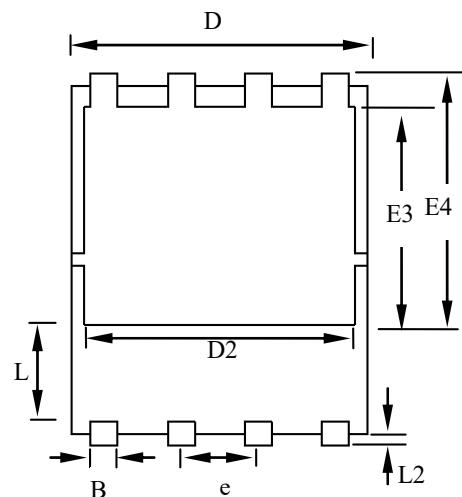
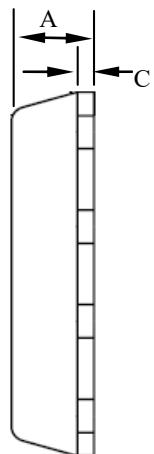
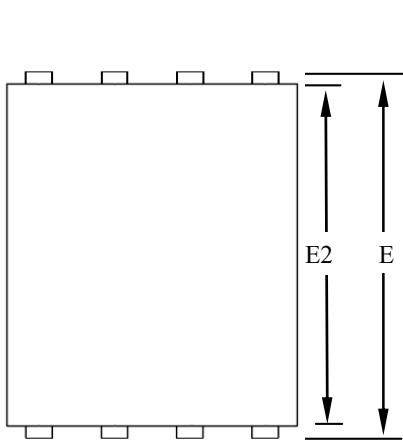


**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Switching Wave**

## DFN5×6 Outline



LAND PATTERN RECOMMENDATION



| SYMBOLS | MILLIMETERS |      |      | INCHES |       |       |
|---------|-------------|------|------|--------|-------|-------|
|         | MIN         | NOM  | MAX  | MIN    | NOM   | MAX   |
| A       | 0.80        | --   | 1.20 | 0.031  | --    | 0.047 |
| B       | 0.30        | --   | 0.51 | 0.012  | --    | 0.020 |
| C       | 0.15        | --   | 0.35 | 0.006  | --    | 0.014 |
| D       | 4.80        | --   | 5.30 | 0.189  | --    | 0.209 |
| D2      | 3.61        | --   | 4.35 | 0.142  | --    | 0.171 |
| E       | 5.90        | --   | 6.35 | 0.232  | --    | 0.250 |
| E2      | 5.42        | --   | 5.90 | 0.213  | --    | 0.232 |
| E3      | 3.23        | --   | 3.90 | 0.127  | --    | 0.154 |
| E4      | 3.69        | --   | 4.55 | 0.145  | --    | 0.179 |
| L       | 0.61        | --   | 1.80 | 0.024  | --    | 0.071 |
| L2      | 0.05        | --   | 0.36 | 0.002  | --    | 0.014 |
| e       | --          | 1.27 | --   | --     | 0.050 | --    |

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