

## FEATURES

- For general purpose applications
- The SD101 series is a Metal-on-silicon junction Schottky barrier device which is protected by a PN junction guard ring. The low forward voltage drop and fast switching make it ideal for protection of MOS devices, steering, biasing, and coupling diodes for fast switching and low logic level applications.

These diodes are also available in the MiniMELF case with the type designation LL101A to LL101C.

- + High temperature soldering guaranteed:260°C/10 seconds at terminals
- $\cdot$  Component in accordance to RoHS 2011/65/EU

## MECHANICAL DATA

- · Case: DO-35 glass case
- · Polarity: Color band denotes cathode end
- Weight: Approx. 0.05 gram

# ABSOLUTE RATINGS (LIMITING VALUES)





Dimensions in inches and (millimeters)

		Symbols	Value	Units
Peak Reverse Voltage	SD101A SD101B SD101C	Vrrm Vrrm Vrrm	60 50 40	V V
Power Dissipation (infinite Heat Sink)		Ptot	400 1)	mW
Maximum Single cycle surge 10ms square wave		Ifsm	2.0	A
Junction temperature		TJ	125	°C
Storage Temperature Range		Tstg	-55 to+150	°C
1) Valid provided that leads at a distance of	of 4mm from case are kept	at ambient temperature		

#### ELECTRICAL CHARACTERISTICS

(Ratings at 25°C ambient temperature unless otherwise specified)

		Symbols	Min.	Typ.	Max.	Unis		
Reverse breakover voltage at IR=10mA	SD101A SD101B SD101C	Vr Vr Vr	60 50 40			V V V		
Leakage current at V <sub>R</sub> =50V V <sub>R</sub> =40V V <sub>R</sub> =30V	SD101A SD101B SD101C	IR IR IR			200 200 200	nA nA nA		
Forward voltage drop at Ir=1mA Ir=15mA	SD101A SD101B SD101C SD101A SD101B SD101C	VF VF VF VF VF			0.41 0.4 0.39 1 0.95 0.9	>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>		
Junction Capacitance at VR=0V ,f=1MH	IZ SD101A SD101B SD101C	С С С С			2.0 2.1 2.2	pF pF pF		
Reverse Recovery time at IF=IR=5mA,recover to 0.1 IR		trr			1	ns		
Thermal resistance, junction to Ambient		Røja			300 1)	°C/W		
1) Valid provided that leads at a distance of 4mm from case are kept at ambient temperature								

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# RATINS AND CHARACTERISTICS CURVES SD101A THRU SD101C

Figure 1. Typical variation of fwd.current vs.fwd. Voltage for primary conduction through the schottky barrier



Figure 3.Typical variation of reverse current at various temperatures



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Figure 2. Typical forward conduction curve of combination Schottky barrier and PN junction guard ring



Figure 4. Typical capacitance curve as a function of reverse voltage

