

General Description

These N-channel enhancement mode power mosfets used advanced trench technology design, provided excellent Rdson and low gate charge. Which accords with the RoHS standard.

Product Summary			
V _{DS}	R _{DSON} (on) (mΩ) Typ	I _D (A)	Q _G (Typ)
20V	4.5@ 4.5V	60	65nc

Features

- Fast switching
- Low on-resistance
- Low gate charge
- 100% Single Pulse Avalanche Energy Test

TO-252

JHD60N02A



Mechanical Data

- Case:TO-252 Package

Ordering Information

Part No.	Package Type	Package	Quality(box)
JHD60N02A	TO-252	Tape & Reel	2500

Block Diagram

Pin Definition:

1. Gate
2. Drain
3. Source

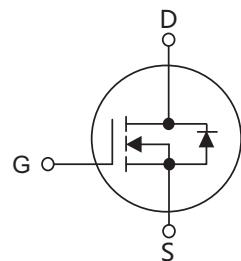


Table1 Absolute Maximum Ratings(T_C=25°C, unless otherwise specified)

Parameter	Symbol	TO-252	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _{GS}	±10	V
Continuous Drain Current T _C =25°C	I _D	60	A
T _C =100°C		42	
Pulsed Drain Current (Note 1)	I _{DM}	210	A
Single Pulse Avalanche Energy(Note 2)	E _{AS}	195	mJ
Power Dissipation T _C =25°C	P _D	35	W
Isolation Voltage	V _{ISO}	/	V
Operating Junction and Storage Temperature	T _J /T _{STG}	-55 ~ +175	°C
Maximum Temperature for soldering	T _L	300	°C

Table 2.Thermal Characteristics

Parameter	Symbol	TO-252	Unit
Thermal resistance Junction to Case	R _{θJC}	2.8	°C/W

Table 3. Electrical Characteristics (T_J=25°C, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	V _{DSS}	V _{GS} =0V,I _D =250μA	20	--	--	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =20V,V _{GS} =0V	--	--	1	μA
Gate- Source Leakage Current	Forward	V _{GS} =10V,V _{DS} =0V	--	--	100	nA
	Reverse	V _{GS} =-10V,V _{DS} =0V	--	--	-100	nA
On Characteristics(Note 4)						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} ,I _D =250μA	0.4	0.62	1.0	V
Static Drain-Source On-State Resistance	R _{DSON}	V _{GS} =4.5V,I _D =20A	--	4.5	6.0	mΩ
Dynamic Characteristics(Note 5)						
Input Capacitance	C _{iss}	V _{DS} =10V,V _{GS} =0V,f=1MHz	--	2450	--	pF
Output Capacitance	C _{oss}		--	430	--	pF
Reverse Transfer Capacitance	C _{rss}		--	205	--	pF
Switching Characteristics (Note 5)						
Turn-On Delay Time	t _{d(on)}	V _{DS} =10V,I _D =10A, V _{GS} =4.5V,R _{GEN} =3Ω	--	12	--	ns
Turn-On Rise Time	t _r		--	26	--	ns
Turn-Off Delay Time	t _{d(off)}		--	35	--	ns
Turn-Off Fall Time	t _f		--	10	--	ns
Total Gate Charge	Q _G	V _{DS} =10V,I _D =15A, V _{GS} =4.5V	--	65	--	nC
Gate-Source Charge	Q _{GS}		--	15	--	nC
Gate-Drain Charge	Q _{GD}		--	13	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
Drain-Source Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =20A	--	--	1.2	V
Maximum Continuous Drain-Source Diode Forward Current	I _S		--	--	60	A
Reverse Recovery Time	t _{rr}	V _{GS} =0V, I _F =15A dI _F /dt=100A/μs (Note 1)	--	35	--	ns
Reverse Recovery Charge	Q _{RR}		--	39	--	nC

Notes: 1 Repetitive Rating:Pulse width limited by maximum junction temperature

2 L=0.5mH, R_g=25Ω, V_{DD}=15V, V_{GATE}=10V, Starting T_J=25°C

4 Pulse Test: Pulse width ≤300μS, Duty cycle≤2%

5 Guaranteed by design,not subject to production

Typical characteristics diagrams

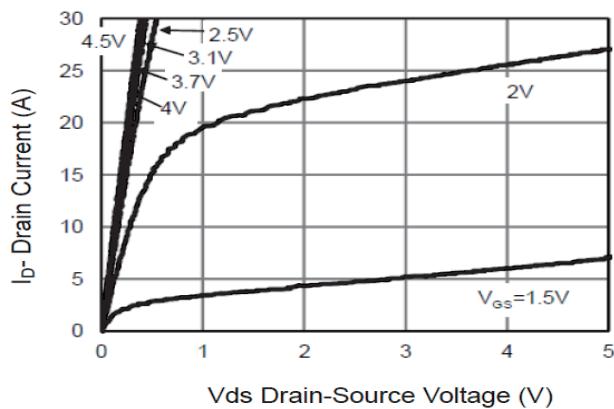


Figure1. Output Characteristics

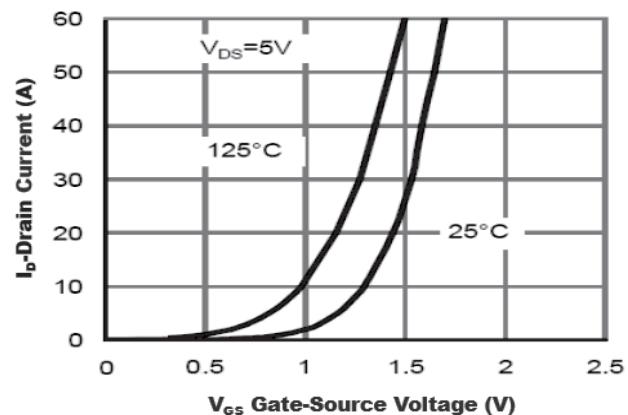


Figure2. Transfer Characteristics

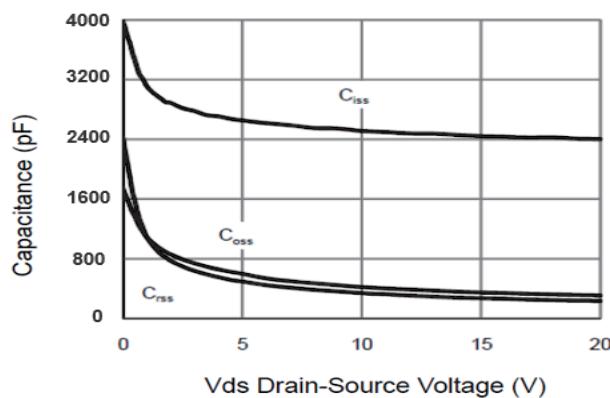


Figure3. Capacitance Characteristics

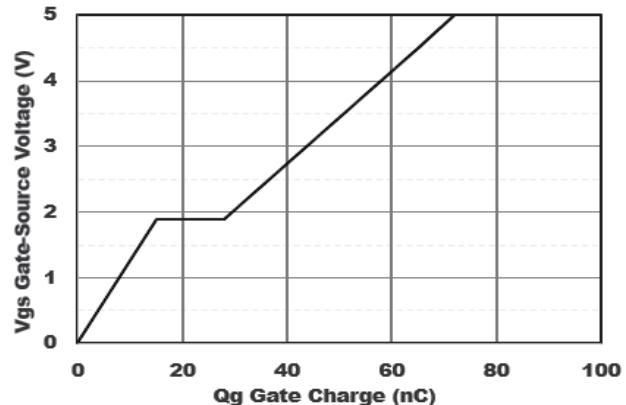


Figure4. Gate Charge

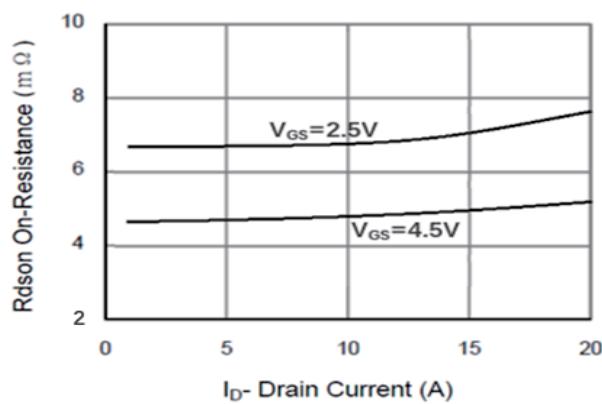


Figure5. Drain -Source on Resistance

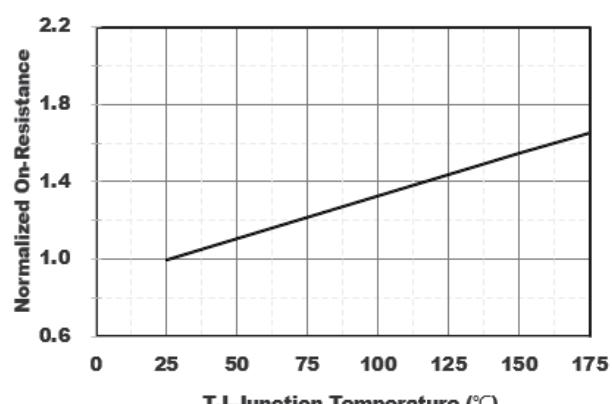


Figure6. Drain-Source on Resistance

Typical characteristics diagrams

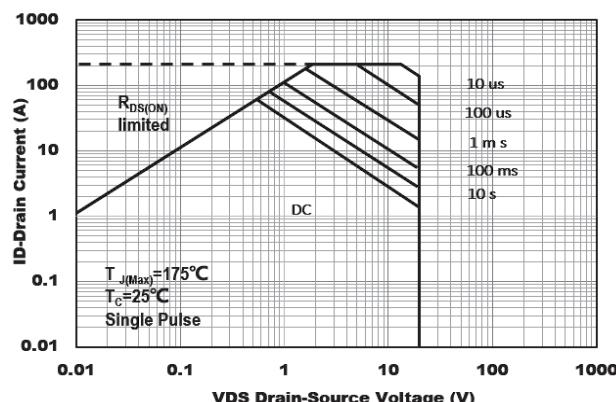


Figure 7. Safe Operation Area

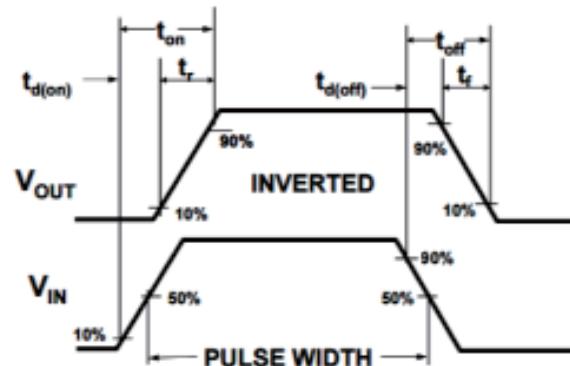
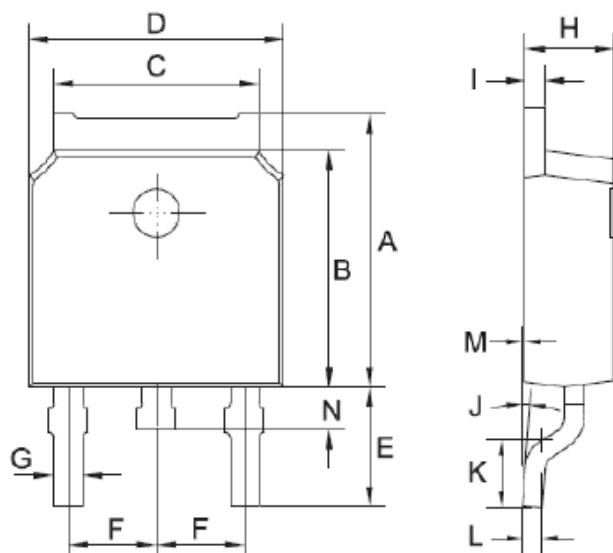


Figure 8. Switching wave

Dimensions

TO-252 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	6.85	7.25	0.270	0.285
B	5.8	6.3	0.228	0.248
C	5	5.53	0.197	0.218
D	6.3	6.8	0.248	0.268
E	2.6	3.3	0.102	0.130
F	2.19	2.39	0.086	0.094
G	0.45	0.85	0.018	0.033
H	2.2	2.4	0.087	0.094
I	0.41	0.61	0.016	0.024
J	0°	8°	0°	8°
K	1.45	1.85	0.057	0.073
L	0.41	0.61	0.016	0.024
M	0	0.12	0.000	0.005
N	0.6	1	0.024	0.039

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