

General Description

- Advanced Trench MOS Technology
- 100% EAS Guaranteed
- Fast Switching Speed
- Green Device Available

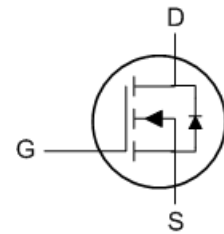
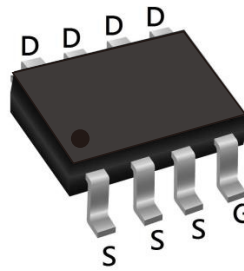
Product Summary

BVDSS	RDSON	ID
100V	20mΩ	9.3A

Applications

- Portable Equipment.
- Battery Powered Systems.
- Hard Switching and High-Speed Circuit.

SOP-8 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_A=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	9.3	A
$I_D@T_A=70^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	7.4	A
I_{DM}	Pulsed Drain Current ²	37	A
EAS	Single Pulse Avalanche Energy ³	45	mJ
I_{AS}	Avalanche Current	30	A
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation ⁴	3.1	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹ ($t \leq 10S$)	---	40	$^\circ\text{C/W}$
	Thermal Resistance Junction-ambient ¹ (Steady State)	---	85	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction-case ¹	---	24	$^\circ\text{C/W}$

Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	100	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=8A$	---	16	20	m Ω
		$V_{GS}=4.5V, I_D=6A$	---	22	30	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	1.8	2.2	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=80V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{DS}=80V, V_{GS}=0V, T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1\text{MHz}$	---	1.1	---	Ω
Q_g	Total Gate Charge	$V_{DS}=50V, V_{GS}=10V, I_D=8A$	---	17.9	---	nC
Q_{gs}	Gate-Source Charge		---	2.8	---	
Q_{gd}	Gate-Drain Charge		---	5.2	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=30V, V_{GS}=10V, R_G=6\Omega, I_D=1A$	---	13	---	ns
T_r	Rise Time		---	6	---	
$T_{d(off)}$	Turn-Off Delay Time		---	30	---	
T_f	Fall Time		---	29	---	
C_{iss}	Input Capacitance	$V_{DS}=50V, V_{GS}=0V, f=1\text{MHz}$	---	849	---	pF
C_{oss}	Output Capacitance		---	185	---	
C_{riss}	Reverse Transfer Capacitance		---	8	---	
Diode Characteristics						
I_S	Continuous Source Current ^{1,5}	$V_G=V_D=0V$, Force Current	---	---	9.3	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	---	1.2	V

Note :

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- The EAS data shows Max. rating . The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.1\text{mH}, I_{AS}=30A$
- The power dissipation is limited by 150°C junction temperature
- The data is theoretically the same as I_D and I_S , in real applications , should be limited by total power dissipation.

Typical Characteristics

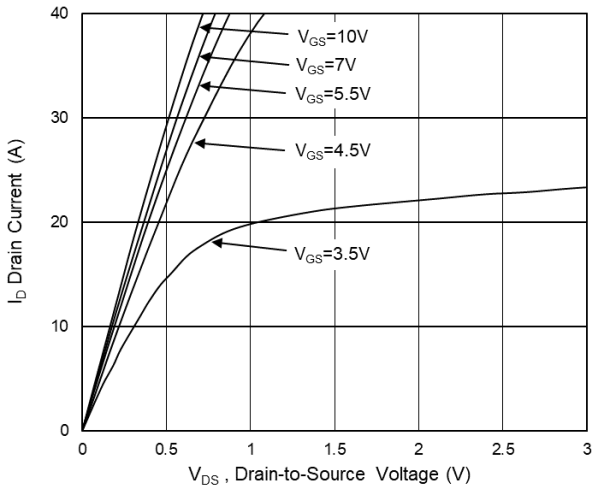


Fig.1 Typical Output Characteristics

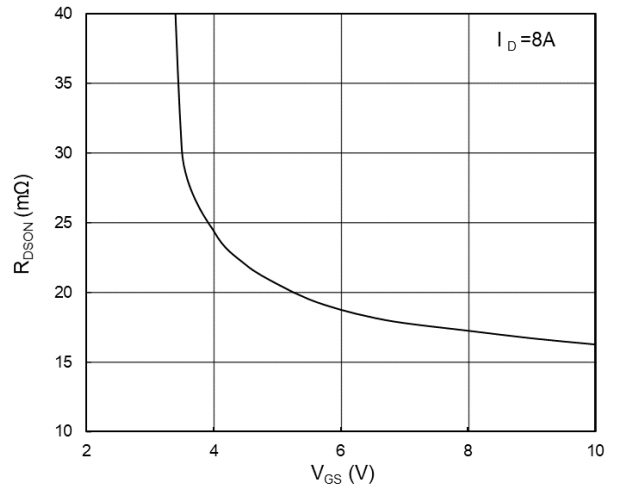


Fig.2 On-Resistance vs G-S Voltage

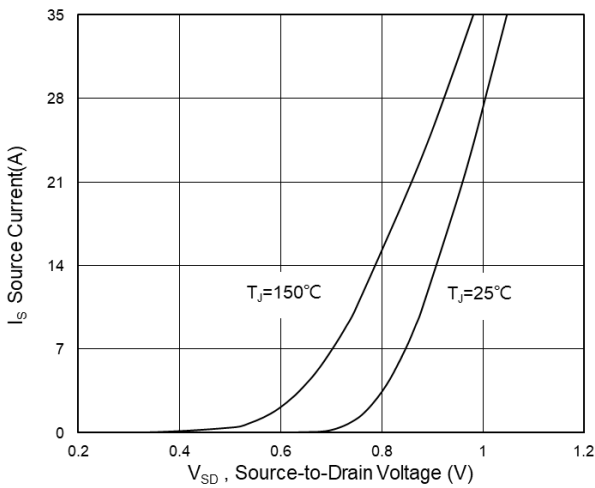


Fig.3 Source Drain Forward Characteristics

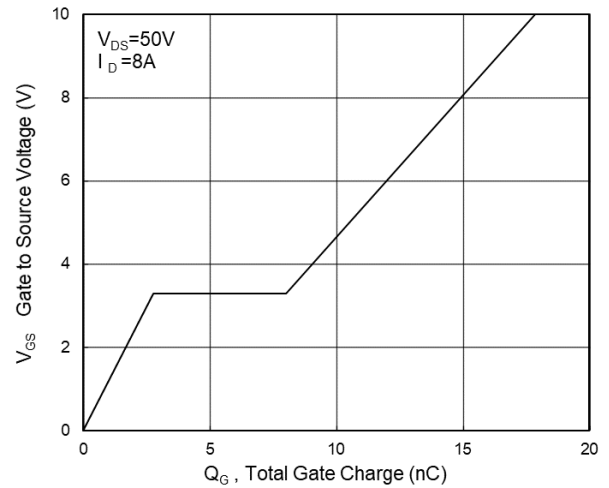


Fig.4 Gate-Charge Characteristics

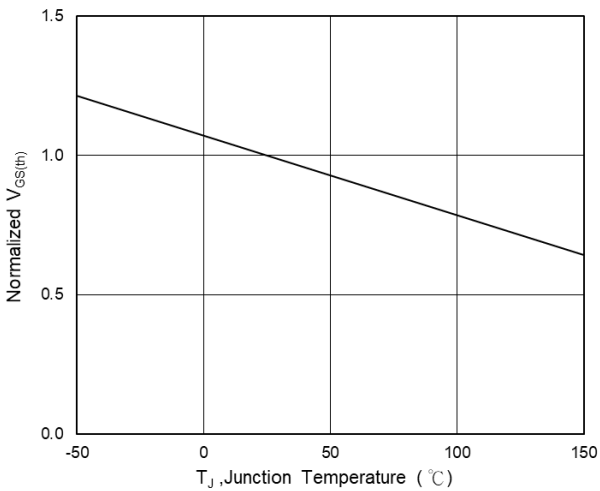


Fig.5 Normalized $V_{GS(th)}$ vs T_J

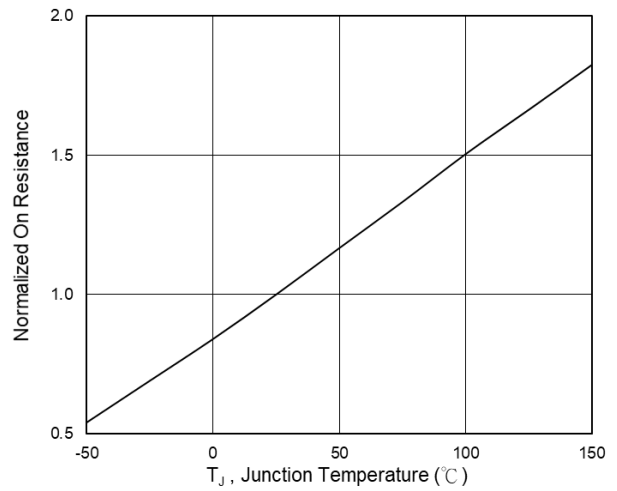


Fig.6 Normalized $R_{DS(on)}$ vs T_J

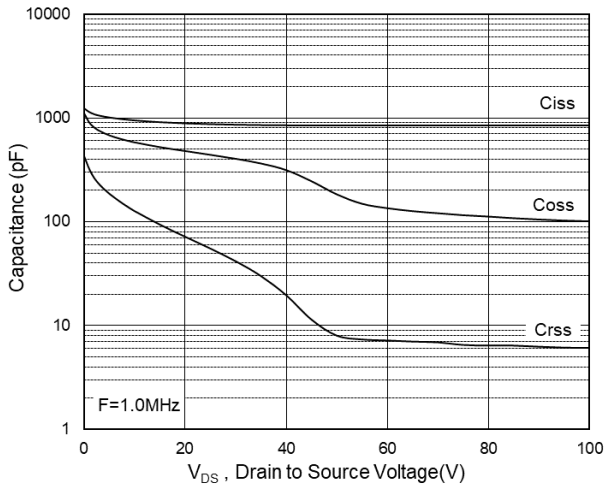


Fig.7 Capacitance

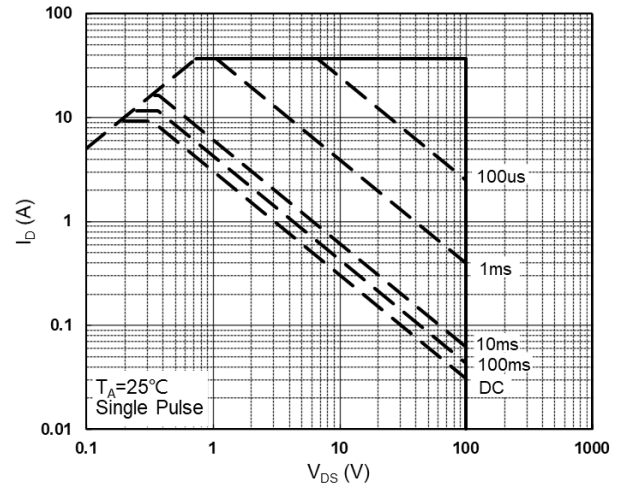


Fig.8 Safe Operating Area

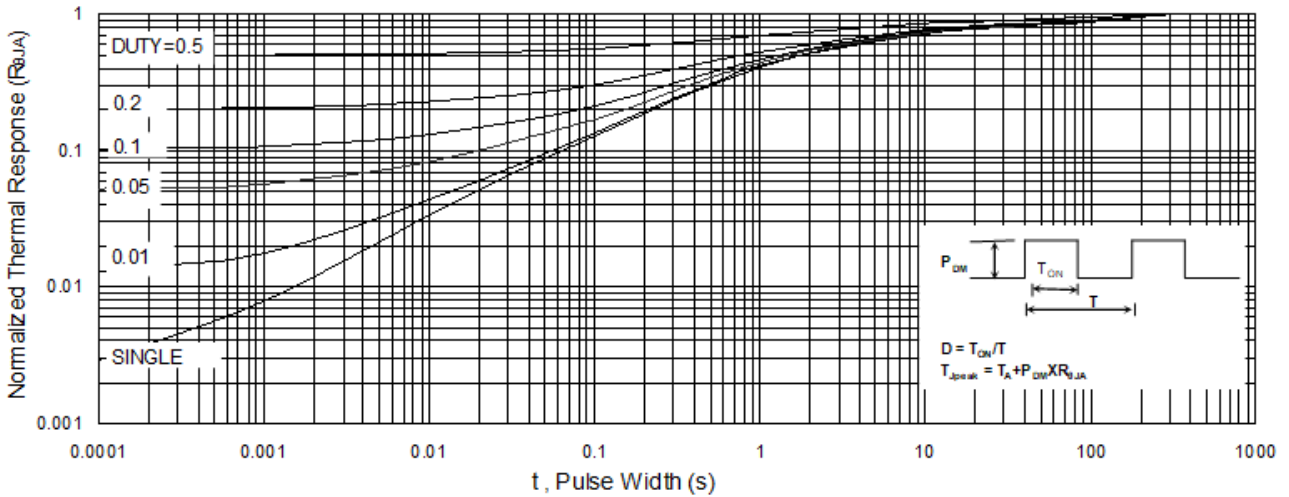


Fig.9 Normalized Maximum Transient Thermal Impedance

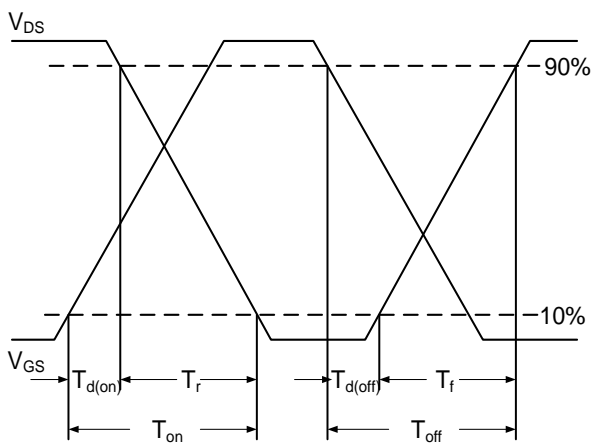


Fig.10 Switching Time Waveform

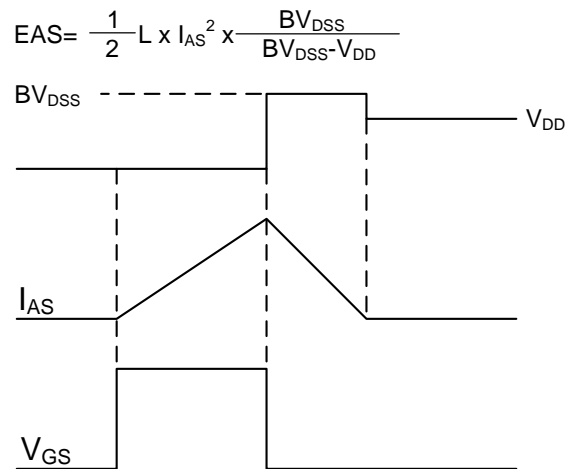
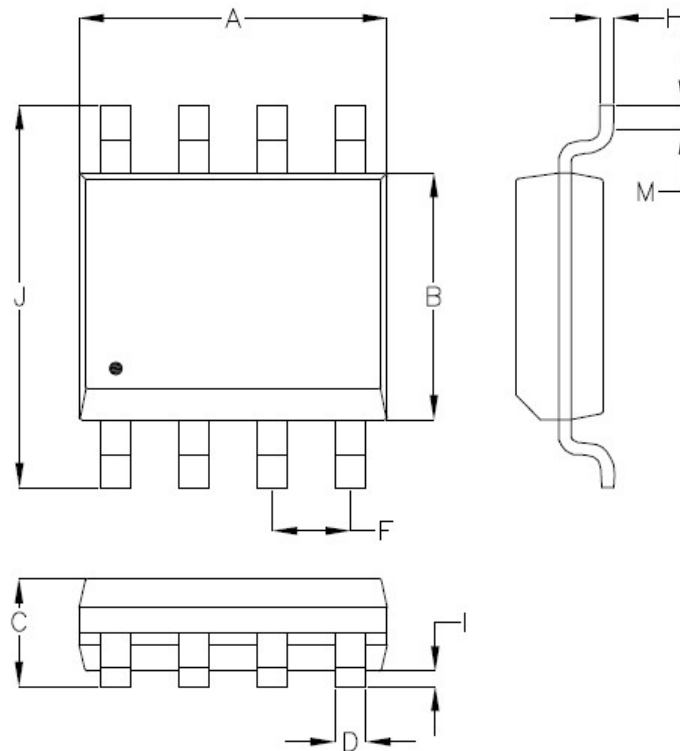


Fig.11 Unclamped Inductive Switching Waveform

SOP-8L Package Outline



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.700	5.150	0.185	0.203
B	3.700	4.100	0.146	0.161
C	1.23	1.753	0.048	0.069
D	0.310	0.510	0.012	0.020
F	1.070	1.470	0.042	0.058
H	0.160	0.254	0.006	0.010
I	0.050	0.254	0.002	0.010
J	5.750	6.250	0.226	0.246
M	0.400	1.270	0.016	0.050

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