

Features

- Uses advanced SGT technology
- Low gate charge and input capacitance
- 100% avalanche tested
- Rohs compliant

Mechanical Data

- Case:TO-220, TO-263 Package

Application

- Motor control and drives
- Battery management
- DC/DC converter
- General purpose applications



Product Summary			
V _{DS}	R _{DS(on)} (mΩ)Typ	I _D (A)	Q _g (Typ)
150V	5.9 @ 10V,60A	140	56.8nc



Ordering Information

Part No.	Marking.	Package Type	Package	Quality(box)
DS140N15G3	DS140N15G3	TO-220	Tube	1000
DS140N15DG3	DS140N15DG3	TO-263	Tape & Reel	800

Pin Definition:

1. Gate
2. Drain
3. Source

Block Diagram

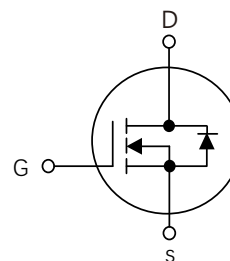


Table1 Absolute Maximum Ratings (T_c=25°C, unless otherwise specified)

Parameters	Symbol	DS140N15G3 DS140N15DG3	Unit
Drain-Source Voltage	V _{DS}	150	V
Gate-Source Voltage	V _{GS}	±20	V
Contionous Drain Current	I _D	T _C =25°C	A
		T _C =100°C	
Pulsed Drain Current (Note 1)	I _{DM}	560	A
Single Pulse Avalanche Energy(Note 2)	EAS	1024	mJ
Power Dissipation T _C =25°C	P _D	300	W
Operating Junction and Storage Temperature	T _J /T _{STG}	-55 ~ +175	°C

Table 2. Thermal Characteristics

Parameters	Symbol	Value	Unit
Thermal resistance Junction to Ambient,Max	$R_{\theta JA}$	65	$^{\circ}\text{C}/\text{W}$
Thermal resistance Junction to Case,Max	$R_{\theta JC}$	0.5	$^{\circ}\text{C}/\text{W}$

Table 3. Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise specified)

Parameters		Symbol	Test Conditions	Min	Typ	Max	Unit
Off Characteristics							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V,I _D =250μA	150			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =150V,V _{GS} =0V			1	μA
Gate- Source Leakage Current	Forward	I _{GSS}	V _{GS} =20V,V _{DS} =0V			100	nA
	Reverse		V _{GS} = -20V,V _{DS} =0V			-100	nA
On Characteristics(Note 3)							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} ,I _D =250μA	2.0	3.0	4.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V,I _D =60A		5.9	7.0	mΩ
Dynamic Characteristics(Note 4)							
Input Capacitance		C _{ISS}	V _{DS} =75V,V _{GS} =0V,f=1MHz		3947		pF
Output Capacitance		C _{OSS}			598		pF
Reverse Transfer Capacitance		C _{RSS}			17		pF
Switching Characteristics (Note 4)							
Turn-On Delay Time		t _{d(on)}	V _{DS} =75V,I _D =60A, V _{GS} =10V,R _G =3Ω		26		ns
Turn-On Rise Time		t _r			38		ns
Turn-Off Delay Time		t _{d(off)}			46		ns
Turn-Off Fall Time		t _f			25		ns
Total Gate Charge		Q _G	V _{DS} =75V,I _D =70A, V _{GS} =10V		56.8		nC
Gate-Source Charge		Q _{GS}			22		nC
Gate-Drain Charge		Q _{GD}			15		nC
Drain-Source Diode Characteristics and Maximum Ratings							
Drain-Source Diode Forward Voltage		V _{SD}	V _{GS} =0V,I _S =60A			1.2	V
Maximum Continuous Drain-Source Diode Forward Current(Note 3)		I _S				140	A
Reverse Recovery Time		t _{rr}	I _F =60A dI _F /dt=100A/μs		95		ns
Reverse Recovery Charge		Q _{RR}	I _F =60A dI _F /dt=100A/μs		157		nC

Notes: 1 Repetitive Rating:Pulse width limited by maximum junction temperature

2 $L=0.5\text{mH}$, $V_{DD}=50V, V_G=150V$, Starting $T_J=25^{\circ}\text{C}$

3 Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$

4 Guaranteed by design, not subject to production

Typical Characteristics Diagrams

Figure 1. Output Characteristics

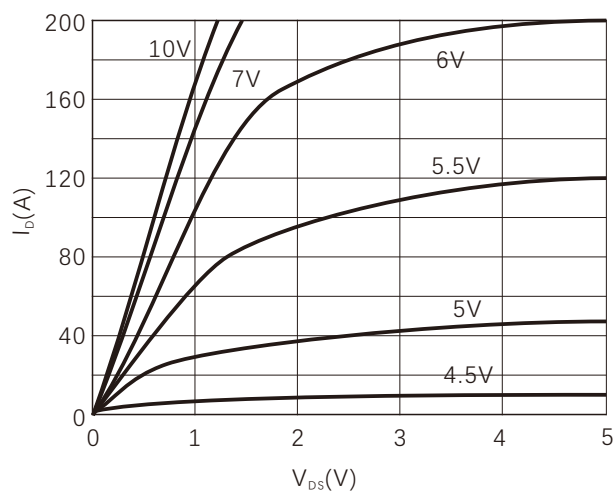


Figure 2. Transfer Characteristics

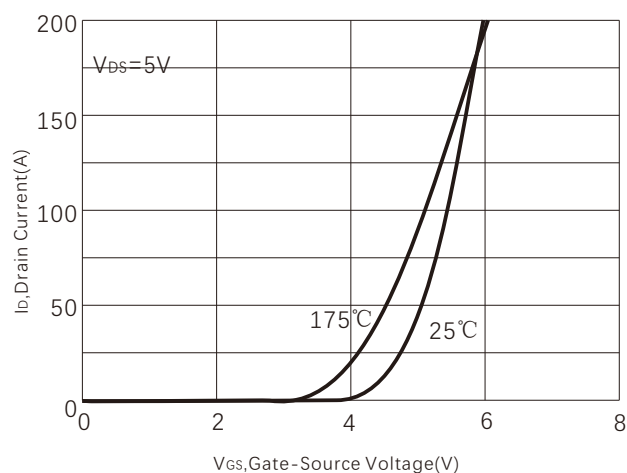


Figure 3. $R_{DS(ON)}$ vs Junction Temperature

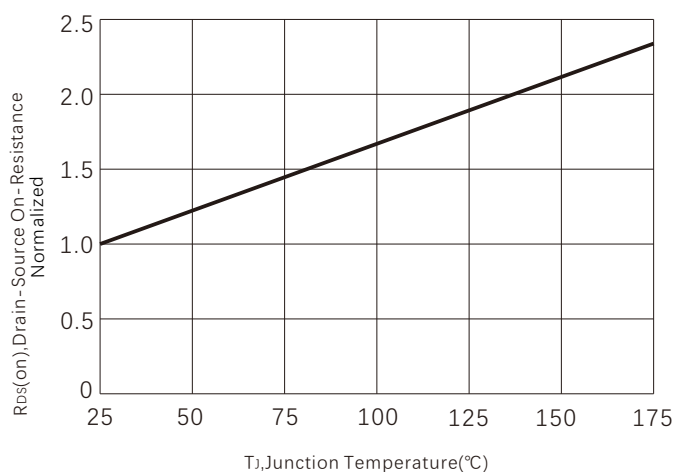


Figure 4. Capacitance

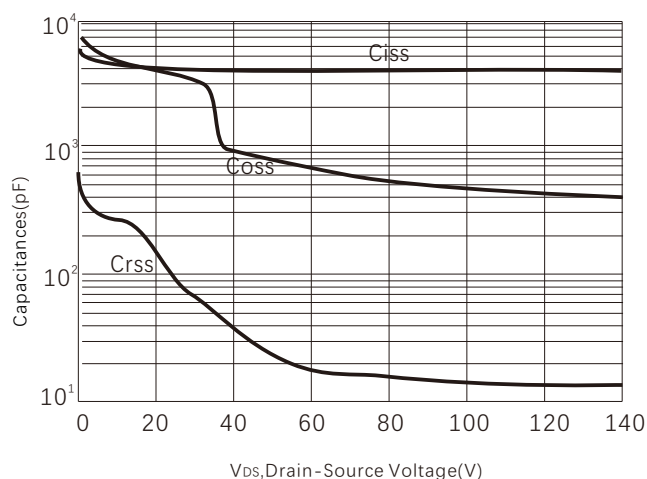


Figure 5. Gate charge

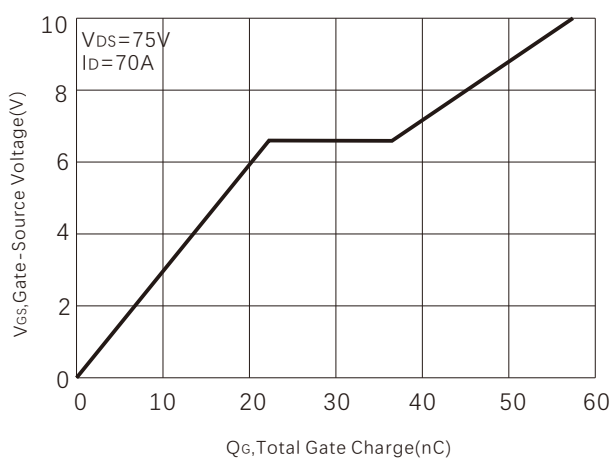


Figure 6. Source-Drain Diode Forward Voltage

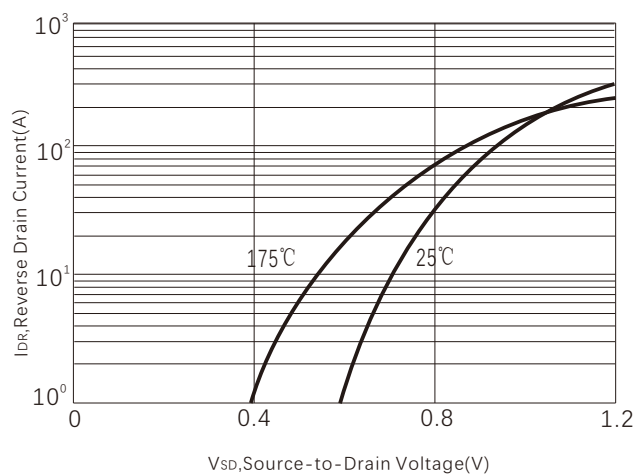


Figure 7. Power dissipation

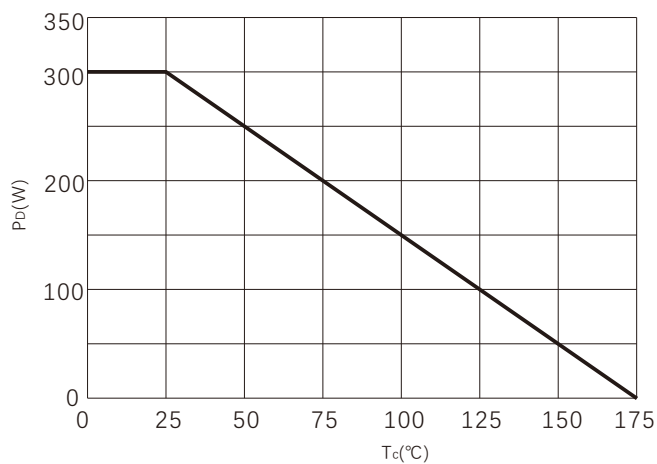


Figure8.Maximum Drain Current vs Temperature

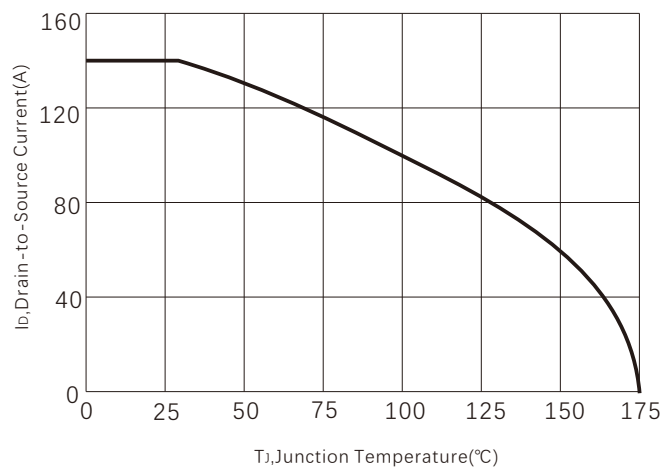


Figure 9.

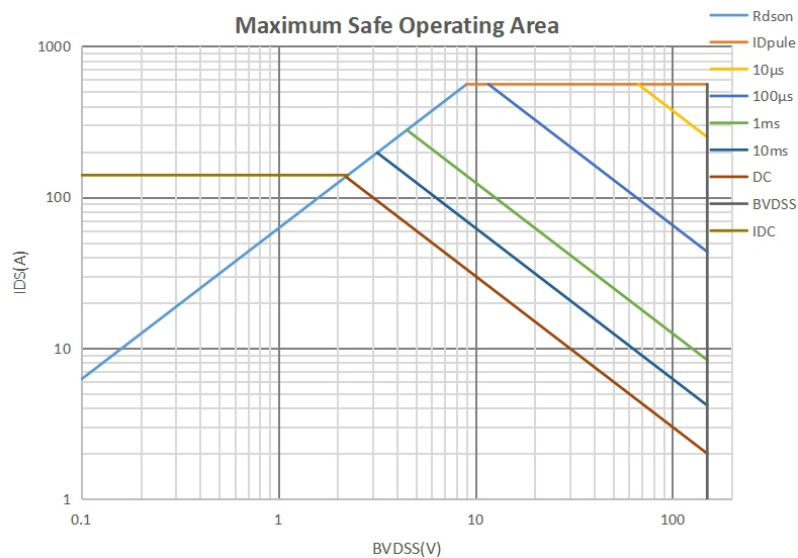
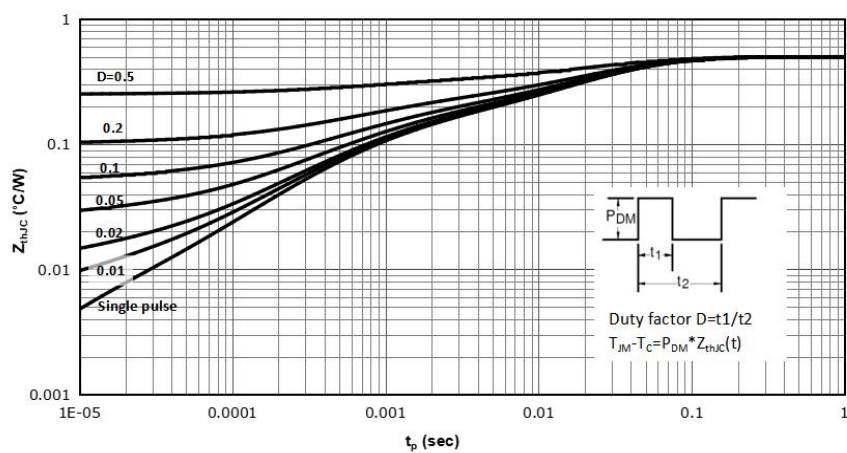
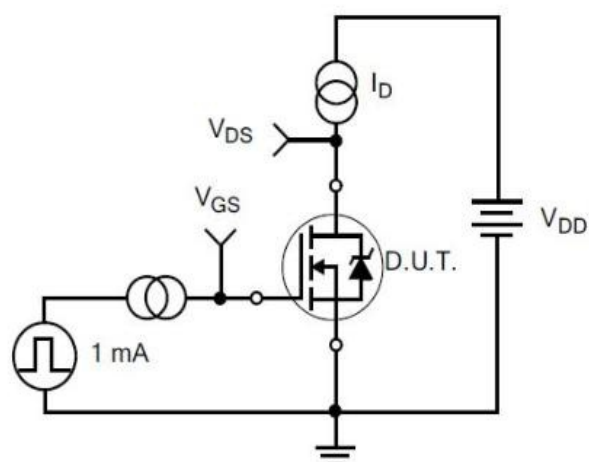


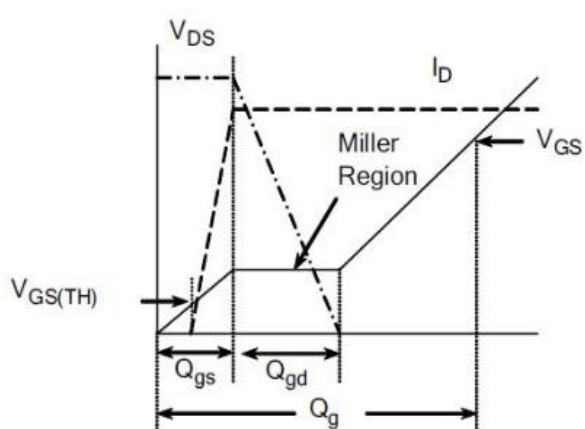
Figure 10. Maximum Transient Thermal Impedance



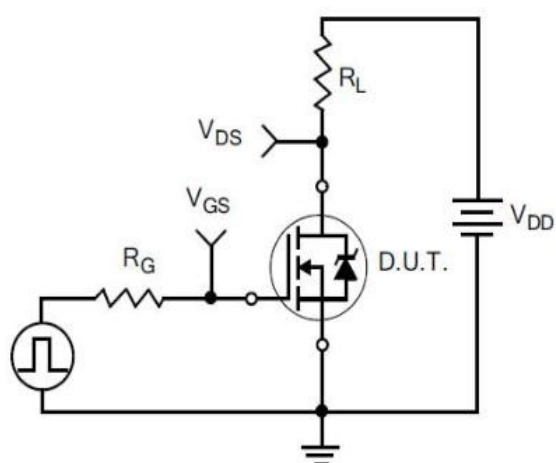
Typical Test Circuit



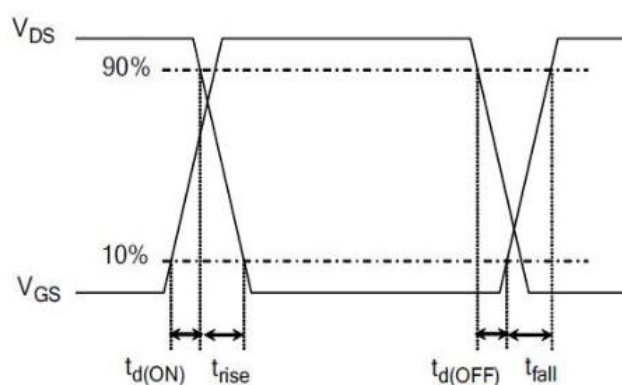
1) Gate Charge Test Circuit



2) Gate Charge Waveform

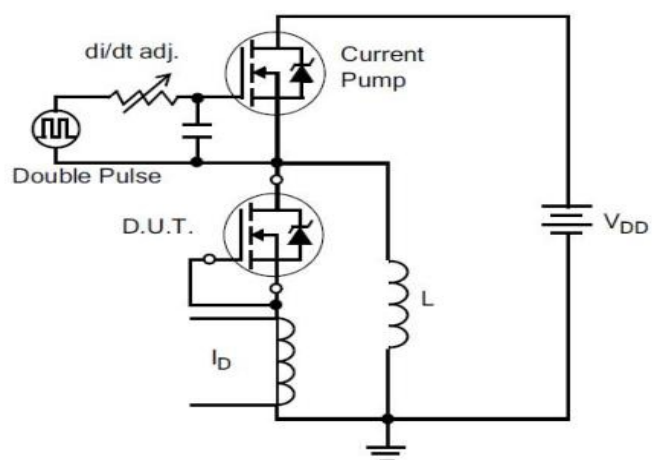


3) Resistive Switching Test Circuit

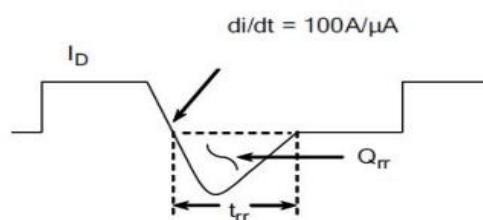


4) Resistive Switching Waveforms

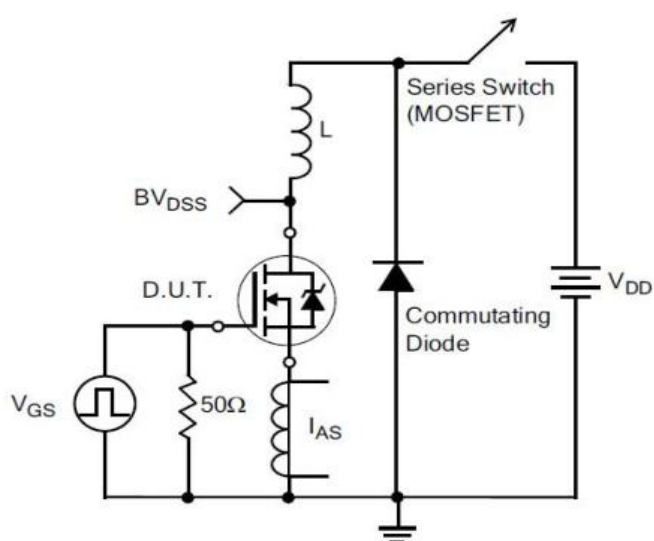
Typical Test Circuit



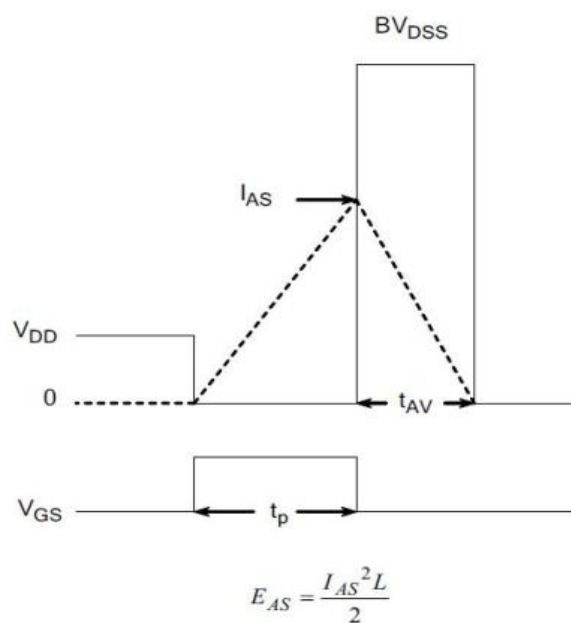
5) Diode Reverse Recovery Test Circuit



6) Diode Reverse Recovery Waveform

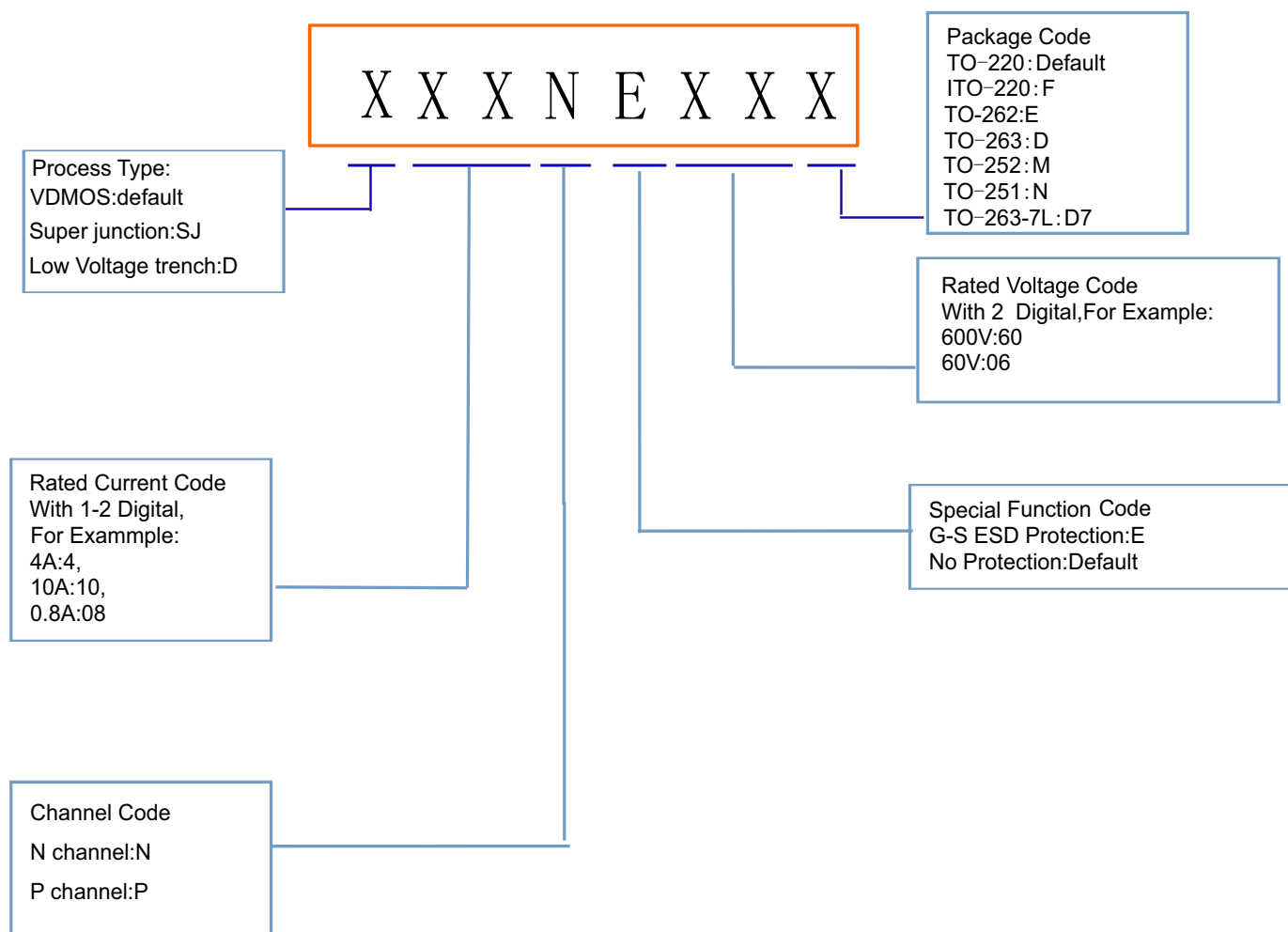


7) . Unclamped Inductive Switching Test Circuit



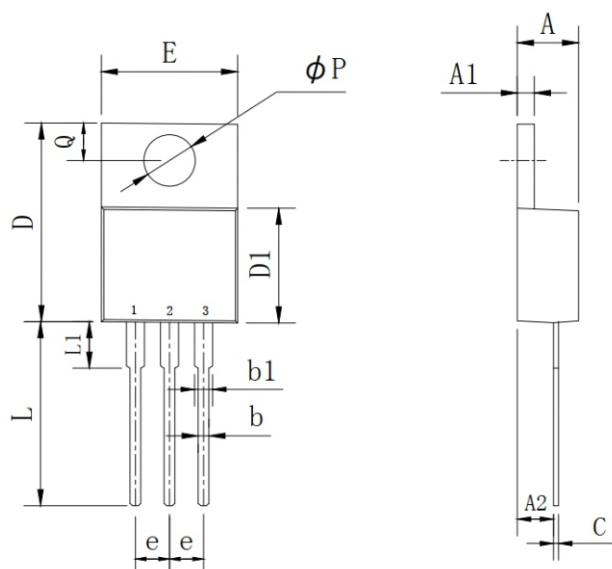
8) Unclamped Inductive Switching Waveforms

Product Names Rules



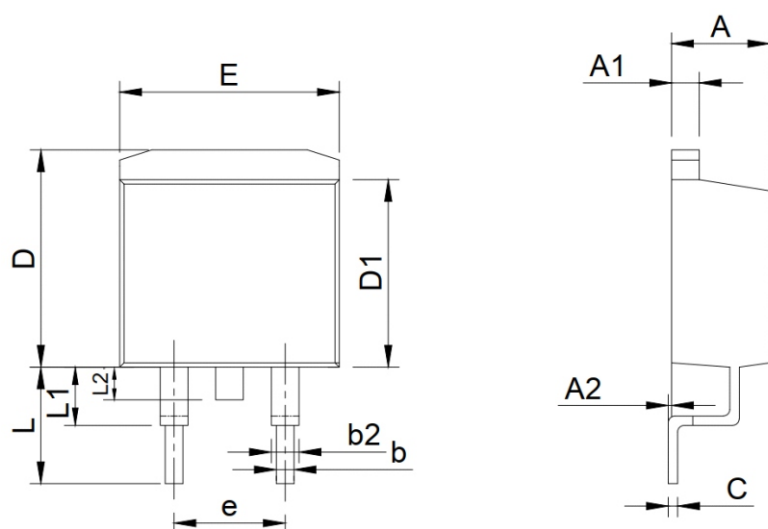
Dimensions

TO-220 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	4.25	4.87	0.167	0.192
A1	1.07	1.47	0.042	0.058
A2	2.03	2.92	0.080	0.115
b	0.51	1.11	0.020	0.044
b1	0.97	1.6	0.038	0.063
C	0.3	0.7	0.012	0.028
D	14.6	15.9	0.575	0.626
D1	8.04	9.3	0.317	0.366
E	9.57	10.57	0.377	0.416
e	2.34	2.74	0.092	0.108
L	12.58	14.3	0.495	0.563
L1	2.8	4.2	0.110	0.165
P	3.4	4.14	0.134	0.163
Q	2.45	3	0.096	0.118

TO-263 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	4.25	4.87	0.167	0.192
A1	1.07	1.47	0.042	0.058
A2	0	0.25	0.000	0.010
b	0.61	1.01	0.024	0.040
b1	1.2	1.34	0.047	0.053
C	0.3	0.6	0.012	0.024
D	9.48	10.84	0.373	0.427
D1	8.49	9.3	0.334	0.366
E	9.7	10.31	0.382	0.406
e	4.88	5.28	0.192	0.208
L	4.46	5.85	0.176	0.230
L1	1.33	2.33	0.052	0.092
L2	0	2.2	0.000	0.087

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