

DESCRIPTION

SiC Schottky Diode has no switching loss, provides improved system efficiency against Si diodes by utilizing new semiconductor material-Silicon Carbide, enables higher operating frequency, and helps increasing power density and reduction of system size /cost. Its high reliability ensures robust operation during surge or over_voltage conditions.

FEATURES

- Max Junction Temperature 175°C
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery/No Forward Recovery

MECHANICAL DATA

- Case: JEDEC TO-247AB
- Molding compound meets UL94V-0 flammability rating
- Terminals: Lead solderable per J-STD-002 and JESD22-B102
- Polarity: As marked
- Mounting Torque: 10 in.-lbs maximum

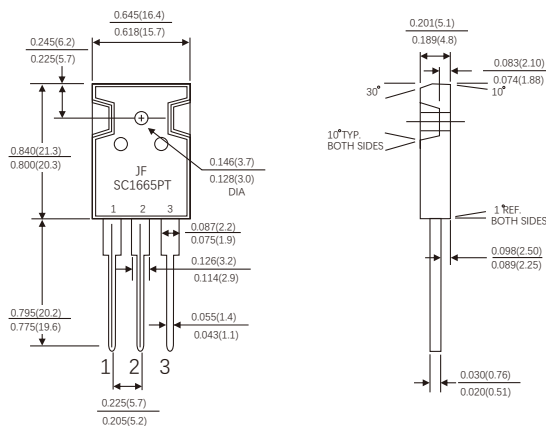
TYPICAL APPLICATIONS

- General Purpose
- SMPS, Solar inverter, UPS
- Power Switching Circuits

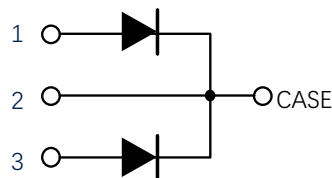
KEY PERFORMANCE AND PACKAGE PARAMETERS

Type	V _{DC}	I _F	Q _c	T _J ,max	Package
SC1665PT	650V	8A/16A	22/44nC	175°C	TO-247AB

TO-247AB



Dimensions in inches and (millimeters)



MAXIMUM RATINGS(Per Leg)

(Ratings at 25°C ambient temperature unless otherwise specified)

Parameter	Symbol	Value	Unit
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	650	V
Continuous Forward C urrent for $R_{th(j-c)}$	I_F	24($T_c \leq 25^\circ\text{C}$) 11($T_c \leq 135^\circ\text{C}$) 8($T_c \leq 153^\circ\text{C}$)	A
Non-Repetitive Forward Surge Current (Half-Sine Pulse , $t_p=8.3\text{ms}$)	I_{FSM}	50	A
Power dissipation for $R_{th(j-c,max)}$ ($T_c=25^\circ\text{C}$)	P_{tot}	107	W
Operating junction temperature range	T_j	-55...175	$^\circ\text{C}$
Storage temperature range	T_{stg}	-55...175	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Parameter	Symbol	Typ	Unit
Diode thermal resistance junction-case(Per Leg/device)	$R_{th(j-c)}$	1.40/0.70	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS (Per Leg)

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions
VF	Forward Voltage	1.4 1.7	1.65 2.3	V	$I_F=8A, T_J=25^{\circ}C$ $I_F=8A, T_J=175^{\circ}C$
IR	Reverse Current	- -	20 200	μA	$V_R=650V, T_J=25^{\circ}C$ $V_R=650V, T_J=175^{\circ}C$
C	Total Capacitance	520 50 41	/	pF	$V_R=0V, T_J=25^{\circ}C, f=1MHz$ $V_R=200V, T_J=25^{\circ}C, f=1MHz$ $V_R=400V, T_J=25^{\circ}C, f=1MHz$
QC	Total Capacitive Charge	22	/	nC	$V_R=650V, I_F=8A, di/dt=200A/\mu s, T_J=25^{\circ}C$

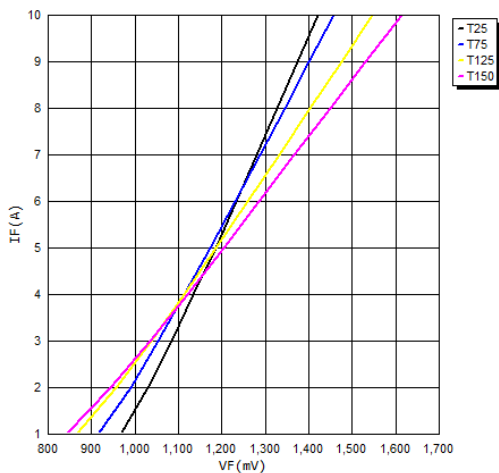


Figure 1. Forward Characteristics (Per Leg)

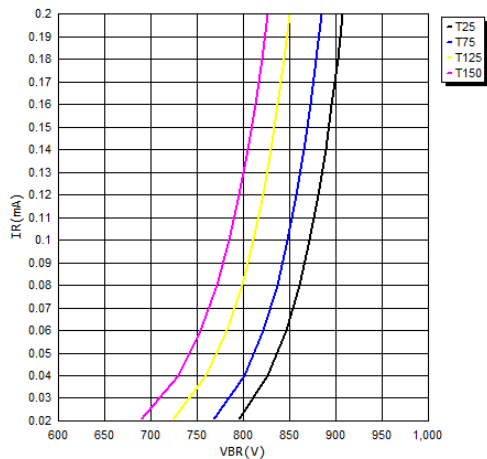


Figure 2. Reverse Characteristics (Per Leg)

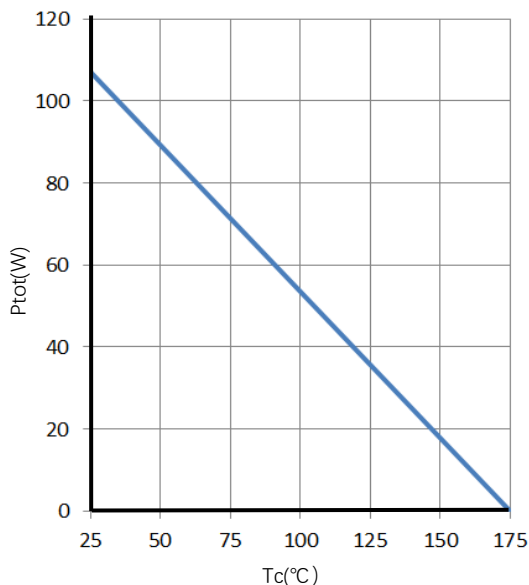


Figure 3. Power Derating (Per Leg)

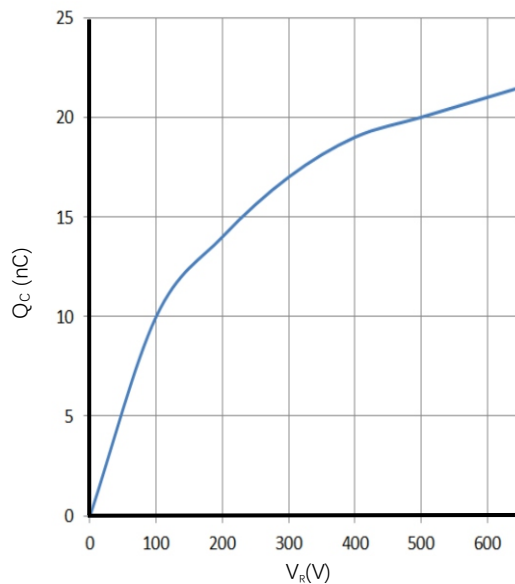


Figure 4. Total Capacitive Charge vs. Reverse Voltage (Per Leg)

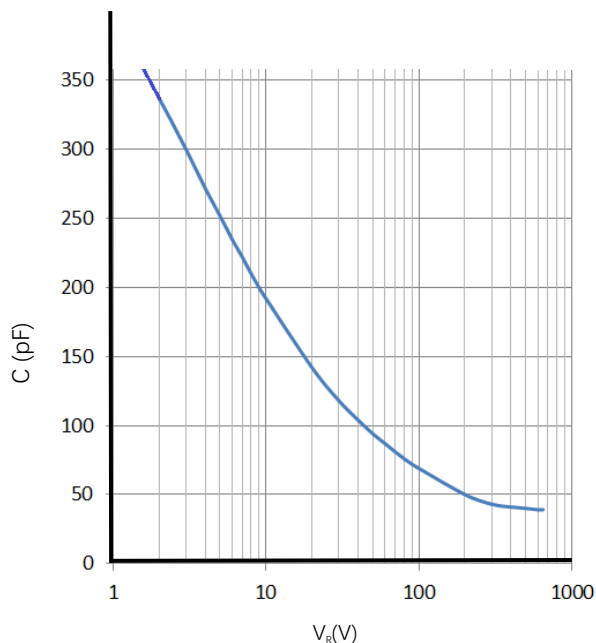


Figure 5. Total Capacitance vs. Reverse Voltage (Per Leg)

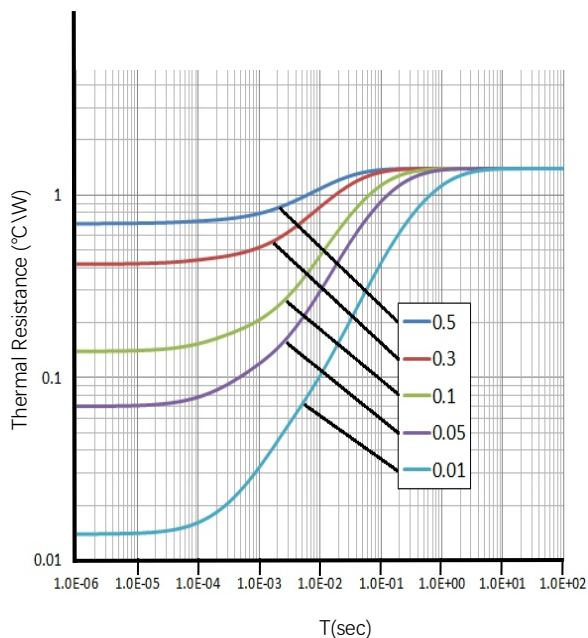


Figure 6. Transient Thermal Impedance (Per Leg)

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