

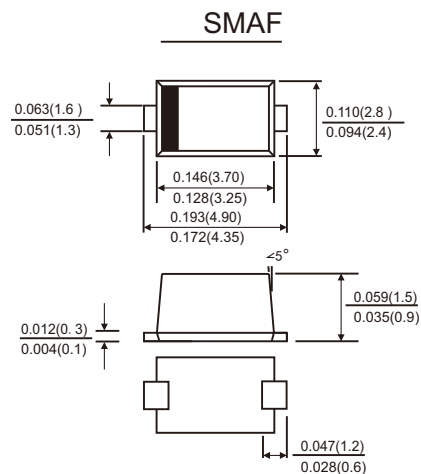
FEATURES

- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- Glass passivated junction
- Low forward voltage drop
- High current capability, High reliability
- Low power loss, high efficiency
- High surge current capability
- High speed switching, Low leakage
- High temperature soldering guaranteed:260°C/10 seconds at terminals
- Component in accordance to RoHS 2015/863/EU



MECHANICAL DATA

- Case: SMAF molded plastic body
- Terminals: Solder Plated, solderable per MIL-STD-750,method 2026
- Polarity: Color band denotes cathode end



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(Rating at 25°C ambient temperature unless otherwise specified. Single phase ,half wave ,60Hz,resistive or inductive load. For capacitive load, derate current by 20%.)

Parameter	Symbols	US2MS	Units
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	1000	Volts
Maximum RMS Voltage	V _{RMS}	700	Volts
Maximum DC Blocking Voltage	V _{DC}	1000	Volts
Maximum Average Forward Rectified Current	I _(AV)	2.0	Amp
Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load (JEDEC method)	I _{FSM}	50	Amps
Maximum Instantaneous Forward Voltage at 2.0 A	V _F	1.7	Volts
Maximum DC Reverse Current at rated DC blocking voltage	T _J =25°C	I _R	μA
	T _J =125°C	100	
Typical Thermal resistance	R _{θJA}	75	°C/W
	R _{θJL}	27	
Maximum reverse recovery time(Note1)	T _{rr}	75	ns
Typical junction capacitance(Note2)	C _J	11	pF
Operating junction and storage temperature range	T _J T _{STG}	-55 to+150	°C

Note: 1.Test conditions: I_F=0.5A,I_R=1.0A,I_{RR}=0.25A.

2.Measured at 1MHz and applied reverse voltage of 4.0 Volts.

FIG.1-TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC

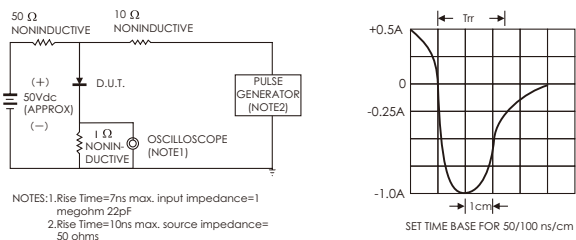


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

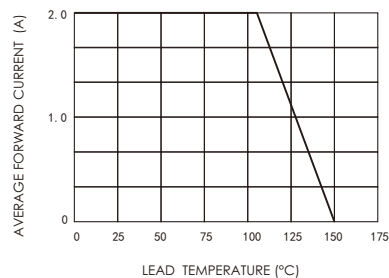


FIG.3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

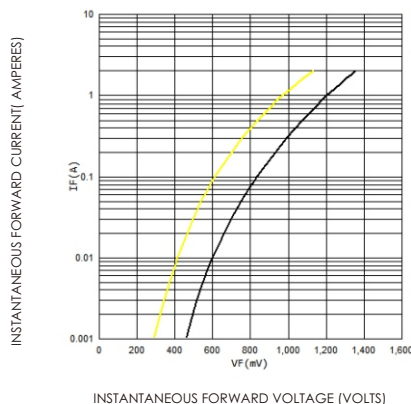


FIG.4-TYPICAL REVERSE CHARACTERISTICS

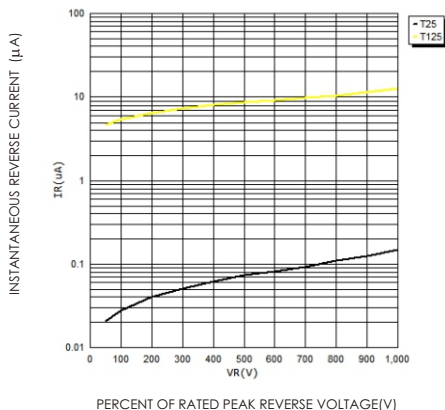


FIG.5-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

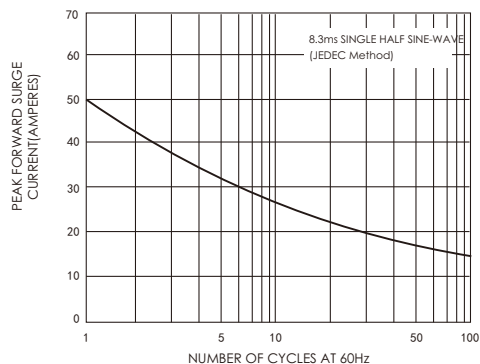
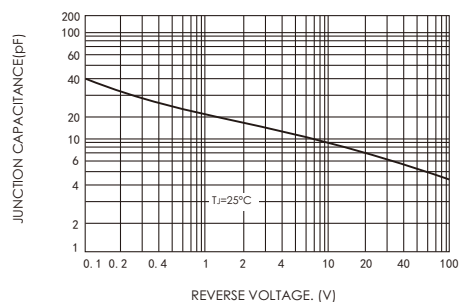


FIG.6-TYPICAL JUNCTION CAPACITANCE



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