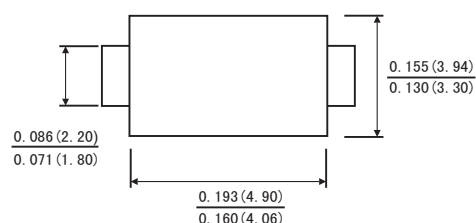


### FEATURES

- Glass passivated junction
- For Surface Mount Applications, Easy to pick and place
- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- High temperature soldering guaranteed: 260°C/10 seconds at terminals,
- Component in accordance to RoHS 2011/65/EU

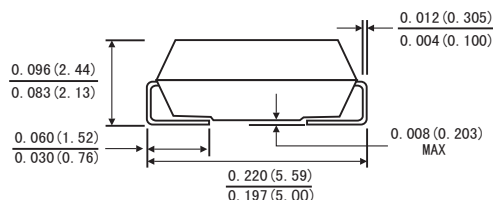


### SMB(DO-214AA)



### MECHANICAL DATA

- Case: JEDEC DO-214AA molded plastic over glass passivated chip
- Terminals: Solder plated
- Polarity: Color band denotes cathode end



### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Dimensions in inches and (millimeters)

(Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.)

		Symbols	RS2AB	RS2BB	RS2DB	RS2GB	RS2JB	RS2KB	RS2MB	Units
Maximum Recurrent Peak Reverse Voltage		V <sub>RRM</sub>	50	100	200	400	600	800	1000	Volts
Maximum RMS Voltage		V <sub>RMS</sub>	35	70	140	280	420	560	700	Volts
Maximum DC Blocking Voltage		V <sub>DC</sub>	50	100	200	400	600	800	1000	Volts
Maximum Average Forward Rectified Current		I(AV)	2.0							Amps
Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load (JEDEC method)		I <sub>FSM</sub>	50.0							Amps
Maximum Instantaneous Forward Voltage at 2.0 A		V <sub>F</sub>	1.3							Volts
Maximum DC Reverse Current	T <sub>A</sub> =25°C	I <sub>R</sub>	5.0							μA
at rated DC blocking voltage	T <sub>A</sub> =100°C		50							
Maximum reverse recovery time(Note1)		t <sub>rr</sub>	150				250	500		ns
Typical junction capacitance(Note2)		C <sub>J</sub>	50.0							pF
Operating junction and storage temperature range		T <sub>J</sub> T <sub>STG</sub>	-55 to+150							°C

Note: 1. Test conditions:  $I_F=0.5A$ ,  $I_R=1.0A$ ,  $I_{RR}=0.25A$ .

2. Measured at 1MHz and applied reverse voltage of 4.0 Volts D.C.

## RATINGS AND CHARACTERISTIC CURVES RS2AB THRU RS2MB

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

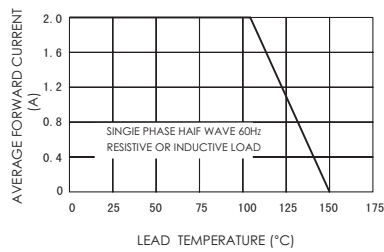


FIG.2-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

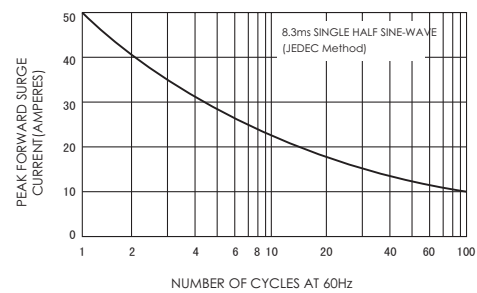


FIG.3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

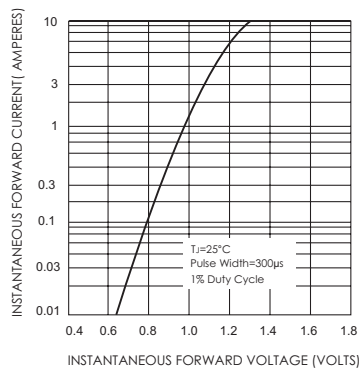


FIG.4-TYPICAL REVERSE CHARACTERISTICS

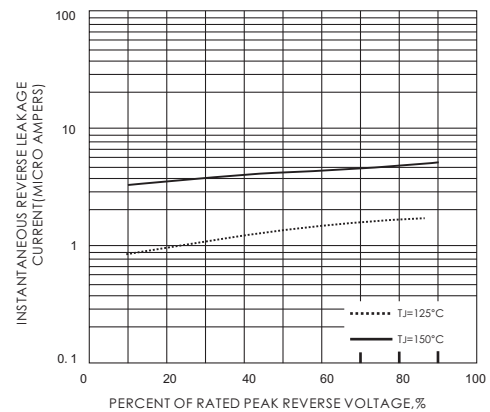


FIG.5-TYPICAL JUNCTION CAPACITANCE

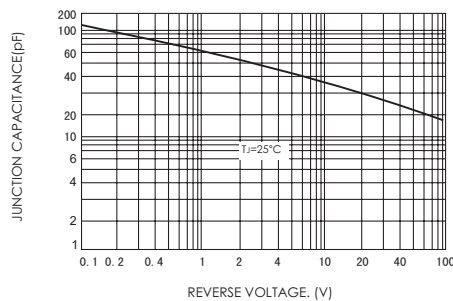


FIG.6-TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC

