

General Description

Power MOSFET is fabricated using advanced super junction technology. The resulting device has extremely low on resistance, making it especially suitable for applications which require superior power density and outstanding efficiency.

Features

- Low on-resistance
- Low gate charge and input capacitance
- 100% avalanche tested

Application

- Power factor correction (PFC)
- Switched mode power supplies (SMPS)
- Uninterruptible power supply (UPS)

Ordering Information

Part No.	Package Type	Package	Quantity(box)
SJ10N65M	TO-252	Tape & Reel	3000

Product Summary			
V _{DS}	R _{DS(on)} (Ω) Typ	I _D (A)	Q _g (Typ)
650V	0.55 @ 10V	4	13.1nc

TO-252

SJ10N65M



Pin Definition:

1. Gate
2. Drain
3. Source

Block Diagram

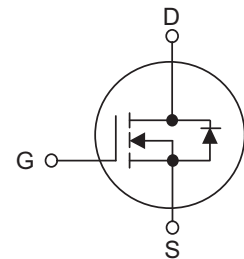


Table1 Absolute Maximum Ratings (T_C=25°C, unless otherwise specified)

Parameter	Symbol	SJ10N65M	Unit
Drain-Source Voltage	V _{DS}	650	V
Gate-Source Voltage	V _{GS}	±30	V
Continuous Drain Current	I _D	T _C =25°C	10
		T _C =100°C	4.4
Pulsed Drain Current (Note 1)	I _{DM}	21	A
Single Pulse Avalanche Energy(Note 2)	E _{AS}	120	mJ
Avalanche Current(Note 1)	I _{AR}	10	A
Power Dissipation T _C =25°C	P _D	63	W
Operating Junction and Storage Temperature	T _J /T _{STG}	-55 ~ +150	°C

Table 2. Thermal Characteristics

Parameter	Symbol	SJ10N65M	Unit
Thermal resistance Junction to Ambient	$R_{\theta JA}$	62.5	C/W
Thermal resistance Junction to Case	$R_{\theta JC}$	2	C/W

Table 3. Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise specified)

Parameter		Symbol	Test Conditions	Min	Typ	Max	Unit
Off Characteristics							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V,I _D =250μA	650			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =650V,V _{GS} =0V			1	μA
Gate- Source Leakage Current	Forward	I _{GSS}	V _{GS} =30V,V _{DS} =0V			50	nA
	Reverse		V _{GS} =-30V,V _{DS} =0V			-50	nA
On Characteristics(Note 4)							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} ,I _D =250μA	2.5	3.5	4.5	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V,I _D =4A		0.55	0.65	Ω
Dynamic Characteristics(Note 5)							
Input Capacitance		C _{ISS}	V _{DS} =25V,V _{GS} =0V,f=1MHz		480		pF
Output Capacitance		C _{OSS}			325		pF
Reverse Transfer Capacitance		C _{RSS}			0.95		pF
Switching Characteristics (Note 5)							
Turn-On Delay Time		t _{d(on)}	V _{DD} =300V,I _D =4A, V _{GS} =15V,R _G =10Ω		12		ns
Turn-On Rise Time		t _r			6.13		ns
Turn-Off Delay Time		t _{d(off)}			26		ns
Turn-Off Fall Time		t _f			3.3		ns
Total Gate Charge		Q _G	V _{DD} =480V,I _D =4A, V _{GS} =10V		13.1		nC
Gate-Source Charge		Q _{GS}			4.2		nC
Gate-Drain Charge		Q _{GD}			4.0		nC
Drain-Source Diode Characteristics and Maximum Ratings							
Drain-Source Diode Forward Voltage		V _{SD}	V _{GS} =0V, I _S =4A		0.85		V
Maximum Continuous Drain-Source Diode Forward Current		I _S				10	A
Reverse Recovery Time		trr	V _R =50V, I _F =4A		200		ns
Reverse Recovery Charge		Q _{RR}	dI _F /dt=100A/μs (Note 1)		1600		nC

Notes : 1 Repetitive Rating:Pulse width limited by maximum junction temperature

2 $I_{AS}=2A, V_{DD}=50V$, Starting $T_J=25^\circ\text{C}$

4 Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$

5 Guaranteed by design,not subject to production

Typical Characteristics Diagrams

Figure 1. On-Region Characteristics

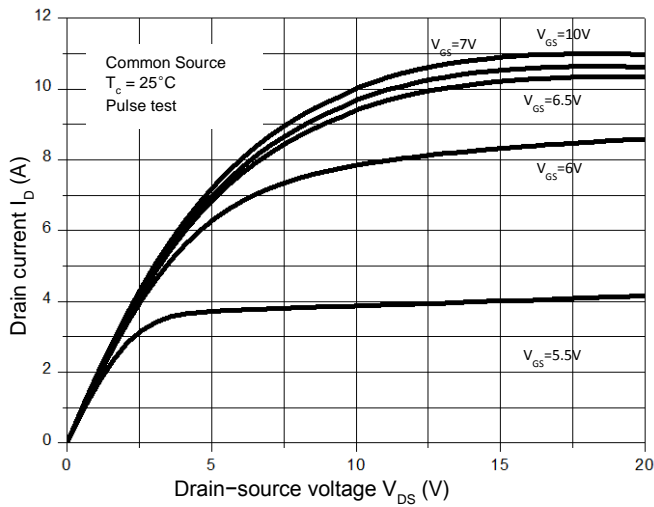


Figure 2. Transfer Characteristics

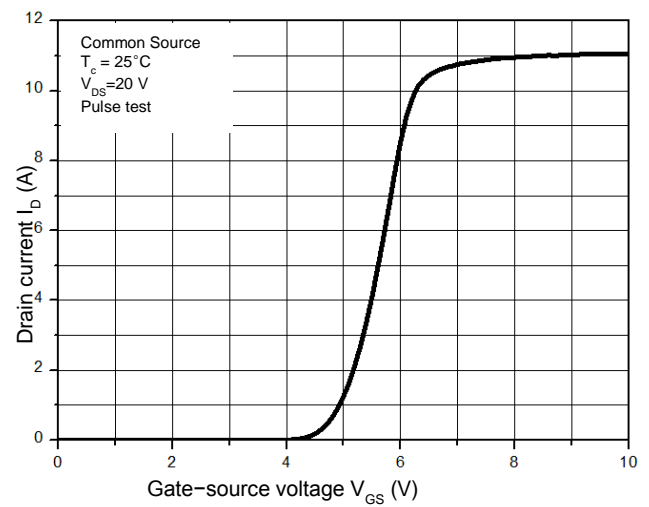


Figure 3. On-Resistance Variation vs. Drain Current

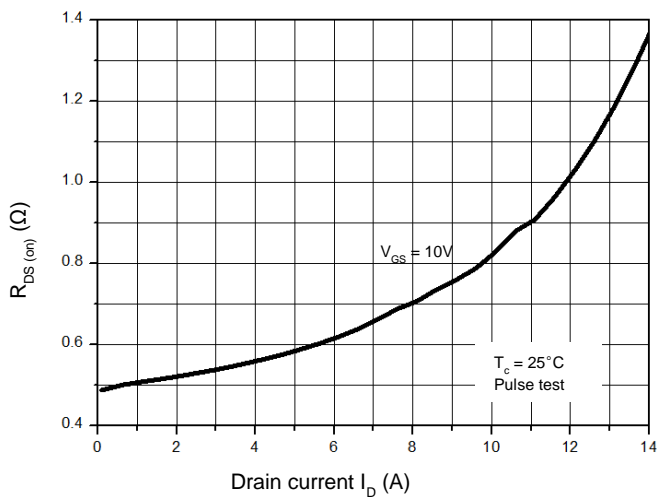


Figure 4. Threshold Voltage vs. Temperature

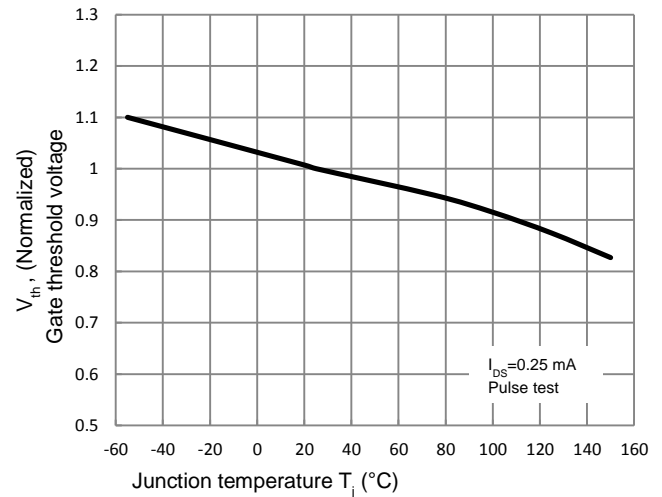


Figure 5. Breakdown Voltage vs. Temperature

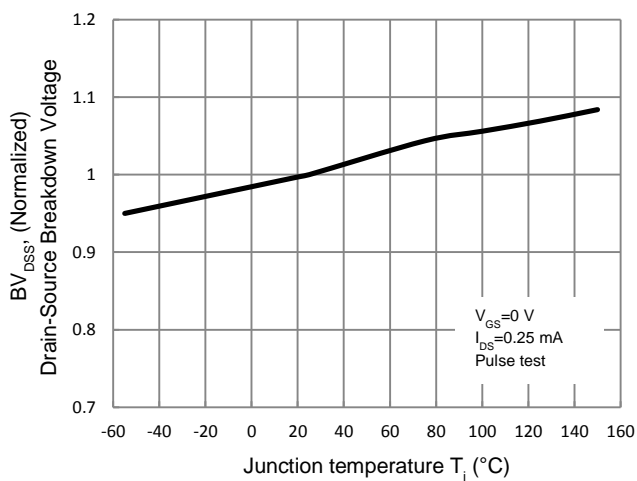
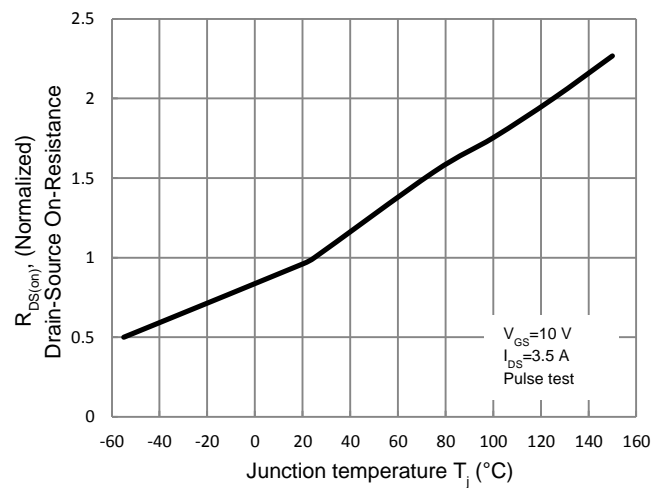


Figure 6. On-Resistance vs. Temperature



Typical Characteristics Diagrams

Figure 7. Capacitance Characteristics

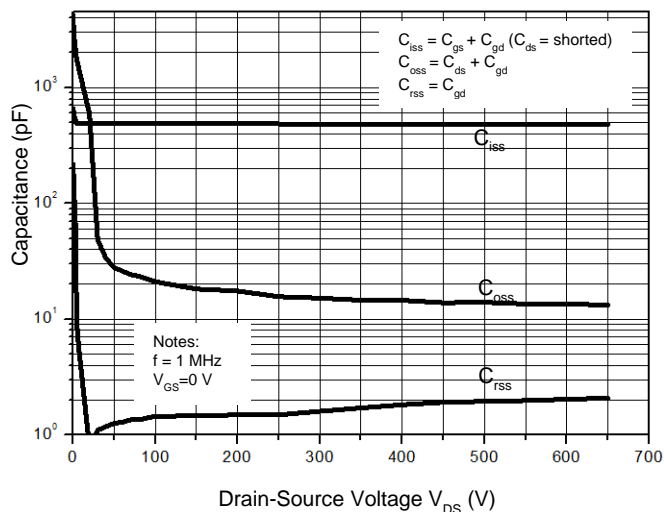


Figure 8. Gate Charge Characterist

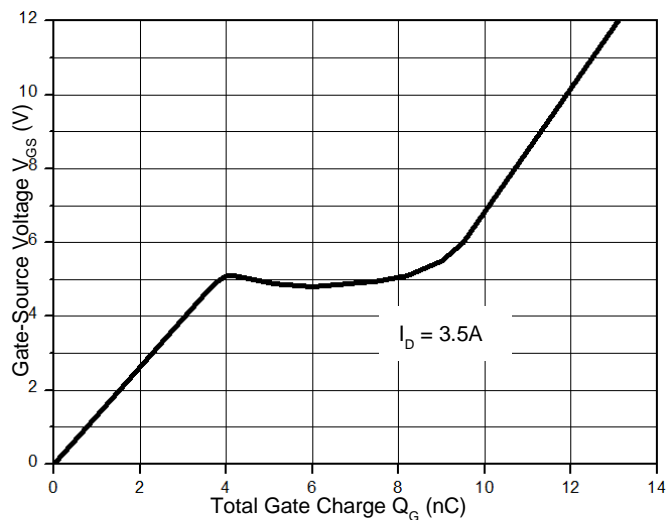


Figure 9. Maximum Safe Operating Area

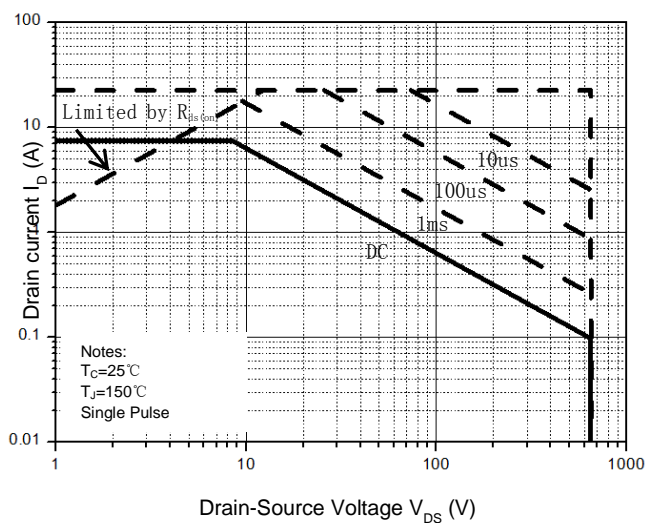
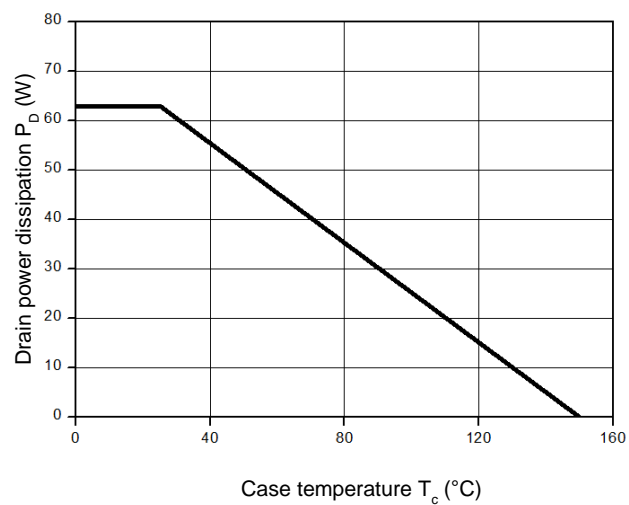
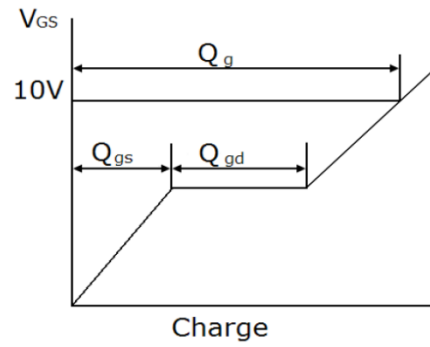
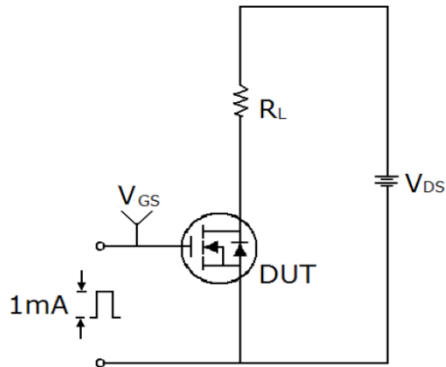


Figure 10. Power Dissipation vs. Temperature

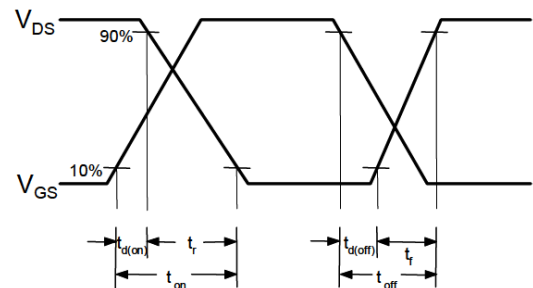
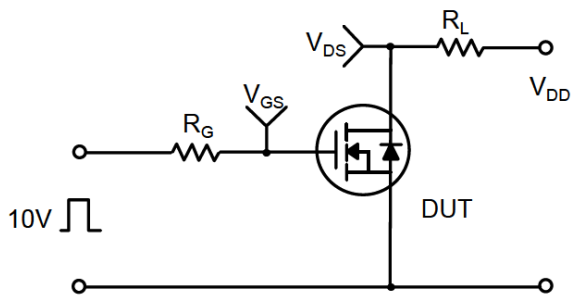


Typical Test Circuit

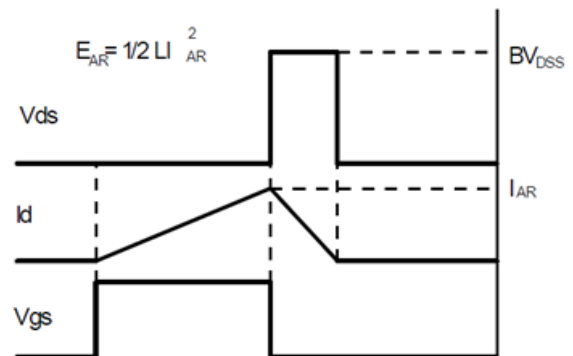
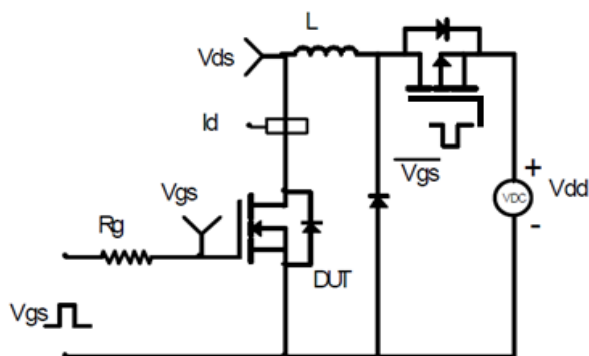
Gate Charge Test Circuit & Waveform



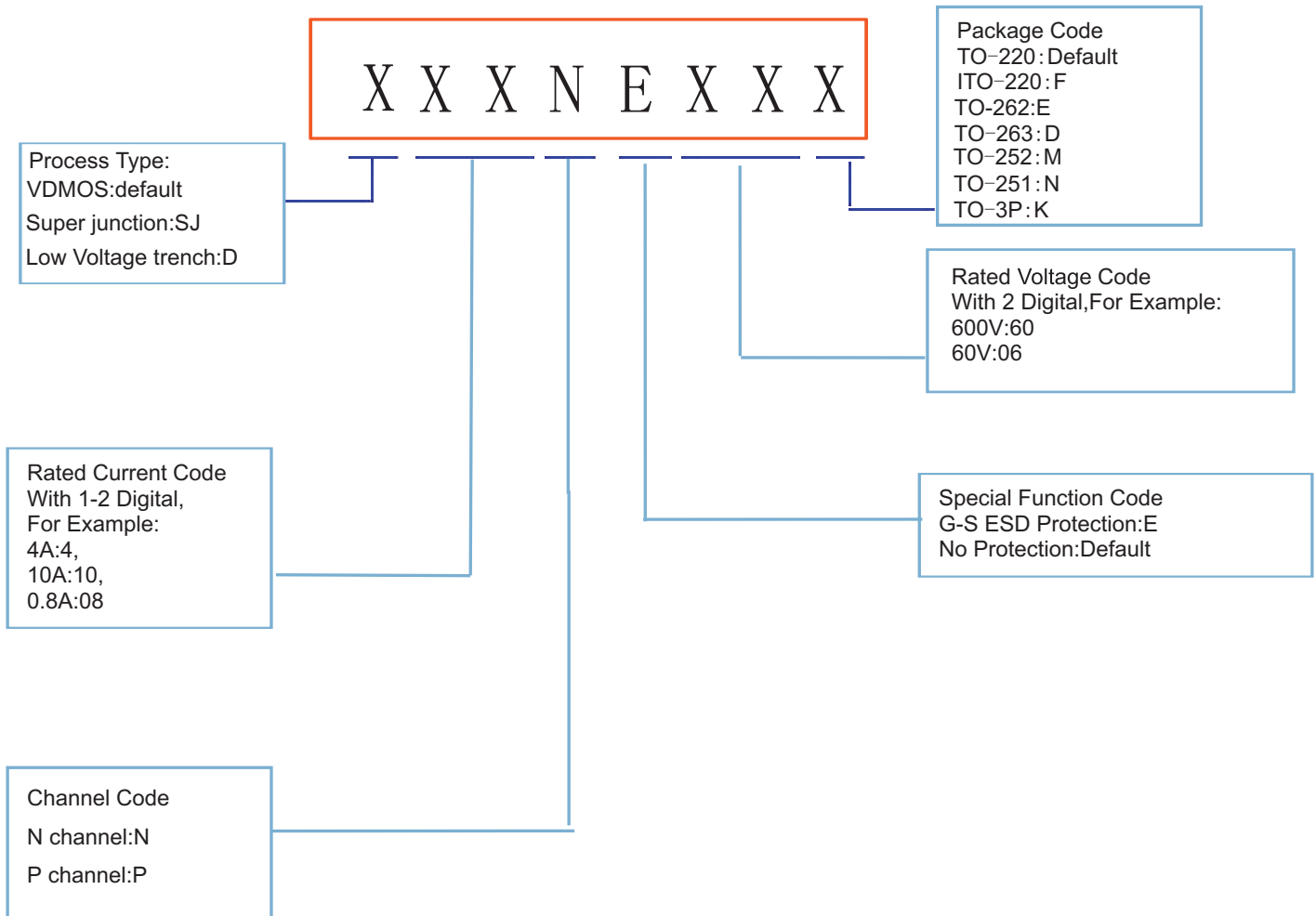
Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

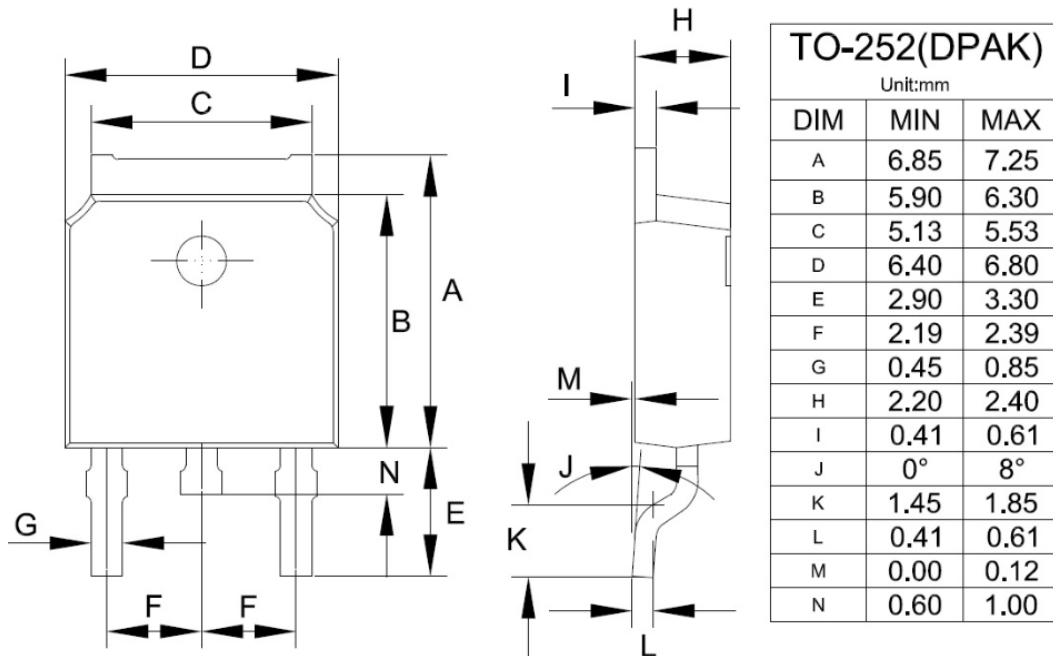


Product Names Rules



Dimensions

TO-252 Mechanical Drawing



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