

### Features

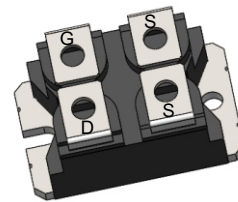
- Fast switching
- High input impedance
- Fully Avalanche Rated
- Isolation Type Package
- Electrically Isolation Base Plate

Product Summary			
V <sub>DS</sub>	R <sub>DS(on)</sub> (mΩ)Typ	I <sub>D</sub> (A)	Q <sub>g</sub> (Max)
200V	7.5 @ 10V 100A	220	335nc

SOT-227

### Mechanical Data

- Case:SOT-227 Package



### Application

- Synchronous Rectification
- Backlighting
- Battery Chargers
- Power Converters
- AC Motor Drives

### Block Diagram

Pin Definition:

- G. Gate
- D. Drain
- S. Source

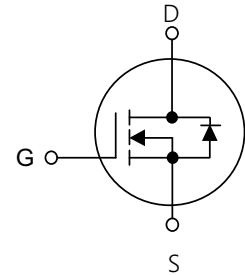


Table1 Absolute Maximum Ratings (T<sub>c</sub>=25°C, unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DS</sub>	200	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current	I <sub>D</sub>	T <sub>c</sub> =25°C	220 *
		T <sub>c</sub> =100°C	180 *
Pulsed Drain Current (Note 1)	I <sub>DM</sub>	760	A
Power Dissipation T <sub>c</sub> =25°C	P <sub>D</sub>	560	W
Isolation Voltage (A.C. 1 minute)	V <sub>ISO</sub>	2500	V
Operating Junction and Storage Temperature	T <sub>J</sub> /T <sub>STG</sub>	-55~+150	°C

Table 3. Electrical Characteristics ( $T_j=25^{\circ}\text{C}$ , unless otherwise specified)

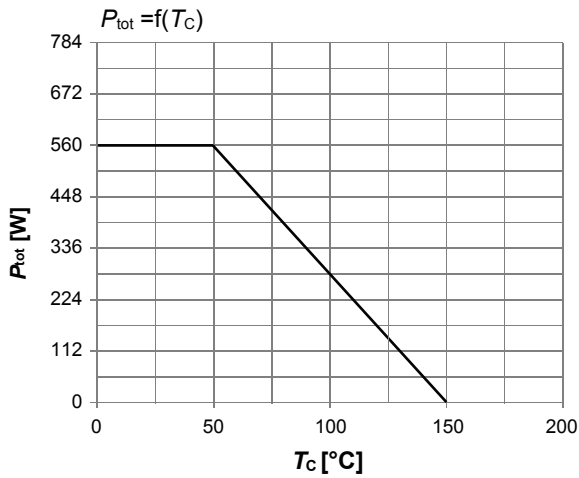
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=3mA$	200	-	-	V	
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=200V, V_{GS}=0V$	-	-	50	$\mu A$	
Gate- Source Leakage Current	Forward	$I_{GSS}$	$V_{GS}=20V, V_{DS}=0V$	-	-	200	nA
	Reverse	$I_{GSS}$	$V_{GS}=-20V, V_{DS}=0V$	-	-	-200	nA
On Characteristics							
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=8mA$	2.0	2.5	3.5	V	
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=100A$	-	7.5	7.8	$m\Omega$	
Gate Resisitance	$R_G$	$V_{DD}=0V, V_{GS}=0V, f=1MHz$	-	1.6	2.9	$\Omega$	
Dynamic Characteristics							
Input Capacitance	$C_{ISS}$	$V_{DS}=25V, V_{GS}=0V, f=1MHz$	-	46.68	-	nF	
Output Capacitance	$C_{OSS}$		-	993	-	pF	
Reverse Transfer Capacitance	$C_{RSS}$		-	586	-	pF	
Transconductance	$g_{fs}$	$ V_{DS}  > 2 I_D R_{DS(ON)M}, I_D = 100A$	-	120	-	S	
Switching Characteristics							
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=75V, I_D=150A$ $V_{GS}=10V, R_G=1.6\Omega,$	-	112	-	ns	
Turn-On Rise Time	$t_r$		-	44	-	ns	
Turn-Off Delay Time	$t_{d(off)}$		-	248	-	ns	
Turn-Off Fall Time	$t_f$		-	48	-	ns	
Total Gate Charge	$Q_G$	$V_{DS}=10V, I_D=100A,$ $V_{GS}=10V$	-	335	-	nC	
Gate-Source Charge	$Q_{GS}$		-	188	-	nC	
Gate-Drain Charge	$Q_{GD}$		-	115	-	nC	
Drain-Source Diode Characteristics and Maximum Ratings(Note 2)							
Drain-Source Diode Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_S=100A$	-	-	0.85	V	
Maximum Continuous Drain-Source Diode Forward Current	$I_S$		-	-	180	A	
Reverse Recovery Time	$t_{rr}$	$I_F=0.5A, I_R=1.0A$ $I_{RR}=0.25A$	-	-	300	ns	

Notes: 1 Repetitive Rating Pulse width limited by maximum junction temperature

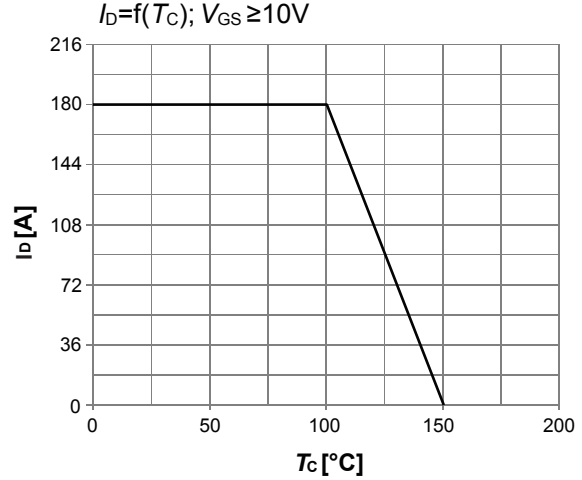
2 Pulse Test: Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$

Typical Characteristics Diagrams

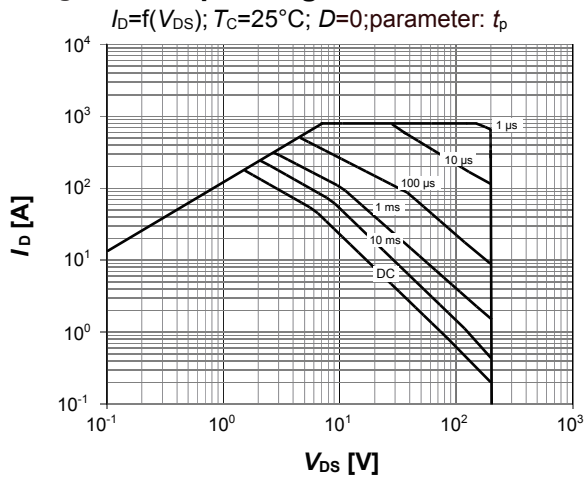
**Fig 1. Power dissipation**



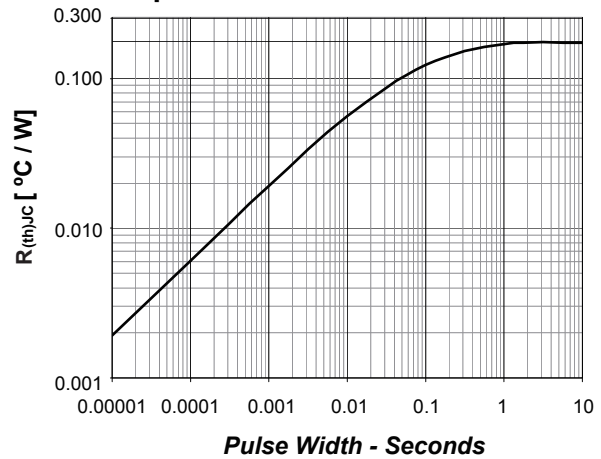
**Fig 2. Drain current**



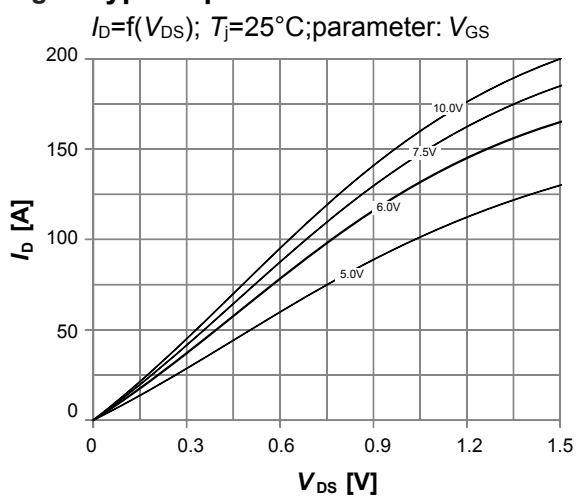
**Fig 3. Safe operating area**



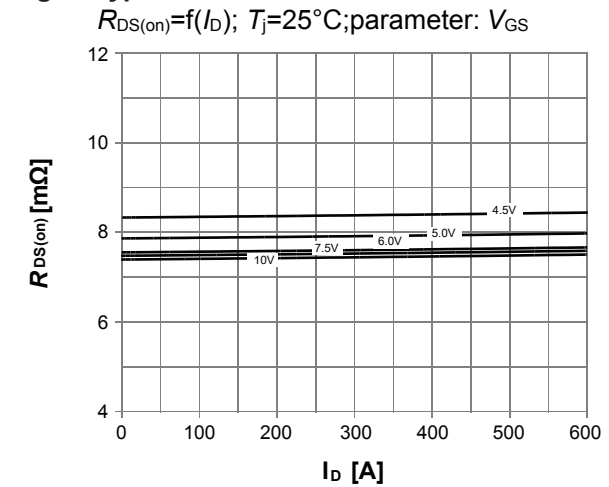
**Fig 4. Maximum Transient Thermal Impedance**



**Fig 5. Typ. output characteristics**

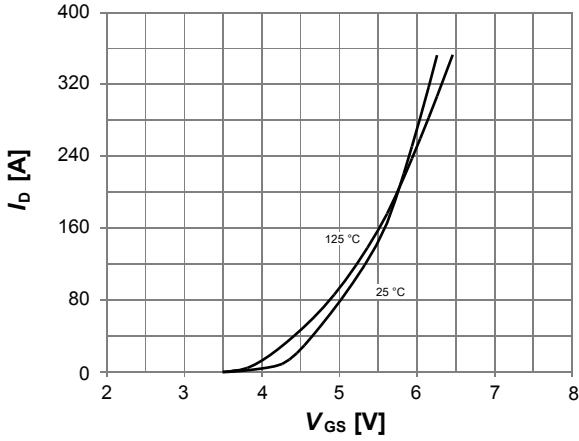


**Fig 6. Typ. drain-source on resistance**



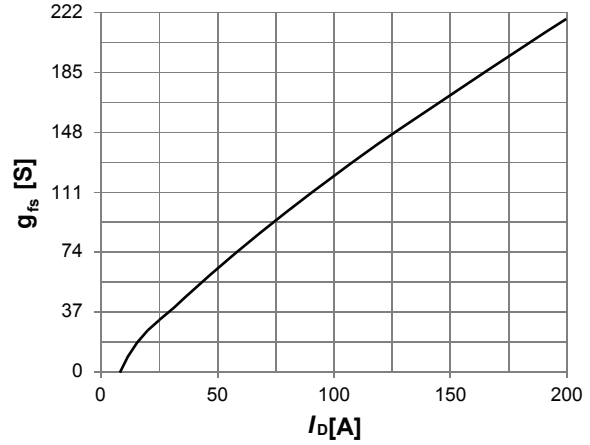
**Fig 7. Typ. transfer characteristics**

$I_D = f(V_{GS}); |V_{DS}| > 2|I_D| R_{DS(on)max}; \text{parameter: } T_j$



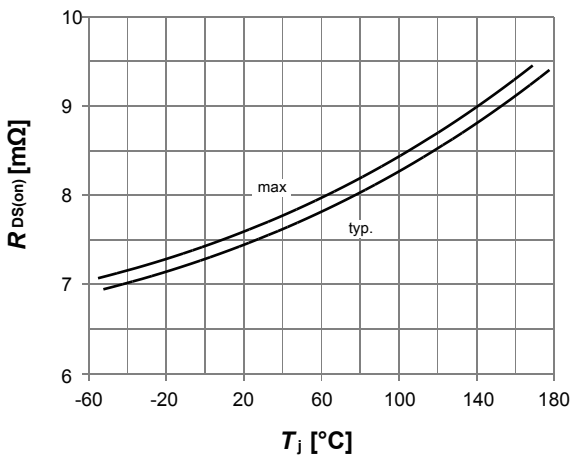
**Fig 8. Typ. forward transconductance**

$g_{fs} = f(I_D); T_j = 25^\circ\text{C}$



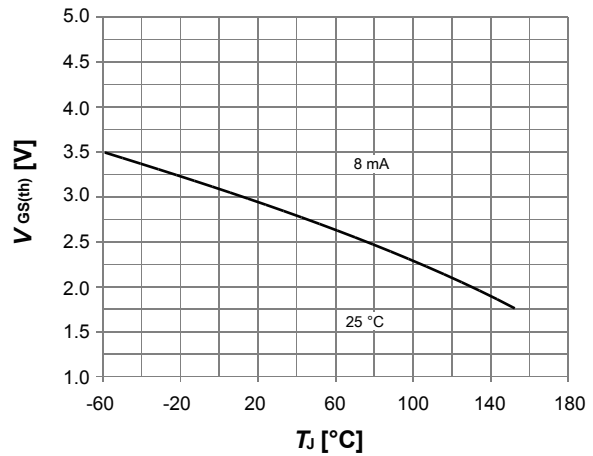
**Fig 9. Drain-source on-state resistance**

$R_{DS(on)} = f(T_j); I_D = 150\text{A}; V_{GS} = 10\text{V}$



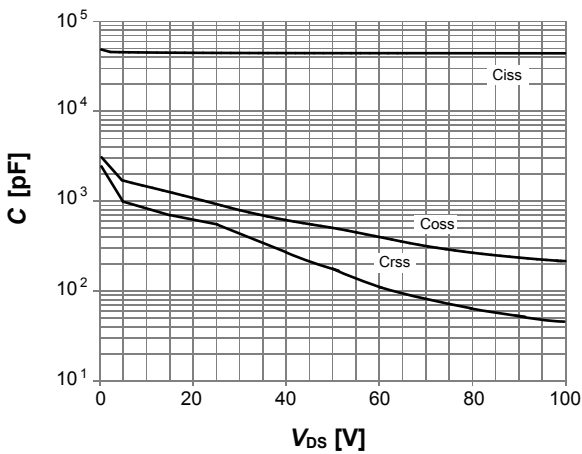
**Fig 10. Typ. gate threshold voltage**

$V_{GS(th)} = f(T_j); V_{GS} = V_{DS}; \text{parameter: } I_D$

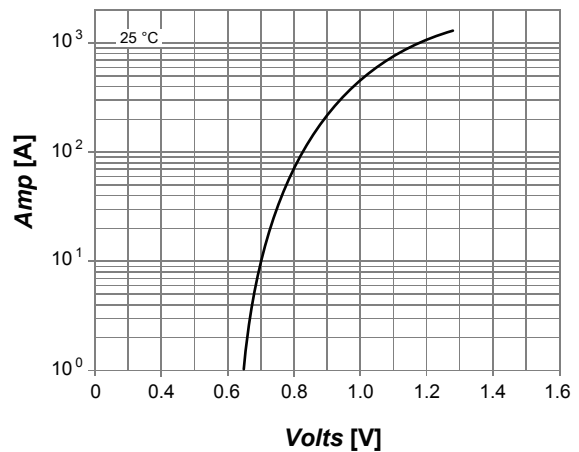


**Fig 11. Typ. capacitances**

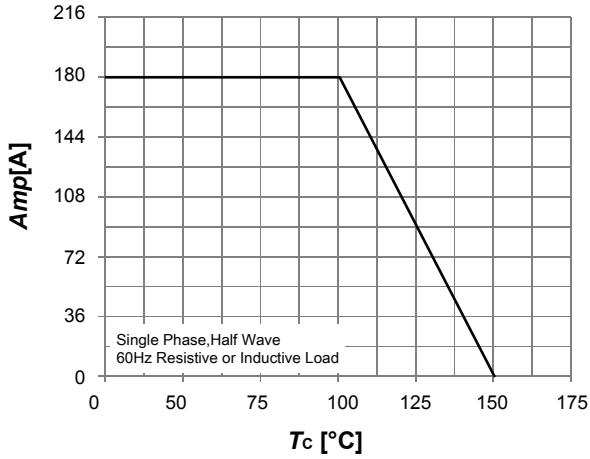
$C = f(V_{DS}); V_{GS} = 0\text{V}; f = 1\text{MHz}$



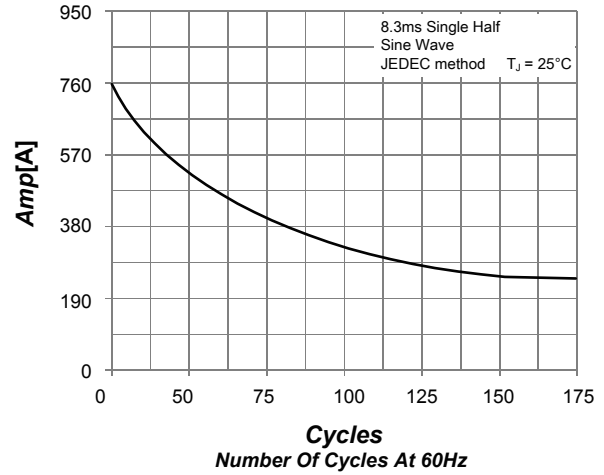
**Fig 12. Typical forward characteristics of reverse diode**



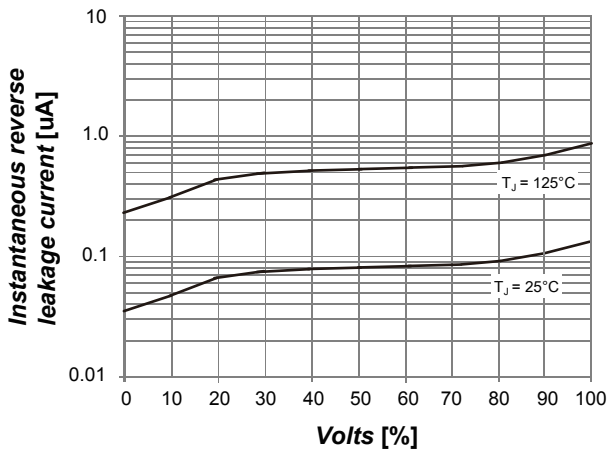
**Fig 13. Forward derating curve of reverse diode**



**Fig 14. Peak forward surge current of reverse diode**

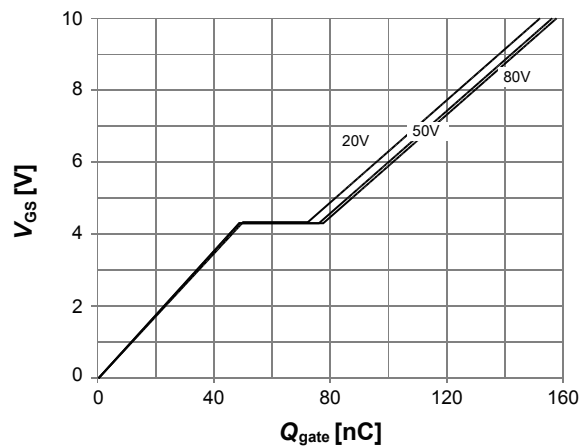


**Fig 15. Typical reverse diode characteristics**

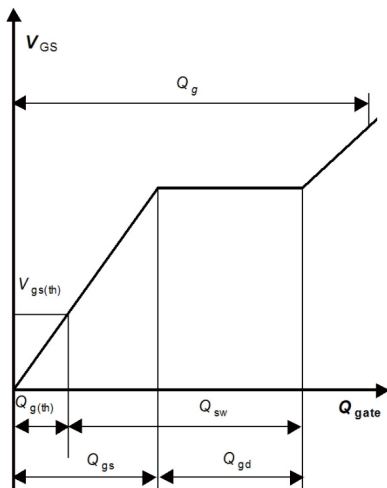


**Fig 16. Typ. gate charge**

$V_{GS}=f(Q_{gate})$ ;  $I_D=100A$  pulsed; parameter:  $V_{DD}$

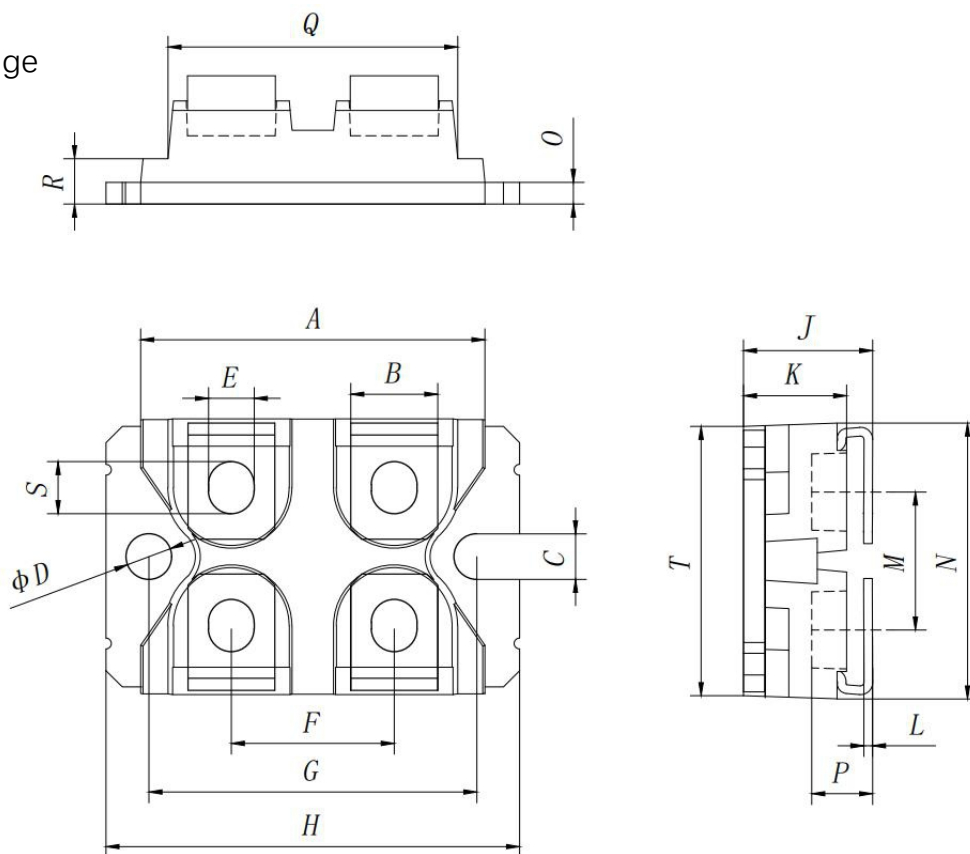


**Fig 17. Gate charge waveforms**



Dimensions

SOT-227package



SYMBOLS	DIMENSION IN MM		
	MIN	NOM	MAX
<i>A</i>	31.20	31.70	32.20
<i>B</i>	7.50	8.00	8.50
<i>C</i>	3.80	4.20	4.60
<i>D</i>	3.80	4.20	4.60
<i>E</i>	3.80	4.20	4.60
<i>F</i>	14.50	15.00	15.50
<i>G</i>	29.80	30.20	30.60
<i>H</i>	37.70	38.10	38.50
<i>J</i>	11.50	11.90	12.30
<i>K</i>	8.90	9.50	10.00
<i>L</i>	0.75	0.80	0.85
<i>M</i>	12.40	12.70	13.00
<i>N</i>	25.00	25.40	25.80
<i>O</i>	1.70	2.00	2.30
<i>P</i>	4.95	5.60	6.10
<i>Q</i>	26.40	26.70	27.00
<i>R</i>	3.90	4.18	4.45
<i>S</i>	4.20	4.80	5.40
<i>T</i>	23.80	24.80	25.80

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