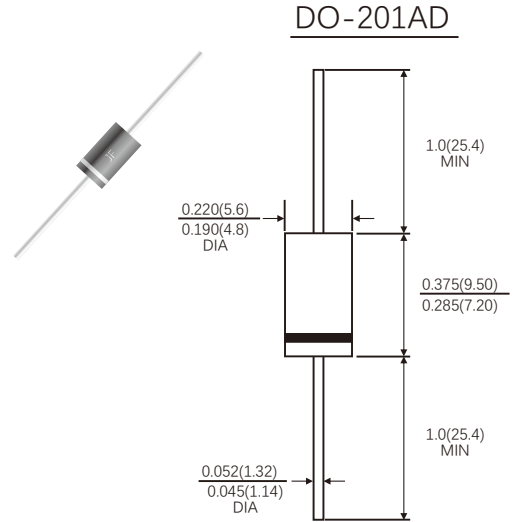


FEATURES

- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- Low forward voltage drop
- High current capability, High reliability
- Low power loss, high efficiency
- High surge current capability
- High speed switching, Low leakage
- High temperature soldering guaranteed: 260°C/10 seconds at terminals
- Component in accordance to RoHS 2015/863/EU

MECHANICAL DATA

- Case: JEDEC DO-201AD molded plastic body
- Epoxy: UL94V-0 rate flame retardant
- Lead: Plated axial leads, solderable per MIL-STD-750, method 2026
- Polarity: Color band denotes cathode end
- Mounting Position: Any
- Weight: 0.042ounce, 1.19 grams



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.)

Parameters	Symbols	Value	Units
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	400	Volts
Maximum RMS Voltage	V_{RMS}	280	Volts
Maximum DC Blocking Voltage	V_{DC}	400	Volts
Maximum Average Forward Rectified Current 0.375"(9.5mm) lead length	$I(AV)$	10.0	Amps
Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load (JEDEC method)	I_{FSM}	120	Amps
Maximum Instantaneous Forward Voltage at 10A	V_F	1.30	Volts
Maximum DC Reverse Current at rated DC blocking voltage	$T_J=25^{\circ}C$	5.0	μA
	$T_J=125^{\circ}C$	50	
Maximum reverse recovery time(Note1)	T_{rr}	30	ns
Typical junction capacitance(Note2)	C_j	50	pF
Operating junction and storage temperature range	T_J T_{STG}	-55 to +150	$^{\circ}C$

Note: 1. Test conditions: $I_F=0.5A, I_R=1.0A, I_{RR}=0.25A$.

2. Measured at 1MHz and applied reverse voltage of 4.0 Volts.

FIG.1-TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC

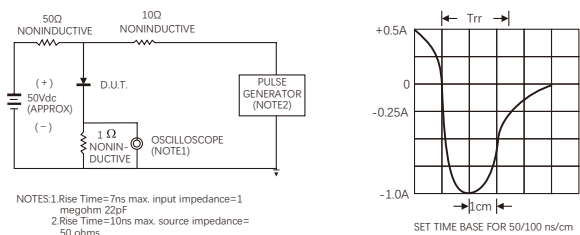


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

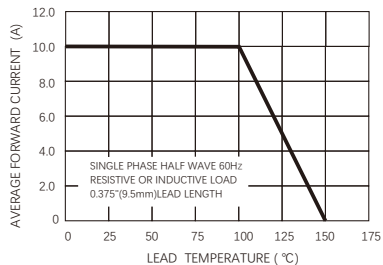


FIG.3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

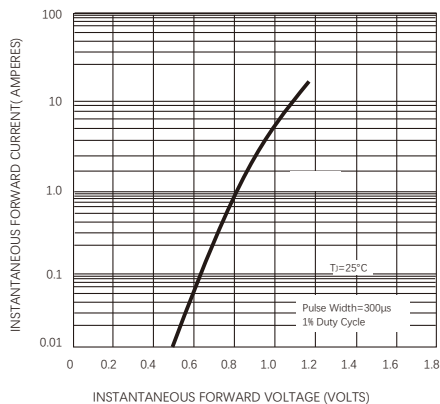


FIG.4-TYPICAL REVERSE CHARACTERISTICS

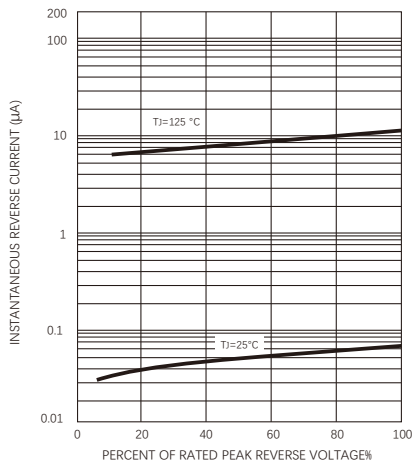


FIG.5-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

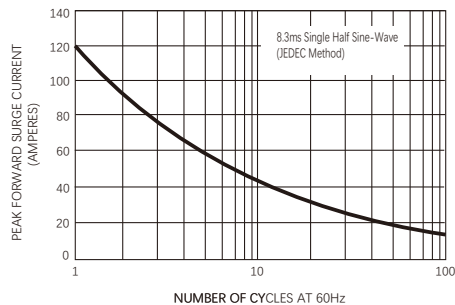
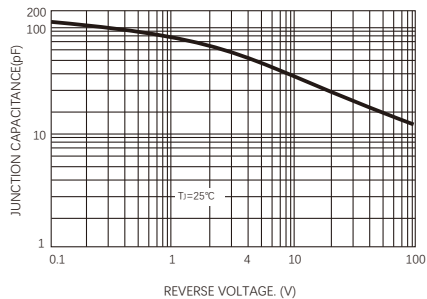


FIG.6-TYPICAL JUNCTION CAPACITANCE



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