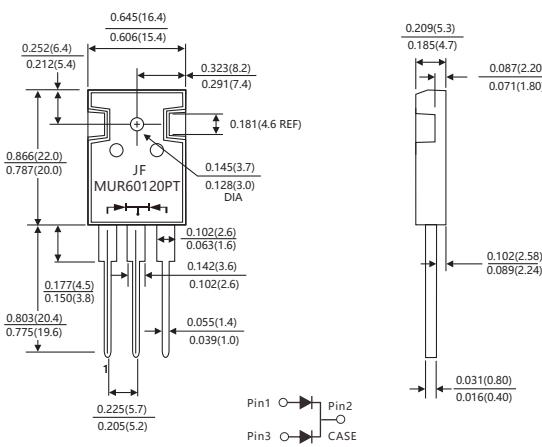


FEATURES

- Fred Chip Planar Construction
- SuperFast Switching, High Efficiency
- Low Power loss, High Efficiency
- Low Reverse Leakage Current
- High Surge Current Capability
- Plastic Material has UL Flammability Classification 94V-O

TO-247AB

unit:mm



MECHANICAL DATA

- Case: TO-247AB, Molded Plas
- Terminals: Pure tin Plated, Lead free Solderable per MIL-STD-750, Method 2026
- Polarity: As marked
- Weight: 6.4 grams(approx)
- Mounting Position: Any

Maximum Ratings and Electrical Characteristics @ $T_A=25^\circ\text{C}$ unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

| Characteristic | Symbol | MUR60120PT | | Unit |
|---|-----------------|-------------------------|-------------|------|
| Maximum Recurrent Peak Reverse Voltage | V_{RRM} | 1200 | | V |
| Maximum RMS Voltage | V_{RMS} | 840 | | V |
| Maximum DC Blocking Voltage | V_{DC} | 1200 | | V |
| Maximum Average Forward (See Figure 1) | $I_{F(AV)}$ | 60 | | A |
| Peak Forward Surge Current : 8.3ms single half sine-wave superimposed on rated load(JEDEC method) | I_{FSM} | 300 | | A |
| Maximum Forward Voltage at 30A per leg | V_F | Typ. | Max. | V |
| | | 2 | 2.4 | |
| Maximum Reverse Recovery Time (Measured With IF=0.5A, IR=1.0A, IRR=0.25A) | T_{rr} | Typ. | Max. | nS |
| | | 55 | 75 | |
| Maximum DC Reverse Current at Rated DC Blocking Voltag | I_R | TA = 25°C TA = 125°C | 5 50 | uA |
| Typical Thermal Resistance Junction to case | $R_{\theta JC}$ | | 1.5 | °C/W |
| Typical Thermal Resistance Junction to Ambient | $R_{\theta JA}$ | | 45 | °C/W |
| Operating Junction and Storage Temperature Range | T_J, T_{STG} | | -55 to +150 | °C |

MUR60120PT

RATING AND CHARACTERISTIC CURVES

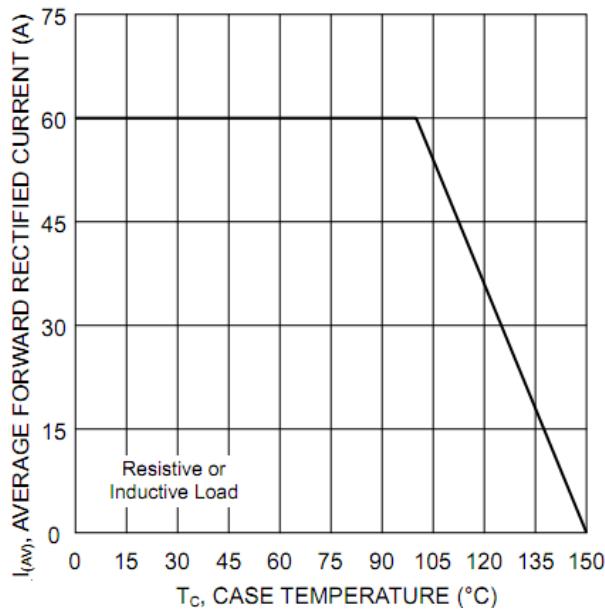


Fig-1
FORWARD CURRENT DERATING CURVE

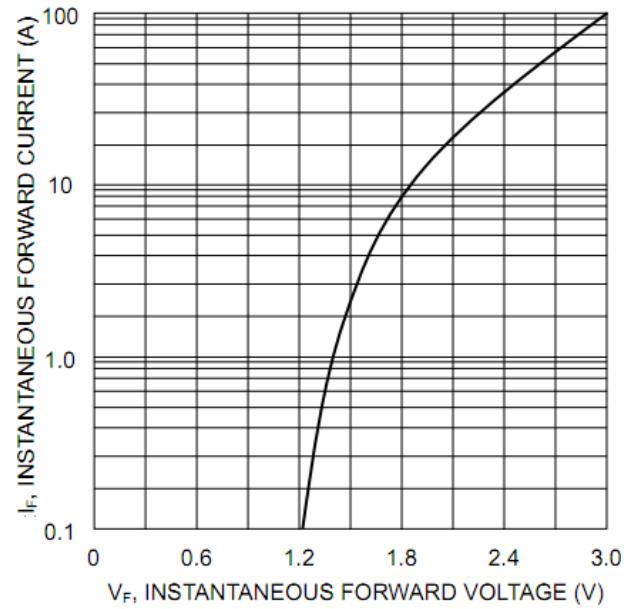


Fig-2
TYPICAL INSTANTANEOUS FORWARD CHARACTERISTIC

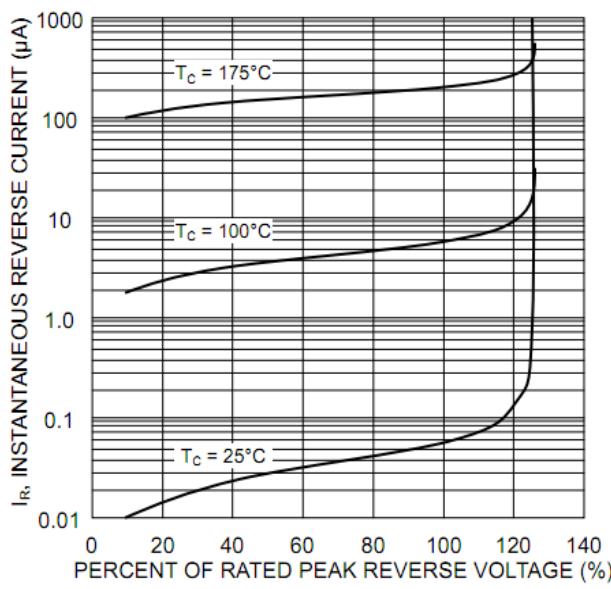


Fig-3
TYPICAL REVERSE CHARACTERISTICS

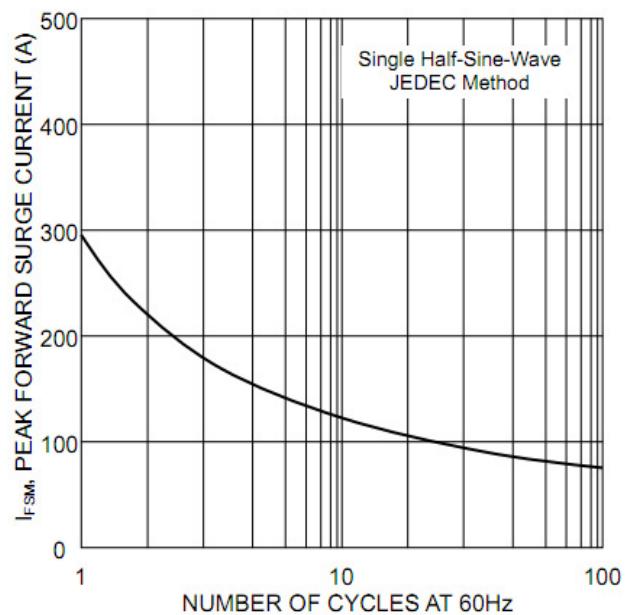


Fig-4
MAXIMUM NON-REPETITIVE SURGE CURRENT