

# 7N65 7N65F 7N65D 7N65E 7N65M 7N65N

## 650V N-Channel Power MOSFET

### FEATURES

- $R_{DS(ON)} < 1.5\Omega$  @  $V_{GS} = 10V$
- Fast switching capability
- Low gate charge
- Lead free in compliance with EU RoHS directive.
- Green molding compound

### MECHANICAL DATA

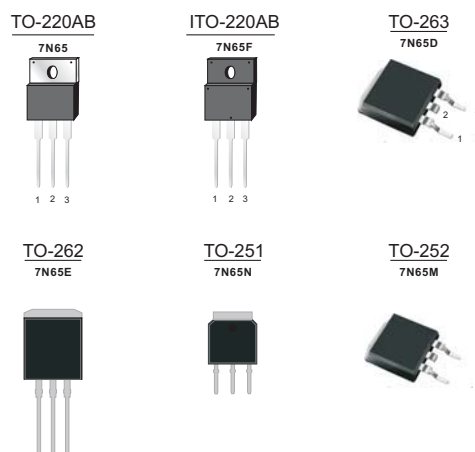
- Case: TO-220, ITO-220, TO-263, TO-262 Package  
TO-251, TO-252 Package

### Ordering Information

Part No.	Package	Packing
7N65-TU	TO-220	50pcs / Tube
7N65F-TU	ITO-220	50pcs / Tube
7N65E-TU	TO-262	50pcs / Tube
7N65D-TU	TO-263	50pcs / Tube
7N65D-TR	TO-263	800pcs / 13"Reel
7N65N-TU	TO-251	75pcs / Tube
7N65M-TU	TO-252	75pcs / Tube
7N65M-TR	TO-252	2.5Kpcs / 13"Reel

### PRODUCT SUMMARY

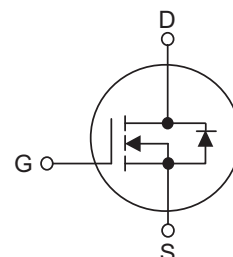
$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
650	1.5 @ $V_{GS} = 10V$	7



### Block Diagram

Pin Definition:

1. Gate
2. Drain
3. Source



### ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub>=25 C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	$V_{DSS}$	650	V
Gate-Source Voltage	$V_{GSS}$	±30	V
Continuous Drain Current	$I_D$	7	A
Pulsed Drain Current (Note 2)	$I_{DM}$	28	A
Avalanche Energy	$E_{AS}$	435	mJ
Power Dissipation	TO-220/TO-263/TO-262	142	W
	TO-251/TO-252	32	
	ITO-220	48	
Junction Temperature	$T_J$	+150	C
Storage Temperature	$T_{STG}$	-55 ~ +150	C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by  $T_J$

3. L = 30mH,  $I_{AS} = 5.25A$ ,  $V_{DD} = 50V$ ,  $R_G = 25 \Omega$ , Starting  $T_J = 25 C$

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### THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/ITO-220 TO-263/TO-262	$R_{\theta JA}$	62.5	C/W
	TO-251/TO-252		110	
Junction to Case	TO-220/TO-263/TO-262	$R_{\theta JC}$	2.35	C/W
	TO-251/TO-252		2.9	
	ITO-220		5.5	

### ELECTRICAL CHARACTERISTICS (T<sub>C</sub>=25 C, unless otherwise specified)

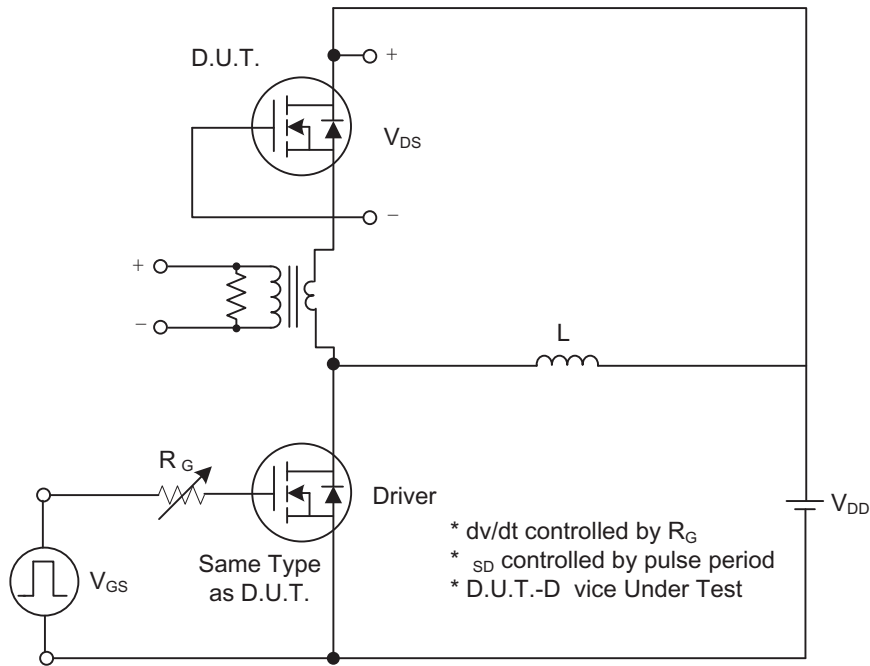
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>							
Drain-Source Breakdown Voltage		$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	650			V
Drain-Source Leakage Current		$I_{DSS}$	$V_{DS}=650V, V_{GS}=0V$			1	$\mu A$
Gate- Source Leakage Current	Forward	$I_{GSS}$	$V_G=30V, V_{DS}=0V$			100	nA
	Reverse		$V_{GS}=-30V, V_{DS}=0V$			-100	nA
Breakdown Voltage Temperature Coefficient		$\Delta BV_{DSS} / \Delta T_J$	$I_D=250\mu A$ , Referenced to 25°C		0.67		V/ C
<b>ON CHARACTERISTICS</b>							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	$V_{GS}=10V, I_D=3.5A$		1.35	1.5	$\Omega$
<b>DYNAMIC CHARACTERISTICS</b>							
Input Capacitance		$C_{ISS}$	$V_{DS}=25V, V_{GS}=0V, f=1MHz$		1210	1400	pF
Output Capacitance		$C_{OSS}$			140	180	pF
Reverse Transfer Capacitance		$C_{RSS}$			40	50	pF
<b>SWITCHING CHARACTERISTICS</b>							
Turn-On Delay Time		$t_{D(ON)}$	$V_{DD}=300V, I_D=7A,$ $R_G=25\Omega$ (Note 1, 2)		50	70	ns
Turn-On Rise Time		$t_R$			150	180	ns
Turn-Off Delay Time		$t_{D(OFF)}$			380	410	ns
Turn-Off Fall Time		$t_F$			180	220	ns
Total Gate Charge		$Q_G$	$V_{DS}=520V, I_D=7A,$ $V_{GS}=10V$ (Note 1, 2)		29	38	nC
Gate-Source Charge		$Q_{GS}$			9		nC
Gate-Drain Charge		$Q_{GD}$			19		nC
<b>DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS</b>							
Drain-Source Diode Forward Voltage		$V_{SD}$	$V_{GS}=0V, I_S=7A$			1.4	V
Maximum Continuous Drain-Source Diode Forward Current		$I_S$				7	A
Maximum Pulsed Drain-Source Diode Forward Current		$I_{SM}$				28	A
Reverse Recovery Time		$t_{rr}$	$V_{GS}=0V, I_S=7A$		490		ns
Reverse Recovery Charge		$Q_{RR}$	$di/dt=100A/\mu s$ (Note 1)		3.2		$\mu C$

- Notes: 1. Pulse Test: Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$ .  
2. Essentially independent of operating temperature.

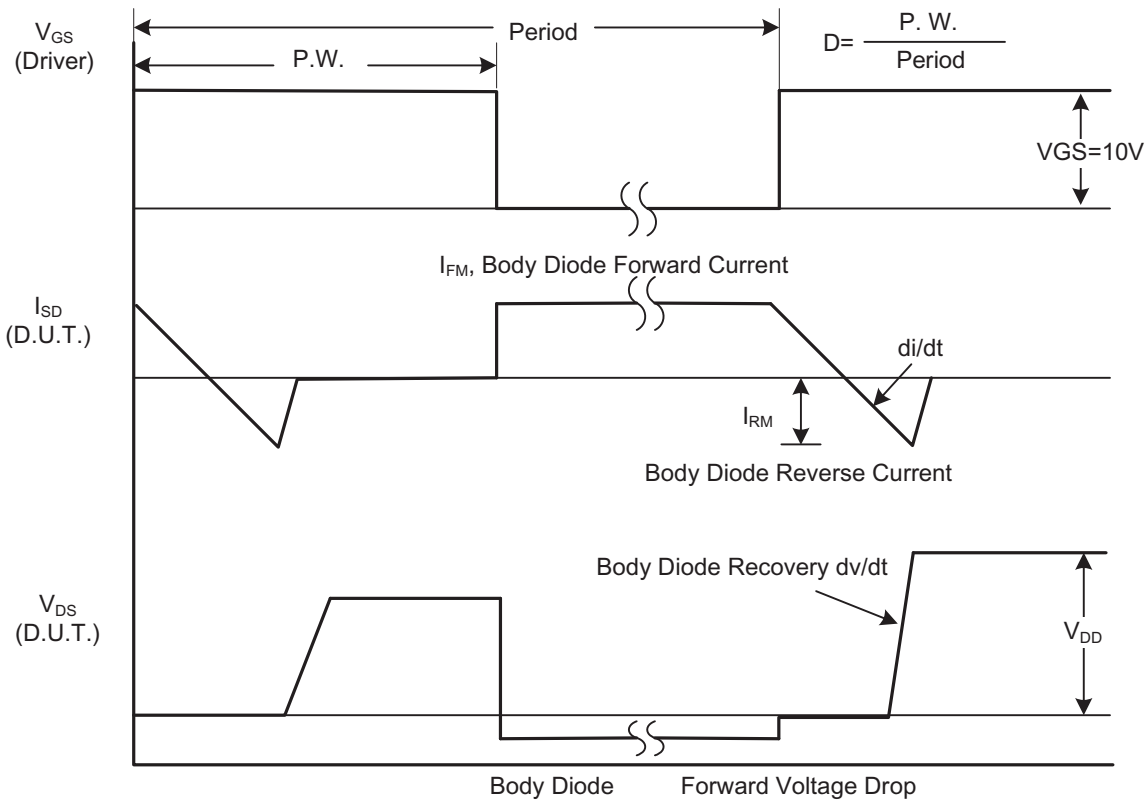
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## 650V N-Channel Power MOSFET

### TEST CIRCUITS AND WAVEFORMS



Peak Diode Recovery dv/dt Test Circuit

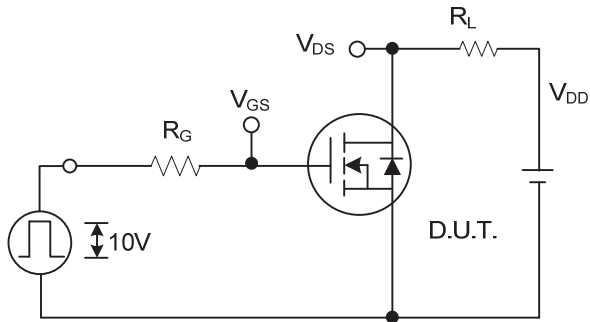


Peak Diode Recovery dv/dt Waveforms

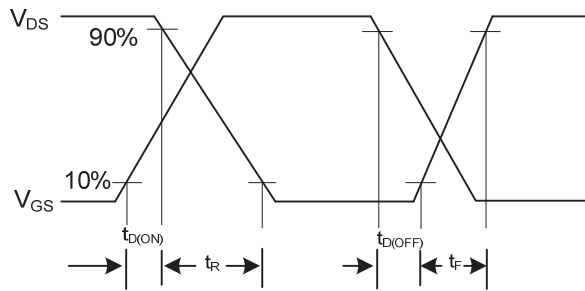
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## 650V N-Channel Power MOSFET

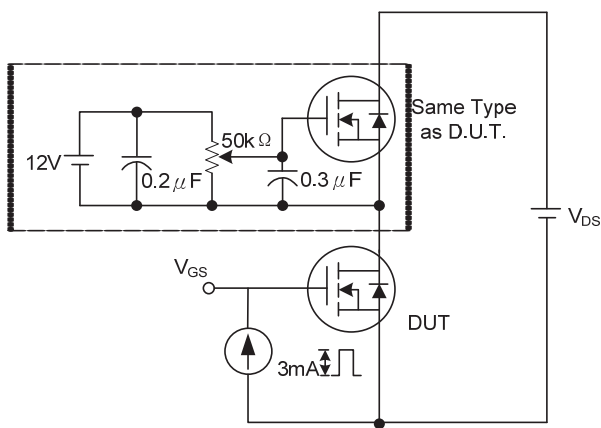
TEST CIRCUITS AND WAVEFORMS(Cont.)



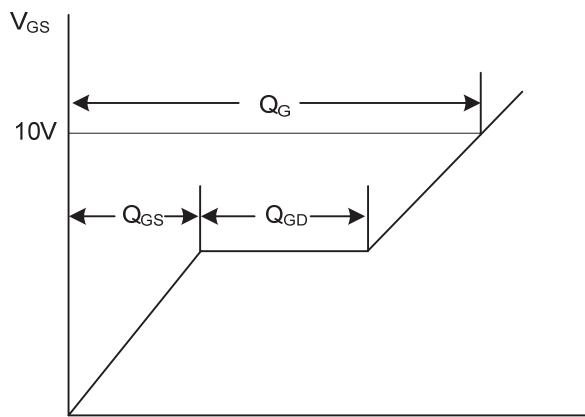
Switching Test Circuit



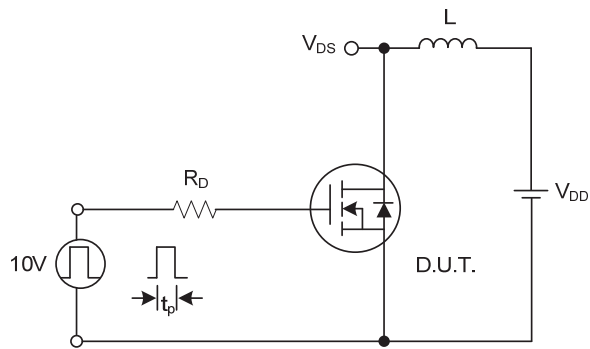
Switching Waveforms



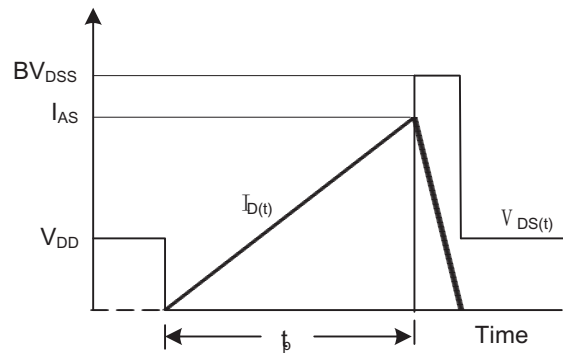
Gate Charge Test Circuit



Charge Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



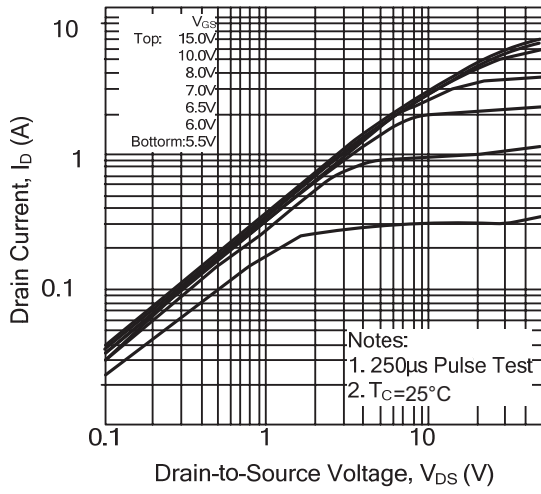
Unclamped Inductive Switching Waveforms

# 7N65 7N65F 7N65D 7N65E 7N65M 7N65N

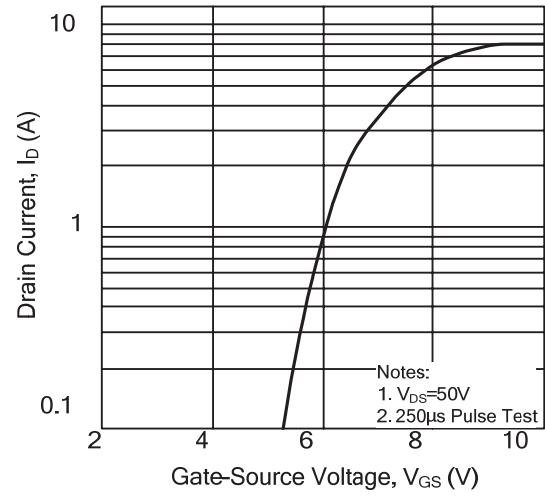
## 650V N-Channel Power MOSFET

### TYPICAL CHARACTERISTICS

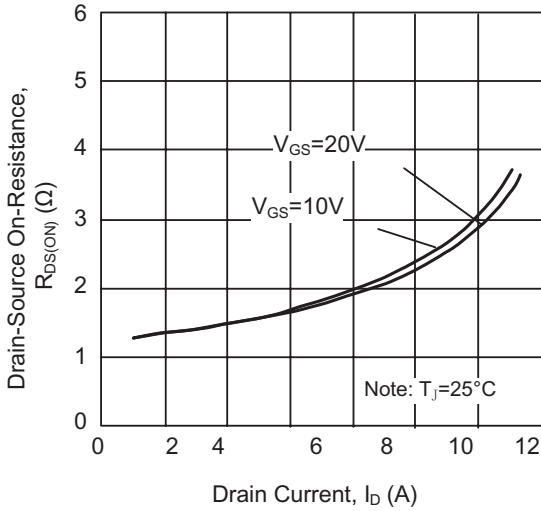
On-State Characteristics



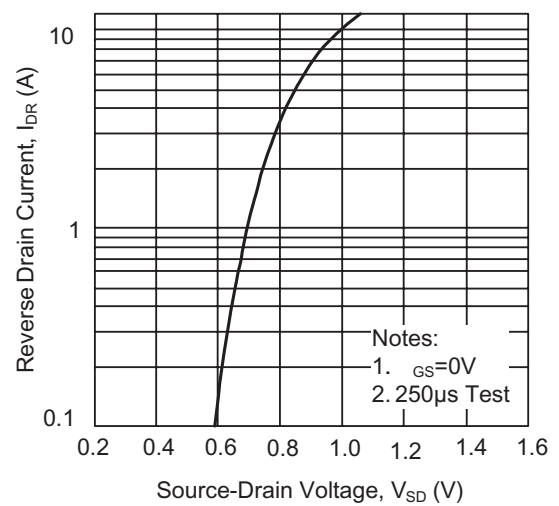
Transfer Characteristics



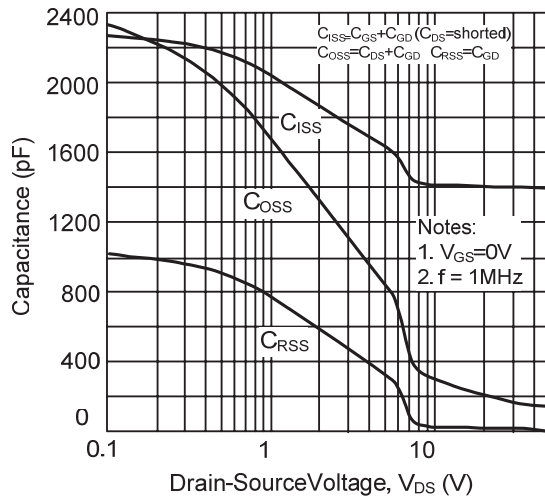
On-Resistance Variation vs. Drain Current and Gate Voltage



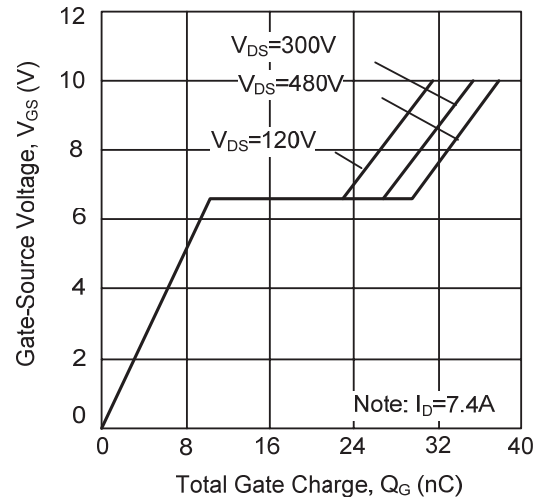
On State Current vs. Allowable Case Temperature



Capacitance Characteristics (Non-Repetitive)



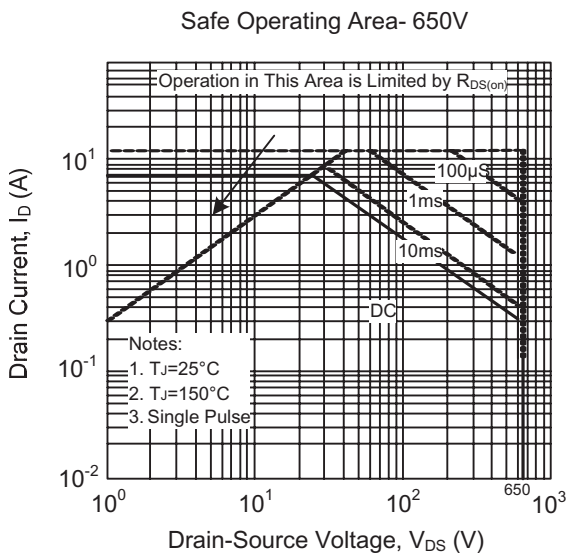
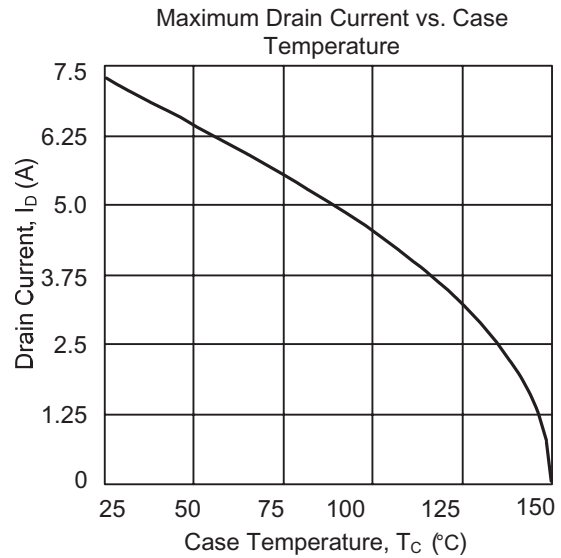
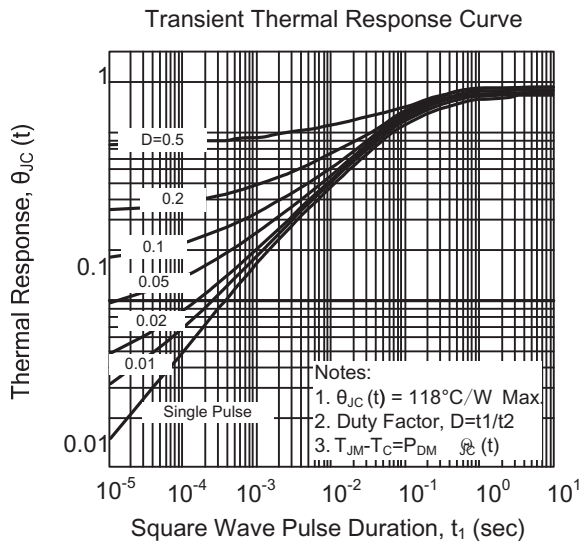
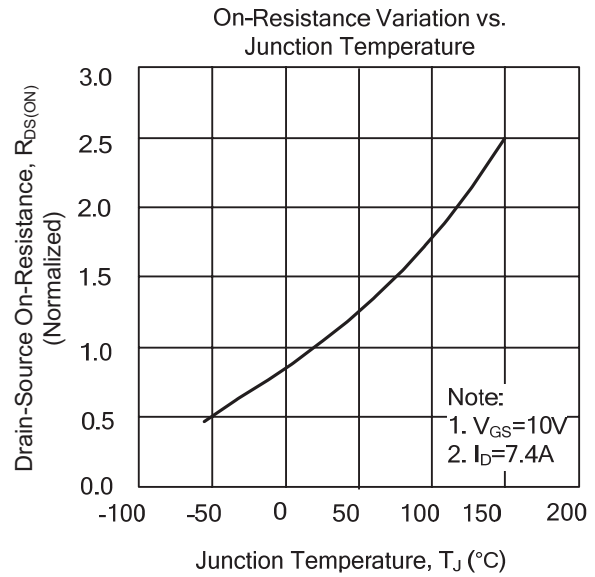
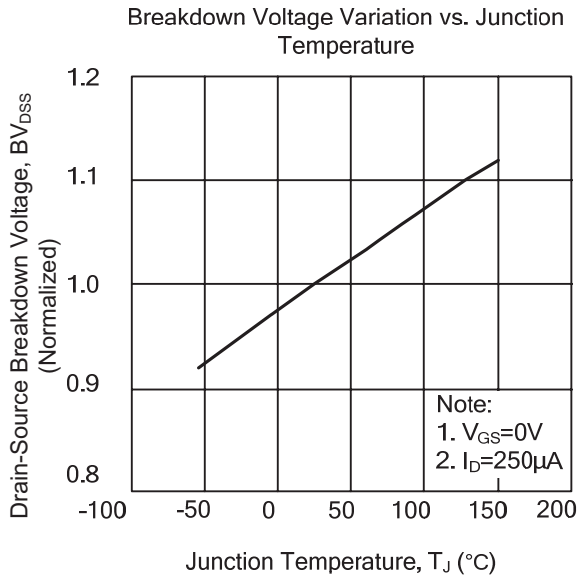
Gate Charge Characteristics



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## 650V N-Channel Power MOSFET

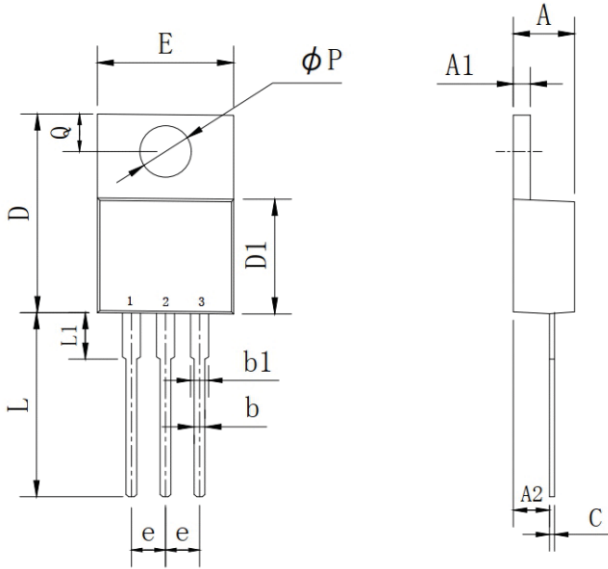
### TYPICAL CHARACTERISTICS



# 7N65 7N65F 7N65D 7N65E 7N65M 7N65N

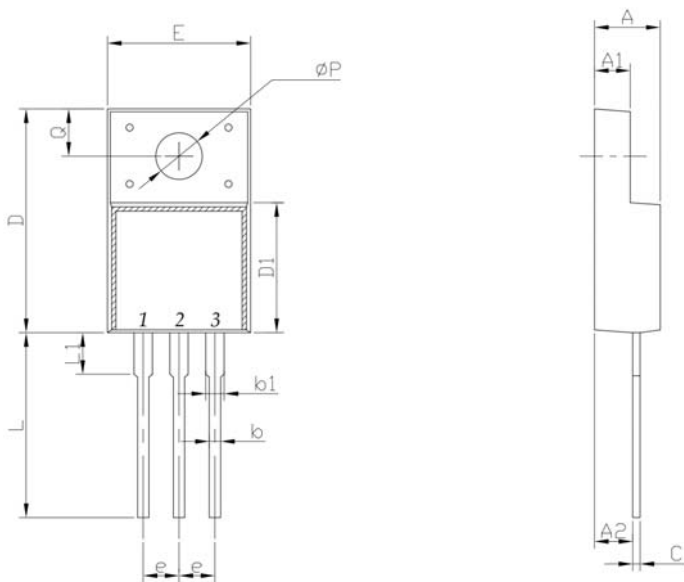
## 650V N-Channel Power MOSFET

### TO-220 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	4.25	4.87	0.167	0.192
A1	1.07	1.47	0.042	0.058
A2	2.03	2.92	0.080	0.115
b	0.51	1.11	0.020	0.044
b1	0.97	1.6	0.038	0.063
C	0.3	0.7	0.012	0.028
D	14.6	15.9	0.575	0.626
D1	8.04	9.3	0.317	0.366
E	9.57	10.57	0.377	0.416
e	2.34	2.74	0.092	0.108
L	12.58	14.3	0.495	0.563
L1	2.8	4.2	0.110	0.165
P	3.4	4.14	0.134	0.163
Q	2.45	3	0.096	0.118

### ITO-220 PACKAGE OUTLINE DIMENSIONS

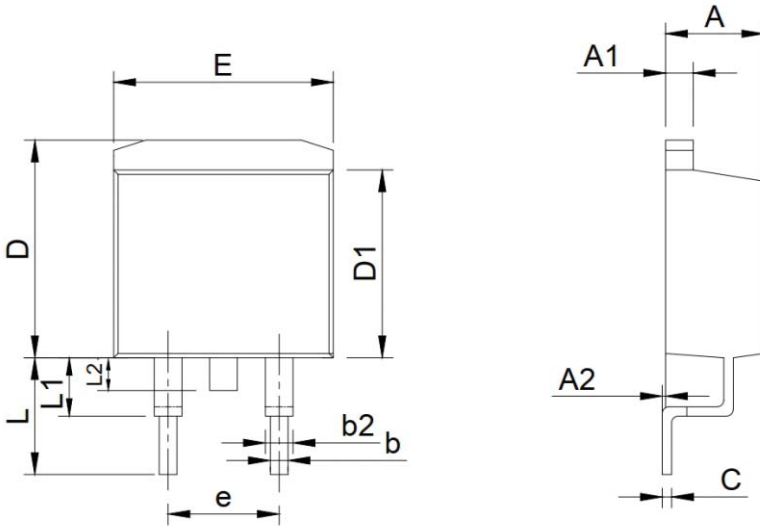


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	4.24	4.9	0.167	0.193
A1	2.3	2.92	0.091	0.115
A2	2.61	2.81	0.103	0.111
b	0.3	1	0.012	0.039
b1	0.9	1.55	0.035	0.061
C	0.3	0.7	0.012	0.028
D	14.5	16.36	0.571	0.644
D1	8.8	9.41	0.346	0.370
E	9.5	10.5	0.374	0.413
e	2.3	2.75	0.091	0.108
L	12.6	14	0.496	0.551
L1	2.45	4.3	0.096	0.169
P	2.9	3.8	0.114	0.150
Q	2.5	3.55	0.098	0.140

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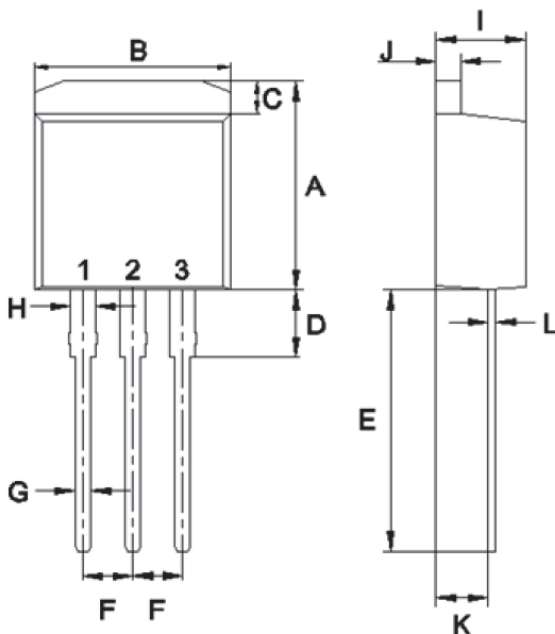
## 650V N-Channel Power MOSFET

### TO-263 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	4.25	4.87	0.167	0.192
A1	1.07	1.47	0.042	0.058
A2	0	0.25	0.000	0.010
b	0.61	1.01	0.024	0.040
b1	1.2	1.34	0.047	0.053
C	0.3	0.6	0.012	0.024
D	9.48	10.84	0.373	0.427
D1	8.49	9.3	0.334	0.366
E	9.7	10.31	0.382	0.406
e	4.88	5.28	0.192	0.208
L	4.46	5.85	0.176	0.230
L1	1.33	2.33	0.052	0.092
L2	0	2.2	0.000	0.087

### TO-262 PACKAGE OUTLINE DIMENSIONS



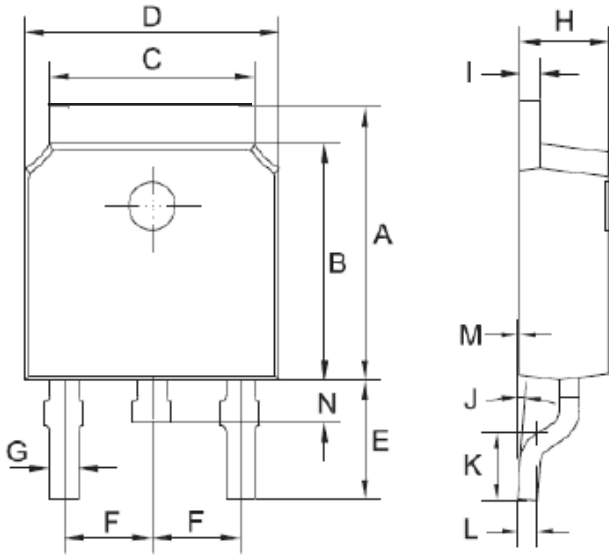
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	10.14	11.14	0.399	0.439
B	9.57	10.57	0.377	0.416
C	1.15	1.84	0.045	0.072
D	2.95	3.95	0.116	0.156
E	12.25	13.75	0.482	0.541
F	2.34	2.74	0.092	0.108
G	0.51	1.11	0.020	0.044
H	0.97	1.57	0.038	0.062
I	4.25	4.87	0.167	0.192
J	1.07	1.47	0.042	0.058
K	2.03	2.92	0.080	0.115
L	0.3	0.6	0.012	0.024



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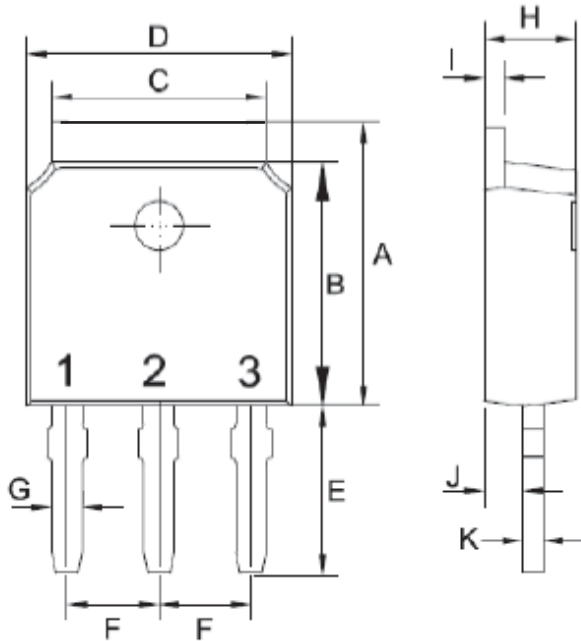
## 650V N-Channel Power MOSFET

### TO-252 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	6.85	7.25	0.270	0.285
B	5.8	6.3	0.228	0.248
C	5	5.53	0.197	0.218
D	6.3	6.8	0.248	0.268
E	2.6	3.3	0.102	0.130
F	2.19	2.39	0.086	0.094
G	0.45	0.85	0.018	0.033
H	2.2	2.4	0.087	0.094
I	0.41	0.61	0.016	0.024
J	0°	8°	0°	8°
K	1.45	1.85	0.057	0.073
L	0.41	0.61	0.016	0.024
M	0	0.12	0.000	0.005
P	0.6	1	0.024	0.039

### TO-251 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	6.85	7.25	0.270	0.285
B	5.8	6.3	0.228	0.248
C	5	5.53	0.197	0.218
D	6.3	6.8	0.248	0.268
E	3.5	4.35	0.138	0.171
F	2.19	2.39	0.086	0.094
G	0.45	0.85	0.018	0.033
H	2.2	2.4	0.087	0.094
I	0.41	0.61	0.016	0.024
J	0.71	1.31	0.028	0.052
K	0.41	0.61	0.016	0.024