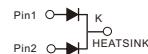


## FEATURES

- Power pack
- Metal silicon junction ,majority carrier conduction
- Guard ring for overvoltage protection
- Low power loss ,high efficiency
- High current capability ,low forward voltage drop
- High forward surge capability
- High frequency operation
- Meets MSL Level 1, per J-STD-020, LF MAX peak of 245°C (for TO-263 package)
- Solder bath temperature 275°C maximum, 10s, per JESD22-B106 (for TO-220AB and ITO-220AB package)
- Component in accordance to RoHS 2011/65/EU



## MECHANICAL DATA

- Case: JEDEC TO-220AB、ITO-220AB、TO-263
- Molding compound meets UL94V-0 flammability rating
- Terminals: Lead solderable per J-STD-002 and JESD22-B102
- Polarity: As marked
- Mounting Torque: 10 in-lbs maximum

## TYPICAL APPLICATIONS

For use in low voltage ,high frequency inverters ,DC/DC converters, free wheeling ,and polarity protection applications

## MAXIMUM RATINGS

(Ratings at 25°C ambient temperature unless otherwise specified )

PRIMARY CHARACTERISTICS	
I <sub>F</sub> (AV)	2×10A
V <sub>RRM</sub>	120V
I <sub>FSM</sub>	250A
V <sub>F</sub> at I <sub>F</sub> =10.0A,Per leg,25°C	0.70V
I <sub>R</sub>	11 μA
T <sub>J</sub> (MAX)	150°C
Package	TO-220AB, ITO-220AB, TO-263
Diode variations	Common cathode

Parameter	Symbol	SR20120SLCT, SRF20120SLCT, SR20120SLD1	Unit
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	120	V
Maximum average forward rectified current (see fig.1)	Per leg	I <sub>F</sub> (AV)	A
Total device			
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC method at rated T <sub>L</sub> )	I <sub>FSM</sub>	250	A
Peak repetitive reverse current per diode at t <sub>p</sub> =2 μ s 1KHz	I <sub>RRM</sub>	0.5	A
Operating junction and Storage temperature range	T <sub>J</sub> ,T <sub>Stg</sub>	-55 to+150	°C
Isolation voltage (ITO-220AB only) from terminals to heatsink t=1 min	V <sub>AC</sub>	1500	V

## RATINGS AND CHARACTERISTIC OF SR20120SLCT,SRF20120SLCT,SR20120SLD1

### ELECTRICAL CHARACTERISTICS ( $T_A=25^\circ\text{C}$ Unless otherwise noted)

Parameter	Test Conditions		Symbol	TYP.	MAX.	Unit
Instantaneous forward voltage	Per leg $I_F=10.0\text{A}$	$T_A=25^\circ\text{C}$	$V_F$ <sup>1)</sup>	0.70	0.75	V
		$T_A=100^\circ\text{C}$		0.63	—	
		$T_A=125^\circ\text{C}$		0.59	—	
		$T_A=25^\circ\text{C}$		0.55	—	
		$T_A=100^\circ\text{C}$		0.52	—	
	Per leg $I_F=5.0\text{A}$	$T_A=125^\circ\text{C}$		0.50	—	
		$T_A=25^\circ\text{C}$		11	50	$\mu\text{ A}$
		$T_A=100^\circ\text{C}$		2.5	—	mA
		$T_A=125^\circ\text{C}$		10	—	
Typical junction capacitance	4V, 1MHz		$C_J$	570		pF

Notes: 1.Pulse test: 300  $\mu\text{s}$  pulse width,1% duty cycle

2.Pulse test: pulse width  $\leqslant 40\text{ms}$

### THERMAL CHARACTERISTICS ( $T_A=25^\circ\text{C}$ Unless otherwise noted)

Parameter	Symbol	SR20120SLCT	SRF20120SLCT	SR20120SLD1	Unit
Typical thermal resistance <sup>3)</sup>	$R_{\theta JC}$	2.5	4.5	2.5	$^\circ\text{C}/\text{W}$

3.Thermal resistance from junction to case

### AVAILABALE PACK INFORMATION

Product code	Pack	Box Size L×W×H(mm)	Quantity(pcs/box)	Carton SizeL×W×H(mm)	Quantity(box/carton)
SR20120SLCT-TO-220AB	P/T	558×148×38	1000	565×225×170	5
SRF20120SLCT-ITO-220AB	P/T	558×148×38	1000	565×225×170	5
SR20120SLD1-TO-263	P/T	558×148×38	1000	565×225×170	5

# RATINGS AND CHARACTERISTIC OF SR20120SLCT, SRF20120SLCT, SR20120SLD1

FIG.1-FORWARD CURRENT DERATING CURVE

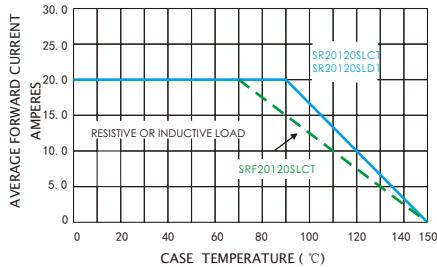


FIG.3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

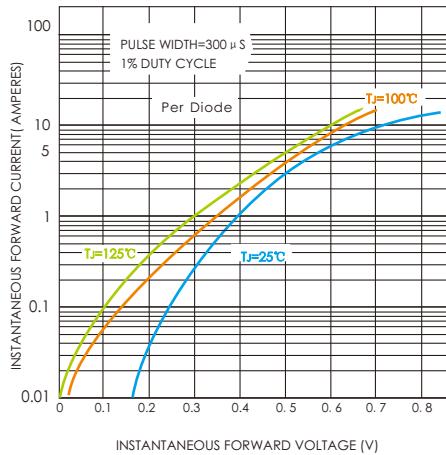


FIG.5-TYPICAL JUNCTION CAPACITANCE

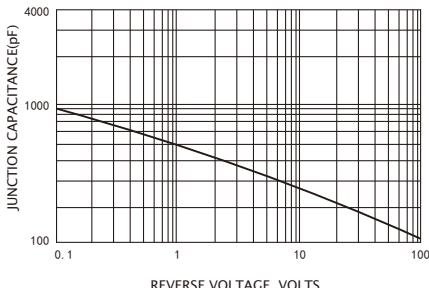


FIG.2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

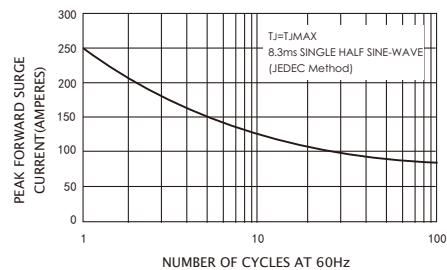
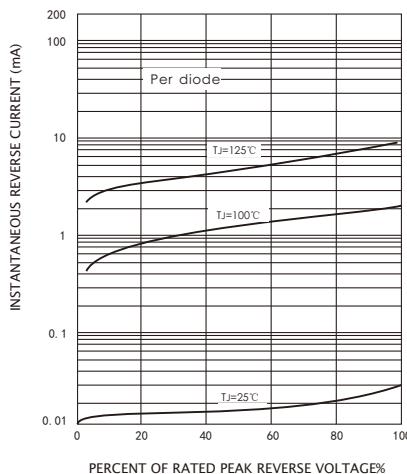
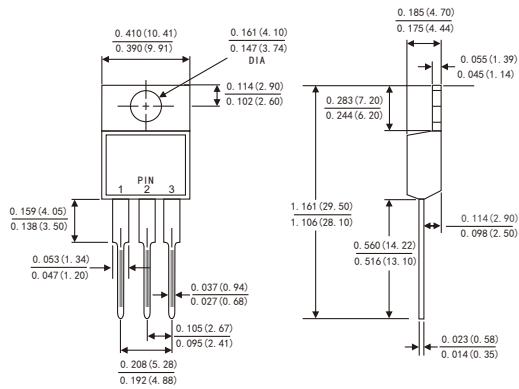


FIG.4-TYPICAL REVERSE CHARACTERISTICS

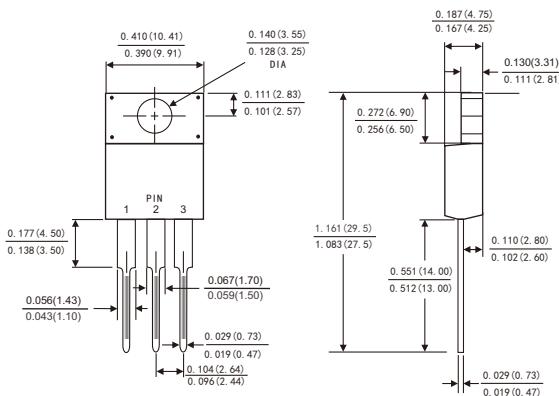


# PACKAGE OUTLINE DIMENSIONS

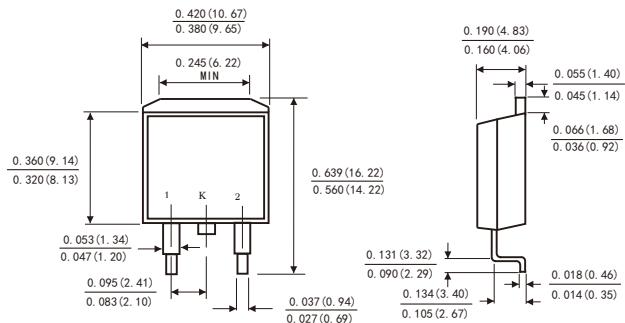
**TO-220AB**



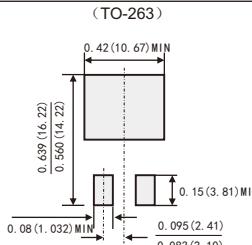
**ITO-220AB**



**TO-263**



**Suggested Pad Layout**



Dimensions in inches and (millimeters)